Lower Bound on the Number of Toffoli Gates in a Classical Reversible Circuit through Quantum Information Concepts

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The question of finding a lower bound on the number of Toffoli gates in a classical reversible circuit is addressed. A method based on quantum information concepts is proposed. The method involves solely *concepts* from quantum information—there is no need for an actual physical quantum computer. The method is illustrated in the example of classical Shannon data compression.

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In the past ten years we have witnessed the birth and explosive growth of the field of quantum information and computation. The main thrust of this new field is to study how quantum systems (such as quantum computers and quantum communication devices) can be used to solve certain mathematical problems or to improve communication capabilities. The crucial feature of this approach is that although the quantum systems themselves can be studied with pen and paper, gains are obtained only when the quantum systems are actually used in practice. The gains are due to new physical behavior unique to quantum systems and not shared by classical ones. In this Letter we take a different direction. We are not interested in using quantum systems; rather, we want to use the concepts and insights gained in the study of quantum information for solving mathematical problems.

The problem we consider here concerns lower bounds on reversible classical circuits. Although reversible classical computation will probably not be realized in the near future (though increasing attention is being devoted to this issue), its study has yielded profound insights into the theory of complexity and into thermodynamics; see [1] for a review.

A reversible classical computation evaluates a function f which takes n-bit input $\bar{x} \in \{0, 1\}^n$ to n-bit output $f(\bar{x}) \in \{0, 1\}^n$. Each particular input has its own unique output, thus f is invertible. A classical circuit that evaluates f can be reduced to a sequence of elementary reversible logical gates. Examples of reversible one-, two-, and three-bit gates are NOT, Controlled-NOT (C-NOT) and Toffoli (Controlled-Controlled-NOT) gates. C-NOT applies NOT on the second bit only if the value of a first bit is 1; Toffoli applies NOT on the third bit only if the values of both first and second bit are 1. Reversible one- and two-bit gates do not constitute a universal set of gates. The Toffoli gate, however, is a universal basic gate for reversible classical computation; i.e., any reversible classical circuit can be built up from Toffoli gates [2].

Although we can build any reversible circuit out of Toffoli gates alone, an interesting conceptual question is to find the *minimal* number of Toffoli gates required if one allows for *any* number of one- and two-bit gates. The problem is interesting because Toffoli gates are, in a sense, the strongest reversible gates, and the minimal number needed tells us about the complexity of the computation itself. Furthermore, Toffoli gates require *physical interaction* between 3 bits, and are therefore more difficult to implement in practice than one- and two-bit gates, and it might be useful to minimize their use.

We formulate the problem as follows: given a reversible function $f(\bar{x})$, what is the minimum number of Toffoli gates needed to construct a circuit that will evaluate $f(\bar{x})$ for (i) every \bar{x} or (ii) a certain subset of \bar{x} ?

As far as we know, a systematic approach to this problem does not exist. In this Letter we use quantum information concepts to address it. In quantum information (computation) one classical bit can be encoded in two orthogonal states of a quantum system. The main idea of our method is to *map* the bits onto some special quantum states, and the action of the logic gates onto unitary transformations acting on these states. Then, the study of the properties of the unitary transformation that is associated with the classical reversible computation will give information about the classical circuit. The map is

$$0 \to |\mathbf{0}\rangle = \frac{1}{\sqrt{2}}(|0\rangle_A|0\rangle_B + |1\rangle_A|1\rangle_B),$$

$$1 \to |\mathbf{1}\rangle = \frac{1}{\sqrt{2}}(|0\rangle_A|0\rangle_B - |1\rangle_A|1\rangle_B).$$
(1)

The states $|\mathbf{0}\rangle$ and $|\mathbf{1}\rangle$, the "logical" qubits into which the classical bits are mapped, represent entangled states of two "constituent" qubits, denoted by the indexes A and B. (Throughout this Letter we will use boldfaced fonts for the logical qubits and normal fonts for the constituent qubits.) Here the states $|0\rangle$, $|1\rangle$ are associated with orthogonal states of a two-level quantum system.

A string of n bits is mapped on the associated quantum state of *n* qubit pairs: $x_1x_2...x_n \rightarrow |\mathbf{x_1}\rangle |\mathbf{x_2}\rangle...|\mathbf{x_n}\rangle$. Any computation $x_1x_2...x_n \rightarrow f_1(\bar{x})f_2(\bar{x})...f_n(\bar{x})$ is mapped on the same transformation of the corresponding quantum states $|\mathbf{x}_1\rangle|\mathbf{x}_2\rangle\dots|\mathbf{x}_n\rangle\rightarrow|\mathbf{f}_1(\bar{\mathbf{x}})\rangle|\mathbf{f}_2(\bar{\mathbf{x}})\rangle\dots|\mathbf{f}_n(\bar{\mathbf{x}})\rangle$. Since we consider a reversible classical computation, the corresponding quantum transformation is unitary. For example, the action of a C-NOT gate $a, b \rightarrow a, a \oplus b$ $(a, b \in \{0, 1\})$ is mapped on the unitary transformation $U_{\text{C-NOT}}$: $|\mathbf{a}\rangle|\mathbf{b}\rangle \rightarrow$ $|\mathbf{a}\rangle|\mathbf{a}\oplus\mathbf{b}\rangle$. [Note that this does not define the unitary $U_{\text{C-NOT}}$ completely as the two logical qubits in Eq. (1) do not constitute a basis of the Hilbert space of the two constituent qubits. Similarly the unitary quantum transformation corresponding to any computation acting on n bits is not specified completely by the above mapping. Finally note that if we only specify a partial truth table for the computation, then the unitary is even less specified.]

The most important property of this mapping is that any classical reversible circuit built only from one- and two-bit gates is mapped onto a transformation that requires neither entanglement nor classical communication, i.e., onto a *local* unitary transformation $U = U^A \otimes U^B$ where $U^A(U^B)$ acts only on the A(B) constituent qubits. For example, the $U_{\text{C-NOT}}$ gate can be built from *local* C-NOT gates:

$$U_{\text{C-NOT}} = \tilde{U}_{\text{C-NOT}}^A \otimes \tilde{U}_{\text{C-NOT}}^B \tag{2}$$

as can be easily checked explicitly. Here $\tilde{U}_{\text{C-NOT}}^A$ ($\tilde{U}_{\text{C-NOT}}^B$) is a C-NOT gate acting on the A(B) constituent qubits in the opposite direction to normal, i.e., with the second bit as the control and the first bit as the target. [Such bi-lateral transformations were considered in [3] for the purpose of density matrix purification.] That the quantum equivalent of any reversible one- or two-bit gates can be constructed by a similar local bi-lateral transformation can be verified explicitly. Hence, any circuit built from one- or two-qubit gates is local.

We can use this property of our mapping to analyze general circuits: given a classical reversible computation we construct the associated quantum unitary transformation U; if U is nonlocal, the corresponding classical transformation cannot be constructed solely by two-bit gates. Furthermore, the amount of nonlocality in U gives a lower bound on the number of Toffoli gates we need.

We define nonlocality of production, $E_U^{\rm Pr}$, as the maximum amount of entanglement (measured in ebits) which U is capable to produce. We define nonlocality of implementation, $E_U^{\rm Im}$, as the minimum amount of entanglement which allows one to implement U using local operations and classical communication (LOCC). These quantities satisfy

$$E_U^{\text{Pr}} \le E_U^{\text{Im}}.\tag{3}$$

We denote by $E_T^{\rm Pr}$ and $E_T^{\rm Im}$ the appropriate quantities for quantum Toffoli gate U_T (we estimate $E_T^{\rm Pr}$, $E_T^{\rm Im}$ below).

One possible implementation of U is to realize the classical circuit using the nonlocal quantum Toffoli gates

(which cost $E_T^{\rm Im}$ ebits each) and the local two-bit and one-bit gates. Hence U can be implemented using $N_T E_T^{\rm Im}$ ebits. This yields the lower bound on N_T :

$$N_T \ge E_U^{\text{Im}} / E_T^{\text{Im}} \ge E_U^{\text{Pr}} / E_T^{\text{Im}}. \tag{4}$$

We now arrive at the crucial point of the method. To determine $E_U^{\rm Pr}$, $E_U^{\rm Im}$ may be a very complicated task—it might actually be as complicated as directly determining the required number of Toffoli gates. On the other hand, it is easy to obtain bounds on them.

To obtain lower bound on E_U^{Pr} we apply U on a *test state* $|\Psi_{::}^{\text{test}}\rangle$:

$$U|\Psi_{\rm in}^{\rm test}\rangle = |\Psi_{\rm out}^{\rm test}\rangle,$$
 (5)

where the test state can be any arbitrary superposition of basic input states $|\mathbf{x_1}\rangle|\mathbf{x_2}\rangle\dots|\mathbf{x_n}\rangle$. We denote the amount of nonlocality between A and B possessed by $|\Psi_{\text{in}}^{\text{test}}\rangle$ and $|\Psi_{\text{out}}^{\text{test}}\rangle$ by $E_{\text{in}}^{\text{test}}$ and $E_{\text{out}}^{\text{test}}$, respectively, where $E=S(\text{Tr}_A|\Psi\rangle\langle\Psi|)=S(\text{Tr}_B|\Psi\rangle\langle\Psi|)$ is the von Neumann entropy of the reduced density matrix. (Applying U to the test state and computing $E_{\text{in}}^{\text{test}}$ and $E_{\text{out}}^{\text{test}}$ is straightforward.) The nonlocality of production in U is not less than the entanglement difference between the two states:

$$E_U^{\text{Pr}} \ge |E_{\text{in}}^{\text{test}} - E_{\text{out}}^{\text{test}}|.$$
 (6)

How good are these bounds on E_U^{Pr} ? First of all, note that any test state leads to a lower bound. However, different test states may lead to different lower bounds because the nonlocal content of U may not be realized in full when U acts on a particular state. (For example, a test state of the form $|\Psi_{\text{in}}^{\text{test}}\rangle = |\mathbf{x_1}\rangle|\mathbf{x_2}\rangle\dots|\mathbf{x_n}\rangle$ is transformed into $|\Psi_{\text{out}}^{\text{test}}\rangle = |\mathbf{f_1}\rangle|\mathbf{f_2}\rangle\dots|\mathbf{f_n}\rangle$ and leads to no increase in entanglement.) Good test states can be found either by trial and error or by systematic optimization.

A more important restriction is due to the fact that Eq. (4) can be far from tight. This is because when implementing the classical circuit some of the Toffoli gates may increase the entanglement whereas others may decrease it. Thus there may be more efficient ways of implementing U than realizing the classical circuit. For instance, if U acts on states composed of n logical qubits, then $E_U \leq 2n$, because one can always implement U by teleporting Alice's qubits to Bob, letting Bob implement U locally, and teleporting Alice's qubits back to her. This shows that our method can only provide bounds that grow linearly in n. On the other hand, it is known that for some problems of classical reversible computation the number of Toffoli gates grows exponentially [4] and for these problems our method is very inefficient. Nevertheless we expect that in many cases the number of Toffoli gates will grow linearly with, or as a fractional power of, n. Bounding the actual power may give an interesting—indeed, sometimes fundamental—insight.

Let us now consider the case of the Toffoli gate itself and prove the basic fact that classical Toffoli gates cannot be built from reversible two-bit gates. We will do this by showing that under our map the quantum equivalent of the Toffoli gate is nonlocal. Specifically we will obtain the upper and lower bounds

$$1 \le E_T^{\text{Pr}} \le E_T^{\text{Im}} \le 2. \tag{7}$$

Then, from Eqs. (4), (6), and (7) we obtain the following lower bound on the number N_T of Toffoli gates required to carry out a computation

$$N_T \ge \frac{|E_{\rm in}^{\rm test} - E_{\rm out}^{\rm test}|}{2}.$$
 (8)

(Note that it is not essential for our method to find the exact value of $E_T^{\rm Im}$ since in general we are interested only in the scaling of the number of Toffoli gates with the size of the problem.)

The lower bound $E_T^{\rm Pr} \ge 1$ is obtained by showing that under our map the quantum Toffoli gate is capable of producing at least one ebit of entanglement. Consider the test state

$$\begin{split} |\Psi_{\text{in}}^{\text{test}}\rangle &= \frac{1}{2}(|\mathbf{0}\rangle_{1}|\mathbf{0}\rangle_{2}|\mathbf{0}\rangle_{3} + |\mathbf{1}\rangle_{1}|\mathbf{0}\rangle_{2}|\mathbf{0}\rangle_{3} + |\mathbf{0}\rangle_{1}|\mathbf{1}\rangle_{2}|\mathbf{0}\rangle_{3} \\ &- |\mathbf{1}\rangle_{1}|\mathbf{1}\rangle_{2}|\mathbf{1}\rangle_{3}) \\ &= \frac{1}{\sqrt{2}}|001\rangle_{A}|001\rangle_{B} + \frac{1}{2\sqrt{2}}(|000\rangle_{A}|000\rangle_{B} \\ &+ |010\rangle_{A}|010\rangle_{B} + |100\rangle_{A}|100\rangle_{B} \\ &- |110\rangle_{A}|110\rangle_{B}), \end{split}$$

where the third logical bit is the target of the Toffoli gate. After acting with U_T on $|\Psi_{in}^{test}\rangle$ we obtain

$$\begin{split} |\Psi_{\text{out}}^{\text{test}}\rangle &= \frac{1}{2}(|\mathbf{0}\rangle_1|\mathbf{0}\rangle_2|\mathbf{0}\rangle_3 + |\mathbf{1}\rangle_1|\mathbf{0}\rangle_2|\mathbf{0}\rangle_3 + |\mathbf{0}\rangle_1|\mathbf{1}\rangle_2|\mathbf{0}\rangle_3 \\ &- |\mathbf{1}\rangle_1|\mathbf{1}\rangle_2|\mathbf{0}\rangle_3) \\ &= \frac{1}{2\sqrt{2}}(|000\rangle_A|000\rangle_B + |001\rangle_A|001\rangle_B \\ &+ |010\rangle_A|010\rangle_B + |100\rangle_A|100\rangle_B + |011\rangle_A|011\rangle_B \\ &+ |101\rangle_A|101\rangle_B - |1110\rangle_A|110\rangle_B - |1111\rangle_A|111\rangle_B). \end{split}$$

The Schmidt coefficients of $|\psi_{\rm in}^{\rm test}\rangle$ and $|\psi_{\rm out}^{\rm test}\rangle$ are found to be $\{\alpha_i\} = \{\frac{1}{2}, \frac{1}{8}, \frac{1}{8}, \frac{1}{8}, \frac{1}{8}, 0, 0, 0\}$ and $\{\beta_i\} = \{\frac{1}{8}, \frac{1}{8}, \frac{1}{8}, \frac{1}{8}, \frac{1}{8}, \frac{1}{8}, \frac{1}{8}, \frac{1}{8}, \frac{1}{8}\}$, respectively. Hence $E_{\rm in}^{\rm test} = 2$ ebits and $E_{\rm out}^{\rm pr} = 3$ ebits and $E_T^{\rm pr} \ge 1$. (In passing we note that the quantum Toffoli gate cannot be implemented without classical communication: if such an implementation were possible it would violate relativistic causality.)

To obtain the upper bound $E_T^{\rm Im} \leq 2$ we will describe explicitly a method for realizing the quantum map of the Toffoli that requires 2 ebits. Consider three pairs of qubits on which we are going to apply U_T , where the states $|\Phi\rangle_1$, $|\Psi\rangle_2$ are control and $|\Theta\rangle_3$ is a target (see Fig. 1). It is convenient to analyze in parallel the cases where $|\Phi\rangle_1$ is $|\mathbf{0}\rangle_1$ or $|\mathbf{1}\rangle_1$. The two parties start by performing local Hadamard rotations $H_{A(B)}$ [acting as $H|0\rangle = \frac{1}{\sqrt{2}} \times (|0\rangle + |1\rangle)$, $H|1\rangle = \frac{1}{\sqrt{2}}(|0\rangle - |1\rangle)$] of A_1 and B_1 of the first

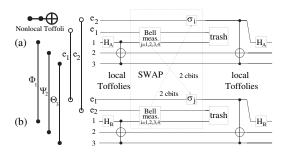


FIG. 1. Implementation of nonlocal Toffoli using 2 ebits as a resource.

pair, obtaining

$$|\mathbf{0}\rangle_{1} \rightarrow |\mathbf{0}'\rangle_{1} = \frac{1}{\sqrt{2}}(|0\rangle_{1}^{A}|0\rangle_{1}^{B} + |1\rangle_{1}^{A}|1\rangle_{1}^{B}),$$

$$|\mathbf{1}\rangle_{1} \rightarrow |\mathbf{1}'\rangle_{1} = \frac{1}{\sqrt{2}}(|0\rangle_{1}^{A}|1\rangle_{1}^{B} + |1\rangle_{1}^{A}|0\rangle_{1}^{B}).$$
(9)

Then the parties proceed by performing local Toffoli gates on their particles, which can be written as

$$U_T^{A(B)} = |0\rangle\langle 0|_1 \otimes I_2 \otimes I_3 + |1\rangle\langle 1|_1 \otimes U_{23}, \tag{10}$$

where U_{23} is a local C-NOT between particles 3 and 2 (with particle 3 as the control and particles 2 as the target). As a result the initial states evolve to:

$$\begin{aligned} |\mathbf{0}'\rangle_{1}|\mathbf{\Psi}\rangle_{2}|\mathbf{\Theta}\rangle_{3} &\rightarrow \frac{1}{\sqrt{2}}(|0\rangle_{1}^{A}|0\rangle_{1}^{B} \\ &+ |1\rangle_{1}^{A}|1\rangle_{1}^{B}U_{23}^{A}U_{23}^{B})|\mathbf{\Psi}\rangle_{2}|\mathbf{\Theta}\rangle_{3}, \\ |\mathbf{1}'\rangle_{1}|\mathbf{\Psi}\rangle_{2}|\mathbf{\Theta}\rangle_{3} &\rightarrow \frac{1}{\sqrt{2}}(|0\rangle_{1}^{A}|1\rangle_{1}^{B}U_{23}^{B} \\ &+ |1\rangle_{1}^{A}|0\rangle_{1}^{B}U_{23}^{A})|\mathbf{\Psi}\rangle_{2}|\mathbf{\Theta}\rangle_{3}. \end{aligned}$$

Next they swap the states of A_1 and B_1 . This operation utilizes two ebits and can be performed using two ordinary teleportations in both directions. This yields

$$\frac{1}{\sqrt{2}}(|0\rangle_{1}^{A}|0\rangle_{1}^{B}+|1\rangle_{1}^{A}|1\rangle_{1}^{B}U_{23}^{A}U_{23}^{B})|\mathbf{\Psi}\rangle_{2}|\mathbf{\Theta}\rangle_{3},$$

$$\frac{1}{\sqrt{2}}(|1\rangle_{1}^{A}|0\rangle_{1}^{B}U_{23}^{B}+|0\rangle_{1}^{A}|1\rangle_{1}^{B}U_{23}^{A})|\mathbf{\Psi}\rangle_{2}|\mathbf{\Theta}\rangle_{3}.$$

Next, they perform (10) again. The resulting states are

$$|\mathbf{0}'\rangle_1 |\mathbf{\Psi}\rangle_2 |\mathbf{\Theta}\rangle_3$$
 and $|\mathbf{1}'\rangle_1 U_{23}^A U_{23}^B |\mathbf{\Psi}\rangle_2 |\mathbf{\Theta}\rangle_3$.

Finally, they apply H_A and H_B again and obtain

$$|\mathbf{0}\rangle_1 |\mathbf{\Psi}\rangle_2 |\mathbf{\Theta}\rangle_3$$
 and $|\mathbf{1}\rangle_1 U_{23}^A U_{23}^B |\mathbf{\Psi}\rangle_2 |\mathbf{\Theta}\rangle_3$.

As we have already noted, two local C-NOT transformations are equivalent to a nonlocal C-NOT transformation. Thus, from the last expression it follows that a nonlocal C-NOT is applied on pairs 2 and 3 (with pair 2 as the control and pair 3 as the target) if and only if the state of the first

pair is |1>. Thus this protocol implements the nonlocal Toffoli gate and utilizes two ebits, which are needed to swap two states in the intermediate stage. Because of linearity of quantum mechanics all these arguments will hold also in the case of arbitrary superposition of initial states.

We illustrate our result (8) on the example of Shannon data compression. We were led to consider this particular example by our research in multiparticle entanglement compression [5]. In fact, this is how we discovered this method in the first place.

The method of classical compression of n-bit source string of 0's and 1's, where p is the probability of each bit to be equal 1, is based on the fact that the most probable (typical) strings generated by the source will contain np ones when n is large [6]. If $p \neq \frac{1}{2}$ then the Shannon entropy of the source H(p) is smaller than 1 and the number of typical strings, $2^{nH(p)}$, is less than the total number of strings 2^n . Thus, a message generated by the source can be compressed to a shorter message.

We consider a "Shannon compressor"—a classical reversible circuit which receives as input an *n*-bit string which contains np ones (i.e., a typical string) and outputs a compressed version of the string in which only the first $\log\binom{n}{np} \simeq nH(p)$ bits carry information and the other n(1-H(p)) redundant bits are set to some standard sequence, e.g., to all 0's:

$$x_1 x_2 \dots x_n \to f_1 f_2 \dots f_{nH} 0_{nH+1} \dots 0_n.$$
 (11)

Our goal is to find a lower bound on the number of Toffoli gates needed to build the "Shannon compressor." We take the initial test state to be the uniform superposition of states with np ones:

$$|\Psi_{\text{in}}^{\text{test}}\rangle = N \sum_{x_i \in \{0,1\}, \sum x_i = np} |\mathbf{x_1}\rangle |\mathbf{x_2}\rangle \dots |\mathbf{x_n}\rangle,$$

where $N = \binom{n}{np}^{-1/2}$. The output state is:

$$|\Psi_{\text{out}}^{\text{test}}\rangle = N \sum_{f_i \in \{0,1\}} |\mathbf{f_1}\rangle |\mathbf{f_2}\rangle \dots |\mathbf{f_{nH}}\rangle |\mathbf{0_{nH+1}}\rangle \dots |\mathbf{0_n}\rangle.$$

For fixed value of p we can calculate the entanglement of $|\Psi_{\rm in}^{\rm test}\rangle$ and $|\Psi_{\rm out}^{\rm test}\rangle$. The entanglement $E_{\rm out}^{\rm test}$ is easy to calculate: it equals the number of output redundant pairs, i.e., $E_{\rm out}^{\rm test}=n(1-H(p))$. We have calculated $E_{\rm in}^{\rm test}$ using a combination of analytical and numerical techniques which are described in [5]. Figure 2 presents our results for p=0.8. A linear dependence of $E_{\rm in}^{\rm test}-E_{\rm out}^{\rm test}$ on n is obtained. Thus, the number of Toffoli gates needed to perform Shannon compression grows at least linearly with n. For instance, for p=0.8 we need at least 0.2332n Toffoli gates. Inspired by our numerical result, Buhrman has found, using a completely different technique, an analytical proof of this lower bound [7].

In summary, we have addressed the problem of evaluating the number of Toffoli gates needed to perform classical

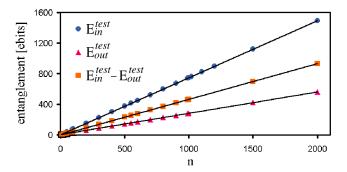


FIG. 2 (color online). Entanglement as a function of string length n for p = 0.8.

reversible computations. We have proposed a method based on quantum information concepts in which strings of classical bits are mapped into sequences of special nonlocal quantum states and classical reversible computations are mapped onto unitary transformations of these quantum states. The nonlocal properties of these transformations provide information about the classical reversible computation. In particular, if the unitary transformation is nonlocal then the corresponding classical reversible circuit cannot be built solely from one- and two-bit gates. The amount of nonlocality possessed by the unitary transformation associated with any classical reversible computation provides a lower bound on the number of Toffoli gates needed for this computation.

As an example we considered classical Shannon compression and calculated the amount of nonlocality of the associated unitary transformation. According to our numerical results, the lower bound on the number of Toffoli gates grows linearly with the size of the string n. Thus quantum methods can provide fundamental insights about classical computation.

We hope that our approach may prove useful for other problems concerning classical reversible computation.

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