

a peak at a finite frequency. The mode may be identified with propagating phase fluctuations. A complete determination of the dispersion relation has not been made.

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<sup>11</sup>If modes of frequencies  $\omega_1$  and  $\omega_2$ , where  $\omega_1 \gg \omega_2$ , are coupled together, then the dispersion relation can be of the form  $\omega = \omega_2 - |V|^2/|\omega_1 - \omega_2|$ , where  $V$  is the coupling parameter.

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## Substrate Bias Effects on Electron Mobility in Silicon Inversion Layers at Low Temperatures

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The mobility of electrons in silicon inversion layers and conductance activation energy have been measured as a function of substrate bias. The mobility increased and the activation energy decreased as the electrons were forced toward the surface. This seems inconsistent with activated conduction arising from potential fluctuations due to oxide charge.

The conductance of electrons in inversion layers in metal-insulator-silicon field-effect transistors (MOSFET's) has been the subject of many studies.<sup>1-4</sup> At low temperatures and all but the highest densities, the electrons lie in the lowest quantum state induced by the field perpendicular to the silicon surface. The results below are surprising because they seem in contradiction to other measurements in the temperature and carrier-density range where the conductance is thermally activated.<sup>1,3,4</sup> Several explanations have been offered to explain the decrease of activation energy of conductance with increasing carrier density. All depend on assuming that oxide charges near the interface play a major role. This is true for scattering by potential fluctua-

tions,<sup>5,6</sup> trapping where the screening increases with charge density and lowers the trap energy,<sup>7</sup> and a percolation model or Mott-Anderson model<sup>8,9</sup> where fluctuations arise from the oxide charges.<sup>10</sup>

If these effects are caused by the fluctuations due to oxide charges, then forcing the electrons closer to the interface should increase the effects. It is well known<sup>11,12</sup> that application of substrate bias,  $V_s$ , between the source contact and the substrate reduces the free-electron charge  $qN_{inv}$  by increasing the depletion charge  $qN_{depl}$  and increases the gate voltage for conductance threshold  $V_t$ . To maintain a given  $N_{inv}$ , the gate voltage must be increased as the substrate is made more negative. Then the free electrons

are forced closer to the surface. At low carrier concentrations, the effect can be strong. Stern<sup>12</sup> has shown that for  $N_{inv} \ll N_{depl}$ , for an acceptor density of  $10^{15}/\text{cm}^3$ , and at 0 K the average distance of the electrons from the surface,  $z_{av}$ , can be reduced from 50 to 25 Å by changing the substrate bias from 0 to -40 V. Such a change in bias might then reasonably be expected to increase the effects of the fluctuations.

Circular MOSFET's similar to those studied in Ref. 1 were studied. The oxide thickness was 500 Å; the width to length ratio was 50; the source-drain spacing was 50 μm; the substrate resistivity was nominally 10 Ω cm; and the surface was (100). Measurements were similar to those reported elsewhere<sup>1</sup>—small-signal conductance and transconductance with the source-drain field kept less than 1 V/cm. To determine the mobility, it is necessary to determine the threshold which was done by measuring the transconductance threshold at 77.3 K. The choice of the nitrogen threshold at helium temperatures is supported by Hall measurements<sup>1</sup> and magneto-oscillatory conductance results.<sup>13</sup> Because of a 40-mV tail, the choice is somewhat arbitrary,

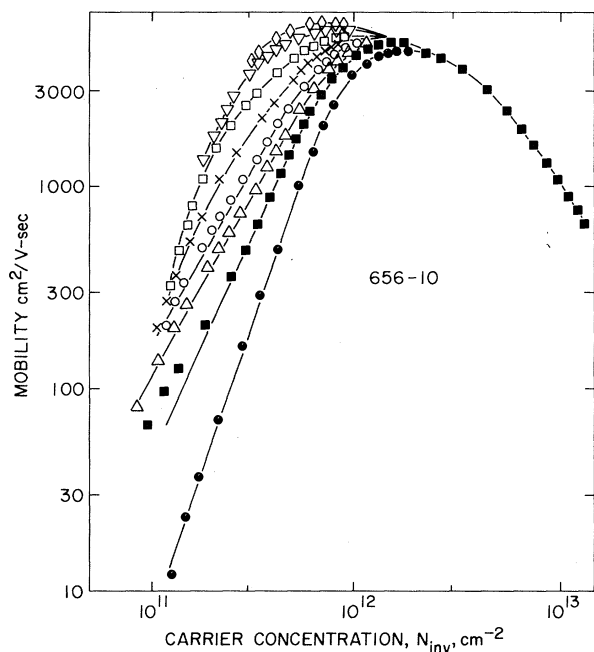


FIG. 1. Electron mobility as a function of carrier concentration and substrate bias at 4.2 K for a sample with  $N_a = 1.36 \times 10^{15}/\text{cm}^3$  and  $Q_{ox}/q = 9.1 \times 10^{10}/\text{cm}^2$ . The curves, starting from the lowest, are for substrate biases of 1.0 V (●), 0 V (■), -1.0 V (Δ), 2.0 V (○), -4.0 V (×), -8.0 V (□), -16 V (▽), and -32 V (◇). The data coincided at higher carrier densities.

but results in an uncertainty in  $N_{inv}$  of not more than  $10^{10}/\text{cm}^2$  which does not significantly affect the results below, unless  $N_{inv} < 10^{11}/\text{cm}^2$ . These thresholds at different substrate biases were plotted as a function of  $(1.12 - V_{sub})^{1/2}$ , where 1.12 V is the surface potential at threshold for zero bias. The result is an excellent straight-line relationship that allows determination of the acceptor density of about  $1.4 \times 10^{15}/\text{cm}^3$ .

The rate at which the depletion layer is formed<sup>14</sup> when the substrate bias is changed is complex, and depends on the initial and final biases, the temperature, the presence of light, and the substrate resistivity. For 10-Ω-cm substrates, up to several minutes were required to come to steady state; for 100-Ω-cm substrates several hours were needed. Details will be reported elsewhere.

The carrier concentration and mobility were calculated from the threshold and conductance in the usual way. The results for one sample are shown in Fig. 1. It is apparent that the mobility increases with reverse substrate bias contrary

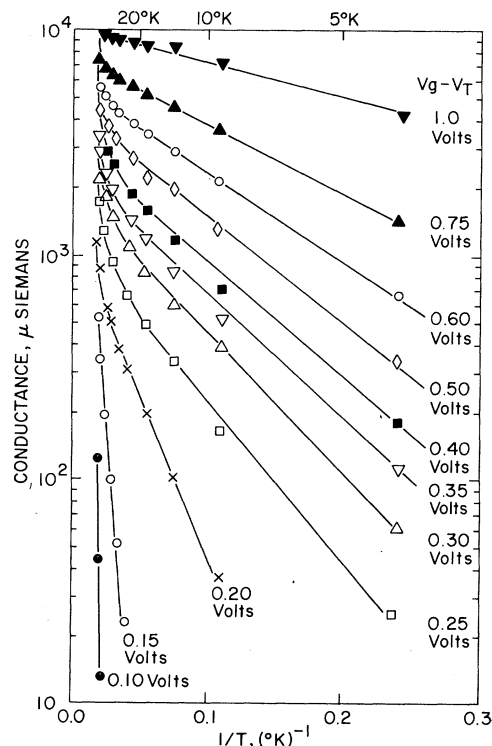


FIG. 2. Conductance as a function of temperature and gate voltage above threshold ( $V_g - V_t$ ), for sample 656-11 for zero substrate bias. A gate voltage above threshold of 1.0 V corresponds to a carrier density of about  $4.4 \times 10^{11}/\text{cm}^2$ . This sample is similar to 656-10.

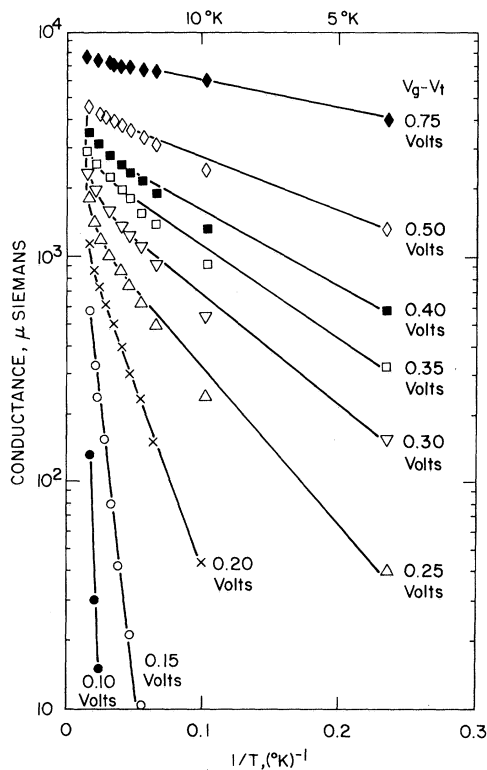


FIG. 3. Similar data as for Fig. 3 except that the substrate bias is  $-3.0$  V.

to expectations. For  $N_{inv} < 10^{12}/\text{cm}^2$ , making the substrate bias more negative has increased the mobility by more than an order of magnitude at some carrier densities. The effect seems to saturate, especially at the lowest concentrations and may, in fact, reverse, but below  $10^{11}/\text{cm}^2$  the data are not as reproducible as at higher concentrations. Other samples indicated that the higher the oxide charge and, therefore, the lower the maximum mobility with  $V_s = 0$ , the smaller the substrate bias effect is. For  $N_{inv} > 10^{12}/\text{cm}^2$  there is almost no effect because  $z_{av}$  is essentially insensitive to the substrate bias.

Measurements were made as a function of temperature above 4.2 K in order to determine the dependence of activation energy on  $N_{inv}$  and  $V_s$ . In Figs. 2 and 3, data are shown as a function of gate voltage above a threshold chosen in an arbitrary but consistent way. It is obvious that the effect of substrate bias is to reduce the activation energy.

Both of the above sets of experiments would

seem to indicate that the effect of substrate bias forcing the electrons closer to the interface is to reduce the effects of the fluctuations, which is an unexpected result. It may be that the role of screening<sup>15</sup> is imperfectly understood, and that the fluctuations are much more effectively screened than expected as the electrons are forced to the surface. Another possibility is that the tail states of the higher quantum levels are populated, and that the effect of substrate bias, which increases the splitting, is to depopulate those levels and to increase the Fermi energy in the ground state. This would increase the conductance and decrease the activation energy. This explanation seems somewhat implausible, since magneto-oscillatory conductance measurements<sup>13</sup> tend to show that only the ground state is occupied. These measurements are difficult, but not impossible for  $N_s < 10^{12}/\text{cm}^2$ . A change of the period of the oscillations with substrate bias would tend to support this model.

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