

square-well model of the impurity potential, and by Alfred and van Ostenburg,¹⁸ based on an interpretation of NMR data.

We have shown how impurity-scattering phase shifts of dilute alloys can be determined from Dingle-temperature data by taking into account the renormalization of the wave function on the impurity site due to backscattering by the host lattice. Our results represent a consistent microscopic interpretation of the Dingle-temperature anisotropies of the dilute silver alloys Ag(Au) and Ag(Sn) taking proper account of the band structure of the host. The derived impurity phase shifts satisfy the optical theorem and the Friedel sum rule, and are consistent with a single set of backscattering coefficients for the silver lattice. We have assumed that off-diagonal elements of the scattering matrix are negligible, and have taken the diagonal elements to be as weak as possible consistent with the data. An investigation of these assumptions from first principles will be the subject of further work.

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Evaluation of Mott's Parameters for Hopping Conduction in Amorphous Ge, Si, and Se-Si

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The temperature dependence of electrical conductivity of rf-sputtered amorphous Ge, Si, and Ge-Si films as functions of annealing temperature and time have been investigated for $350 \geq T \geq 77$ K. Annealing shifts the hopping region to lower temperatures. We find that reasonable values of Mott's parameters are obtained only for properly annealed specimens in the true hopping-conduction region.

The band structure and transport properties of amorphous semiconductors have been subjects of increasing interest in recent years (for recent reviews and bibliographies, see Brodsky,¹ Tsay, Paul, and Mitra,² and others³). Though there is yet no agreement about a proper model for elec-

tron transport mechanisms,^{1,3,4} certain basic experimental facts seem to be well established.^{1,3}

From a series of extremely careful isothermal and step-annealing experiments we have investigated the annealing mechanisms of amorphous germanium,⁵ Ge-Si (50% Ge and 50% Si), and Si

films.⁶ We have systematically studied the influences of annealing on optical absorption and also on the temperature dependence of electrical conductivity in the range between annealing temperatures and 77 K for rf-sputtered Ge, Si, and Ge-Si films prepared in our laboratory. The conductivity of amorphous Ge measured as a function of temperature and time of annealing in the range between room temperature and temperature of annealing has been interpreted⁷ as a function of two activation energies for electrical conduction due to intrinsic and extrinsic processes occurring simultaneously. At still lower temperatures conductivity can be explained by Mott's phonon-assisted tunneling or hopping process. In this Letter, we report the effects of annealing on the low-temperature conductivity of amorphous Ge, Si, and Ge-Si films. We believe this to be the first measurements reported on electrical properties of amorphous Ge-Si films. Also, using our expressions for Mott's parameters derived from Mott's equation⁴ on hopping conductivity, we have evaluated $N(E_F)$ and γ , the density of localized states at the Fermi energy and the inverse rate of falloff of the wave functions associated with these states, respectively, for these materials.

The samples were prepared by rf sputtering on glass or optically polished quartz substrates in an argon atmosphere. The sputtering targets were of intrinsic quality. The sputtering parameters and subsequent characterization of the films were similar to those reported earlier.⁵ Metal-contact-induced crystallization⁸ in amorphous films severely restricts the choice of electrode materials for studying annealing effects on electrical properties. Presputtered molybdenum served as electrodes in our specimens for conductivity measurements. The electrical and optical properties were obtained from samples prepared in the same sputtering operation and subjected later to similar annealing conditions. We have also checked results from samples prepared identically (same sputtering parameters), but in two different sputtering operations. The differences observed were well within the overall accuracy ($\pm 3\%$) of our measurements. A lock-in amplifier and detection system⁵ was used for conductivity measurements. At lower temperatures, as the sample resistance exceeds the input resistance limit of the lock-in amplifier, the voltage was measured across a suitable known resistance instead of that across the sample. A copper-Constantan thermocouple monitored tem-

perature in this range. The lock-in frequency was chosen as 13 Hz. The frequency independence of conductivity at lower frequencies (below 10^4 Hz) for these materials assures that such data as obtained in our measurements can be taken as d.c. conductivity. Moreover, the use of lock-in system has totally eliminated some of the disadvantages (thermo-emf, lead capacitances, other noises, etc.) in the conventional d.c. conductivity measurements with electrometers. Through the use of ratiometric arrangements in a lock-in system, extremely high accuracy ($\pm 1\%$) and fine resolution are possible, which are a few of the prerequisites if one expects to observe small changes in the properties during the annealing process. Some of the specimens were annealed in vacuum and also in an He atmosphere, while others were immersed in silicon oil during annealing. No appreciable difference was observed in the general behavior for these different annealing environments.

The low-temperature conductivity data for sputtered Ge, Si, and Ge-Si films have been plotted as $\sigma\sqrt{T}$ versus $T^{-1/4}$ for $350 \geq T \geq 77$ K in Figs. 1(a), 1(b), and 1(c), respectively. Each of these figures also shows the effect of annealing. It is evident that at low temperatures, the conductivity of all these materials obeys Mott's relation⁴

$$\sigma = \sigma_0 \exp[-(T_0/T)^{1/4}] \quad (1)$$

for hopping conductivity. However, at high temperatures, as expected, they deviate from this law. Annealing, in general, lowers the conductivity by a few orders of magnitude. This has been interpreted⁹ as due to a decrease by annealing in the number of "voids" or "dangling bonds" in the as-deposited films. We observe that an increase in annealing temperature or time shifts the hopping region to lower temperatures and increases the slope of the conductivity curve.

Experimental data of Beyer and Stuke,¹⁰ Chittick,¹¹ and Camphausen, Connell, and Paul¹² show similar behavior. We suggest, with Beyer and Stuke,¹⁰ that (i) at low temperatures, a decrease in conductivity with annealing is due to diminishing hopping states, and (ii) at high temperatures, activation of electrons into other conducting states causes deviation from Mott's relation.

Following Miller and Abrahams,¹³ Mott⁴ has estimated the conductivity at low temperatures due to hopping processes as given in Eq. (1). Hill and others¹⁴ have rigorously worked on Mott's original formulation under various conditions of

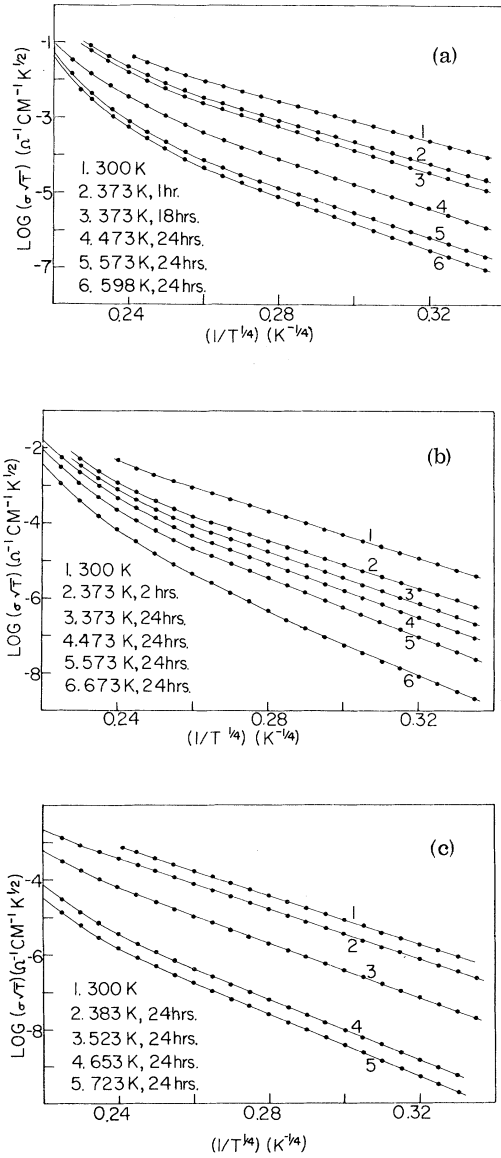


FIG. 1. Plots of $\sigma\sqrt{T}$ versus $T^{-1/4}$ for amorphous semiconductors as a function of annealing temperature; (a) Ge, (b) Ge-Si, and (c) Si films.

temperature, field, and distribution of hopping sites. The constants σ_0 and T_0 in Eq. (1) are expressed functionally as

$$\sigma_0 = e^2 a^2 \nu_{\text{ph}} N(E_F) \quad (2)$$

and

$$T_0 = \lambda \gamma^3 / k N(E_F), \quad (3)$$

where e is the electronic charge, a is the hopping distance, ν_{ph} is a phonon frequency ($\sim 10^{13} \text{ sec}^{-1}$)

obtained from the Debye temperature, and k is Boltzmann's constant. λ is a dimensionless constant (~ 18.1). Ambegaokar, Halperin, and Langner¹⁴ have shown its lower limit to be 16. The constants σ_0 and T_0 have been found to depend on annealing history. Two other parameters, R and W , the hopping distance and the average hopping energy, respectively, due to Mott⁴ and Hill,¹⁴ are given as

$$R = [9/8\pi\gamma k T N(E_F)]^{1/4} \text{ cm} \quad (4)$$

and

$$W = [3/4\pi R^3 N(E_F)] \text{ eV}. \quad (5)$$

Substituting for a in Eq. (2) the average hopping distance R , we have

$$\sigma_0 \sqrt{T} = A [N(E_F) / \gamma]^{1/2}, \quad (6)$$

where $A = 3e^2 \nu_{\text{ph}} / (8\pi k)^{1/2}$. Simultaneous solution^{15,16} of equations (3) and (6) yields

$$N(E_F) = (1.996 \times 10^{48} / \nu_{\text{ph}}^3) \times [(\sigma_0 \sqrt{T})^3 \sqrt{T_0}] \text{ cm}^{-3} \text{ eV}^{-1} \quad (7)$$

and

$$\gamma = (21.22 \times 10^{13} / \nu_{\text{ph}}) [(\sigma_0 \sqrt{T}) \sqrt{T_0}] \text{ cm}^{-1}. \quad (8)$$

We have evaluated Mott's parameters using Eqs. (4), (5), (7), and (8) and our experimental data from Figs. 1(a)-1(c). The results are listed in Table I for these materials. We have also analyzed the conductivity data of several workers on amorphous Ge and Si (unannealed as well as annealed) prepared by various techniques^{1,3,9-12,16} at different laboratories. Quite unreasonable values of $N(E_F) \geq 10^{39}$ and $\gamma \geq 10^{13}$ were obtained. Except for Beyer and Stuke,¹⁰ other authors have not made any distinction between extrinsic conduction and true hopping regions. As for Lewis's¹⁶ data on amorphous Si, even though W and γR are reasonable, we found unreasonably high values of $N(E_F)$ and γ in each case. On the other hand, Beyer and Stuke¹⁰ annealed evaporated Ge films for a very short time (~ 15 min) only. The parameters evaluated from their data approached reasonable limits only for high-temperature annealing.¹⁷ We observe from Table I that for true hopping conduction and properly annealed specimens, the parameters seem to be reasonable within Mott's requirements of the localized-state model, i.e., $\gamma R \gg 1$, $W \gg kT$. γ and $N(E_F)$, however, still seem to be somewhat larger than their expected values.¹⁴ This deviation is most likely

TABLE I. Mott's parameters for amorphous Ge, Si, and Ge-Si films. Phonon frequency is calculated from the Debye temperature.

Material	Annealing History	T_0 (10^7 xK)	$\sigma_0 \sqrt{T}$ ($10^5 \text{x}\Omega^{-1} \text{cm}^{-1} \text{K}^{1/2}$)	γ (10^{10}xcm^{-1})	$N(E_F)$ (cm^{-3}/eV)	$R(150\text{K})$ (10^{-10}xcm.)	$W(150\text{K})$ (eV)	γR
R.F. sputtered amorphous Si	300K	3.47	0.955	0.871	3.97×10^{27}	9.46	0.07	8.24
	383K, 24hrs.	3.81	0.617	0.589	1.12×10^{27}	14.3	0.07	8.43
	523K, 24hrs.	5.13	0.389	0.431	3.26×10^{26}	21.1	0.08	9.08
	653K, 24hrs.	8.06	0.214	0.297	6.81×10^{25}	34.2	0.09	10.17
	723K, 24hrs.	8.76	0.162	0.235	3.08×10^{25}	44.2	0.09	10.38
R.F. sputtered amorphous Ge	300K	1.30	1.52	1.52	5.70×10^{28}	4.23	0.06	6.44
	373K, 1hr.	1.58	1.12	1.24	2.51×10^{28}	5.46	0.06	6.77
	373K, 18hrs.	1.87	0.937	1.13	1.60×10^{28}	6.26	0.06	7.06
	473K, 24hrs.	3.0	0.698	1.06	8.38×10^{27}	7.47	0.07	7.94
	573K, 24hrs.	3.34	0.238	0.383	3.51×10^{26}	21.3	0.07	8.16
	598K, 24hrs. } 623K, 24hrs. }	3.70	0.202	0.342	2.26×10^{26}	24.5	0.07	8.37
R. F. sputtered amorphous Ge-Si	300K	2.40	2.14	2.09	7.89×10^{28}	3.60	0.06	7.51
	373K, 2hrs.	3.27	1.76	2.02	5.12×10^{28}	4.05	0.07	8.11
	373K, 24hrs.	4.31	1.54	2.01	3.94×10^{28}	4.32	0.07	8.69
	473K, 24hrs.	4.59	0.939	1.27	9.22×10^{27}	6.98	0.08	8.83
	573K, 24hrs.	5.17	0.631	0.902	2.97×10^{27}	10.1	0.08	9.10
	673K, 24hrs.	6.28	0.250	0.394	2.04×10^{26}	24.2	0.08	9.55

due to the uncertainties involved in estimating σ_0 and T_0 from experimental data.

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¹⁷We have shown in Ref. 5 that the effect of annealing at high temperature for a shorter time is equivalent to that at low temperature for a much longer time.