Gatemon Benchmarking and Two-Qubit Operations

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(Received 30 December 2015; published 15 April 2016)

Recent experiments have demonstrated superconducting transmon qubits with semiconductor nanowire Josephson junctions. These hybrid gatemon qubits utilize field effect tunability characteristic of semiconductors to allow complete qubit control using gate voltages, potentially a technological advantage over conventional flux-controlled transmons. Here, we present experiments with a two-qubit gatemon circuit. We characterize qubit coherence and stability and use randomized benchmarking to demonstrate singlequbit gate errors below 0.7% for all gates, including voltage-controlled Z rotations. We show coherent capacitive coupling between two gatemons and coherent swap operations. Finally, we perform a two-qubit controlled-phase gate with an estimated fidelity of 91%, demonstrating the potential of gatemon qubits for building scalable quantum processors.

DOI: 10.1103/PhysRevLett.116.150505

The scalability and ubiquity of semiconductor technology make it an attractive platform for a quantum processor. Semiconductor qubit devices offer simple and flexible control using voltages on high impedance gate electrodes that readily allow low-power operation at cryogenic temperatures. However, such field effect-based control also makes semiconductor qubits susceptible to electrical charge noise that can strongly degrade the fidelity of gate operations. In both semiconductor charge qubits and spin qubits using exchange coupling, charge noise directly modulates the energy splitting between states, resulting in inhomogeneous dephasing times that are typically only ~ 10 times longer than gate operation times [1–4]. Recently, a new semiconductor-based qubit, the gatemon, has been introduced [5,6]. This hybrid qubit is a superconducting transmon qubit that, crucially, features a semiconductor Josephson junction (JJ). Gatemons, therefore, combine the *in situ* tunability of a semiconductor with the simple connectivity and operation of transmons [7,8]. Initial experiments measured microsecond dephasing times that far exceeded ~ 10 ns gate operation times [6], encouraging further investigation and optimization of this qubit.

In this Letter, we explore coherence and gate operations of gatemons in a two-qubit circuit. We study the influence of the distinct gatemon spectrum on coherence and use Ramsey interferometry to precisely probe the stability of the semiconductor JJ. The excellent stability observed together with improved coherence allows for randomized benchmarking of single-qubit gates [9,10], including Z rotations implemented with gate pulses [11,12]. We also demonstrate coherent capacitive coupling between two gatemons. Finally, with the implementation of a controlled-phase gate, we demonstrate that semiconductor-based gatemons are conceptually similar to transmons, but with the technological

advantage of full voltage control, making them ideally suited for large-scale quantum processors.

Figure 1(a) shows the two-qubit device. Each gatemon operates as an LC oscillator with a nonlinear inductance



FIG. 1. Two-qubit device and single gatemon quantum coherence. (a) Optical micrograph of the two gatemon device. Each qubit consists of a *T*-shaped island and a gated Al-InAs-Al Josephson junction. (b) Scanning electron micrograph of the gated semiconducting weak link Josephson junction of *Q*1. (c) Lifetime measurement for *Q*2 (blue) with qubit resonance frequency $f_Q = 5.225$ GHz. In red, we perform an echo experiment to determine $T_{2,echo} \sim 9.5 \ \mu$ s. The solid and dashed lines are exponential fits. Inset: pulse sequences for dephasing ($T_{2,echo}$) [red] and relaxation (T_1) [blue] measurements. (d) A Ramsey experiment is performed to determine T_2 for *Q*2 with the pulse sequence shown as an inset. The solid line is a fit to an exponentially damped sinusoid.

due to the JJ. The $|0\rangle$ and $|1\rangle$ states are addressable with a transition frequency $f_Q \approx \sqrt{8E_CE_J}/h$, where E_C is the charging energy and E_J is the Josephson coupling energy [7,13]. The JJ is a superconductor-semiconductor-superconductor junction which allows E_J to be tuned using a gate voltage that controls the carrier density in the few-channel semiconductor region.

The sample is fabricated following the recipe described in Ref. [6] and in the Supplemental Material [14]. The JJ is formed by selectively wet etching a segment of a ~30 nm thick Al shell epitaxially grown around a ~75 nm diameter single crystal InAs nanowire [17]. E_C/h is determined by the capacitance of the *T*-shaped Al island to the surrounding ground plane and designed to be ~200 MHz with E_J/E_C tuned to 70-130 using the side gate voltage $V_{1(2)}$ for Q1(2). The interqubit coupling rate g_{12} is determined by the capacitance C_{12} between the two islands. From electrostatic simulations we estimate $2g_{12}/2\pi \approx 20$ MHz for $f_Q = 6$ GHz.

Qubit manipulation is performed using phase-controlled microwave pulses for rotations around axes in the X-Y plane of the Bloch sphere and voltage pulses on $V_{1,2}$ for Z-axis rotations. For dispersive readout, the two qubits are coupled to individual $\lambda/4$ superconducting cavities (with resonant frequencies $f_{C1} \sim 7.81$ and $f_{C2} \sim 7.73$ GHz), both coupled to a common feed line [18]. Crossover wiring on control lines is used to tie together interrupted regions of the ground plane to help suppress spurious modes [19]. The sample is placed inside an Al box, surrounded by a cryoperm shield and mounted at the mixing chamber of a cryogen-free dilution refrigerator with base temperature <50 mK [14].

Figure 1(c) shows a lifetime measurement for Q2. With the qubit excited to the $|1\rangle$ state with a π pulse, the delay time τ before readout is varied and the decay (blue) fitted to an exponential, giving $T_1 = 5.3 \ \mu s$. We attribute the factor of ~ 10 improvement in lifetime relative to Ref. [6] to reduced interface losses due to the removal of the lossy thermal SiO₂ layer [20]. T_2^* is determined by a Ramsey measurement [Fig. 1(d)], where two X/2 pulses are separated by delay time τ [21]. A fit to the decay of the Ramsey fringes gives a dephasing time $T_2^* = 3.7 \ \mu s$, comparable to dephasing times for flux-controlled transmon devices [22]. We perform an echo experiment by inserting a refocusing X pulse between two Y/2 pulses [Fig. 1(c), red]. The extracted $T_{2,echo} = 9.5 \ \mu s \approx 2T_1$ indicates that gatemon dephasing is dominated by low frequency noise [23].

In present devices, the gatemon spectrum is a nonmonotonic function of gate voltage, reflecting the effect of mesoscopic fluctuations in the transmission through the nanowire [5,6,24]. In Fig. 2, we examine how this nonmonotonic response impacts the stability of f_Q , T_1 , and T_2^* . Insets of Figs. 2(a) and 2(b) plot the qubit transition frequency as a function of gate voltage. Points A and C in



FIG. 2. Qubit spectroscopy and qubit frequency drift. (a), [(b)] The insets show the resonance frequency f_Q of Q1 (Q2) as a function of gate voltage V_1 (V_2). We identify sweet spots A (C) and nonsweet spots B (D) in the qubit spectra. The main panels plot the normalized change in qubit resonance frequency, Δf of Q1 (Q2) as a function of time, measured at points A (C) (green, solid line) and at B (D) (orange, dashed line). (c) T_1 (solid symbols, blue) and T_2^* (empty symbols, black) of Q2 as a function of f_Q . The error bars indicate the variance of several consecutive measurements. (d) Lifetimes and coherence times plotted against the derivative of the frequency with respect to gate voltage, |df/dV|.

the insets of Figs. 2(a) and 2(b) are so-called sweet spots where the spectrum is first order insensitive to gate voltage fluctuations. At these operating points, the drift of the resonance frequencies $\Delta f(t) = [f_O(t) - f_O(0)]/f_O(0)$ as measured with a Ramsey experiment, is very small over several hours. The frequency drift has a standard deviation of 55 and 15 parts per million for Q1 and Q2, respectively [Figs. 2(a) and 2(b) main panels, green solid curves]. Away from sweet spots [Figs. 2(a) and (b) main panels, dashed orange curves], we observe two distinct types of behavior. Q1 drifts slowly on a time scale of hours, whereas Q2exhibits discrete jumps in f_O . The slow time scales of these drifts allow for reproducible T_1 (blue) and T_2^* (black) measurements for Q2 [Fig. 2(c)]. Coherence times are on the order of μ s but widely fluctuating, similar to observations for flux-controlled transmons [18]. The region of short coherence at ~ 5.35 GHz could be due to coupling to a two level system [22]. No correlations between sweet spots and coherence times are observed, when plotting the data as a function of the derivative |df/dV| of the spectrum [Fig. 2(d)], consistent with the slow drifts observed. In the remaining experiments, gatemon operation is restricted to sweet spots. Away from sweet spots, the slow drift could be readily stabilized using feedback in future experiments [25].



FIG. 3. Randomized benchmarking of single-qubit gates. The upper right inset serves as a legend and plots the extracted gate errors r for the reference, microwave gates, and Z rotations. We apply m single-qubit Clifford gates C to Q2. The lower left inset shows the two sequence types applied to determine the gate error r. Both the random reference and interleaved gate G sequences are followed by a recovery pulse C_R , and a measurement. The main panel plots the averaged fidelity over many different random sequences for all gates investigated. The reference is shown in black. Interleaved gates benchmarked include microwave pulses (grey) and the gate pulsed Z rotations Z (red) and Z/2 (blue), solid lines are fits to a power law.

We use randomized benchmarking with Clifford gates to evaluate the single-qubit gate fidelity for Q2 [10]. The Clifford gates are all generated by one or more Gaussian shaped microwave pulses, each taking $t_{gate} = 28$ ns (standard deviation, $\sigma = 7$ ns). The microwave pulses are tuned up using both the AllXY pulse sequence [26] and randomized sequences [27]. To benchmark the average error per Clifford gate, a random gate sequence of length m is applied, followed by a recovery pulse C_R projecting the random state back into the ground state and a measurement. The ground state population serves as a measure of sequence fidelity, which decays with increasing m[Fig. 3, black open symbol]. Fitting the decay with the functional form $Ap^m + B$ yields the average fidelity p per Clifford gate. From a best fit [Fig. 3, solid black], we find the reference average fidelity $p_{\rm ref}=0.981$. Parameters A = 0.53 and B = 0.42 quantify state preparation and measurement errors. As only microwave pulses are used to generate the whole single-qubit Clifford set, an average Clifford gate consists of 1.875 single pulses [11]. We extract the average single-qubit gate error $r_{\rm ref} =$ $(1 - p_{\rm ref})/(2 \times 1.875) = 0.5 \pm 0.07\%.$

To benchmark individual gates, a specific gate *G* is interleaved with a random Clifford gate [10] [Fig. 3 lower left inset]. A fit to the decay gives p_G . Using the average Clifford gate fidelity as reference, we extract the specific gate error $r_G = (1 - p_G/p_{\text{ref}})/2$. Figure 3 shows all



FIG. 4. Coherent gatemon coupling. (a) Measurement of the avoided level crossing between Q1 and Q2. The sum of the normalized heterodyne readout amplitude $|V_{H1}| + |V_{H2}|$ for both qubits is shown as a function qubit drive and V_1 ($V_2 = 2.25$ V). On resonance at $V_1 = -3.78$ V the lower branch disappears. (b) Pulse sequence to probe the coherent coupling between the qubits. With Q1 and Q2 detuned, Q1 is prepared in the ground state and a X pulse prepares Q2 in $|1\rangle$. A gate pulse with amplitude ΔV_2 brings Q2 close to or in resonance with Q1 for time τ . (c) The $|1\rangle$ state probability, $P_{|1\rangle}$, for Q1 as a function of ΔV_2 and τ . (d) $P_{|1\rangle}$ for both Q1 and Q2 at the voltage pulse amplitude that brings the two qubits into resonance.

benchmarked microwave gates in grey, including the identity operation *Id*. The inset displays the extracted errors for different gates, which are consistent with the average microwave gate error. Unique to the gatemon implementation are rotations around the *Z* axis performed by applying a voltage pulse. The voltage pulses are nominally square pulses with length 28 ns with amplitude optimized using random sequences with $m \ge 10$ [27]. Benchmarking *Z*/2 and *Z* rotations, the fidelity decays of both gates are found to be indistinguishable from microwave gates, with extracted errors of $r_Z = 0.35 \pm 0.19\%$ and $r_{Z/2} = 0.18 \pm 0.15\%$ [Fig. 3]. With $T_1 = 3.5 \ \mu s$ at point *C*, all gate errors [Fig. 3 inset] are close to the limit set by relaxation, $r_{\text{limit}} = t_{\text{gate}}/3T_1 = 0.3\%$ [28].

To probe the two-qubit coupling, we measure the spectrum while tuning Q1 and Q2 into resonance, driving both qubits through the same XY control line. On resonance, the two-qubit states hybridize due to the capacitive coupling C_{12} and an anticrossing is observed, see Fig. 4(a). We note the disappearance of the lower branch upon hybridizing, a signature of a dark and a bright state formation [29].

Applying the pulse sequence in Fig. 4(b), a single excitation can coherently oscillate between Q1 and Q2. With the two qubits detuned by ~200 MHz and Q1 idling, Q2 is prepared in $|1\rangle$, and then, a gate pulse is applied for time τ , to bring Q2 into resonance with Q1 [30]. Depending on τ and the pulse amplitude ΔV_2 , elementary excitations swap between the two qubits, creating single excitation superpositions of the two qubits. Figure 4(c) shows the



FIG. 5. Controlled-phase c_Z gate operation. (a) Pulse sequence to probe the controlled phase shift of Q2 (target). Q1 (control) is either prepared in $|0\rangle$ (blue) or $|1\rangle$ (red). The acquired singlequbit phase of Q2 is measured with a Ramsey experiment (black), where the phase of the second pulse θ is varied. Between Ramsey pulses, Q2 is pulsed towards the $|20\rangle - |11\rangle$ anticrossing. (b) The inset shows the energy level diagram of the anticrossing (solid line) and the sum of the $|01\rangle$ and $|10\rangle$ transition frequencies (dashed line). In the main panel the probability of Q2 being in $|1\rangle$, $P_{O2|1\rangle}$, is plotted as a function of θ . (c) Pulse sequence to probe the error of the c_Z^{π} gate. Q1 is prepared either in $|0\rangle$ (blue) or $|1\rangle$ (red). The c_Z^{π} gate is interleaved with a random single-qubit Clifford gate C. (d) Lower left inset: The interleaved two-qubit gate sequence is followed by a recovery pulse C_R and a measurement. The main panel shows the decay in fidelity with increasing sequence length m for a random preparation of Q1(green). Two-qubit gate error r extracted from the decay is shown in the upper right inset for different preparations of Q1.

typical chevron pattern of swap oscillations [31]. Figure 4(d) shows the swap oscillations for both Q1 and Q2 idling at A and C. From the sine fits to the oscillations (solid lines), we extract the interaction rate $2g_{12}/2\pi = 17.8$ MHz.

The $|20\rangle - |11\rangle$ anticrossing is used to implement a controlled-phase gate following [32]. Figure 5(a) illustrates the c_Z^{π} tune-up procedure. Q1 serves as a control qubit and is either prepared in $|0\rangle$ (blue) or $|1\rangle$ (red). To measure the phase acquired by the target qubit, Q2 is prepared in the $(|0\rangle + |1\rangle)/\sqrt{2}$ state. By applying a gate pulse on V₂, the system is brought close to the $|20\rangle - |11\rangle$ anticrossing [schematic in the inset of Fig. 5(b)]. For Q1 in $|0\rangle$ (grey dashed line), Q2 only acquires a dynamical phase due to the change in frequency. For Q1 in the $|1\rangle$ state (black), Q2 acquires an additional two-qubit phase due to the anticrossing. After the gate pulse, Q2 is projected back onto the poles, by a $\pi/2$ pulse with varying phase θ [33]. The used gate pulse is 54 ns long and 0.16 V in amplitude, for which the acquired dynamical phase mod 2π is approximately π [blue in Fig. 5(b)]. For this gate pulse, the additional controlled phase shift on Q2, if Q1 is in the $|1\rangle$ state (red), is $\sim \pi$ and, thus, constitutes a c_Z^{π} gate.

The fidelity of the two-qubit gate was estimated by interleaving the c_Z^{π} gate with single-qubit random Clifford

gates applied to the target qubit, Q2, with the control qubit, Q1, prepared randomly in either the $|0\rangle$ or $|1\rangle$ state, as shown in Fig. 5(c) [33,34]. Q1 is returned to the $|0\rangle$ state after every c_Z^{π} gate. Because Q1 is only subject to X rotations, the two-qubit phase can essentially be mapped as a single-qubit Z rotation. The sequence is repeated m times, followed by a recovery pulse C_R and readout of Q2. The main panel in Fig. 5(d) shows the fidelity decay with m, resulting in c_{Z}^{π} gate error $r = 9 \pm 2\%$. If Q1 is prepared in only $|0\rangle$ or $|1\rangle$, the errors are similar [upper right inset in Fig. 5(d)]. Based on measurements using an identity operation instead of a c_Z^{π} gate pulse, we estimate a 4% error due to relaxation. We attribute the remaining 5% error to leakage into the $|20\rangle$ state and systematic phase errors, which can be minimized through improved shaping of the c_Z^{π} pulse [27,35].

In summary, we have presented gate operations for a hybrid superconductor-semiconductor qubit, with inhomogeneous dephasing times comparable to conventional fluxcontrolled transmon qubits and single-qubit gate fidelities exceeding 99%. Through both optimizing gate operations and further leveraging recent advances in conventional transmon qubit lifetimes, we expect one- and two-qubit gate fidelities comparable with conventional transmons can be achieved. Combined with simple qubit control using gate voltages, gatemon qubits may present an attractive route to large scale quantum computing.

We thank John M. Martinis for helpful discussions. This work was financially supported by Microsoft Project Q, the Villum Foundation, and the Danish National Research Foundation. K. D. P. was further supported by a Marie Curie Fellowship.

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