# Interedge backscattering in buried split-gate-defined graphene quantum point contacts

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Quantum Hall effects offer a formidable playground for the investigation of quantum transport phenomena. Edge modes can be deflected, branched, and mixed by designing a suitable potential landscape in a two-dimensional conducting system subject to a strong magnetic field. In the present work, we demonstrate a buried split-gate architecture and use it to control electron conduction in large-scale single-crystal monolayer graphene grown by chemical vapor deposition. The control of the edge trajectories is demonstrated by the observation of various fractional quantum resistances, as a result of a controllable interedge scattering. Experimental data are successfully modeled both numerically and analytically within the Landauer-Büttiker formalism. Our architecture is particularly promising and unique in view of the investigation of quantum transport via scanning probe microscopy, since graphene constitutes the topmost layer of the device. For this reason, it can be approached and perturbed by a scanning probe down to the limit of mechanical contact.

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### I. INTRODUCTION

The quantum Hall (QH) effect has offered exciting opportunities for the investigation of quantum transport in two-dimensional electron gas systems [1] for more than three decades, and it still is the foundation for a number of research activities. QH physics is particularly interesting—for a set of different reasons—in the case of graphene [2,3], a two-dimensional (2D) layer of carbon atoms arranged in a honeycomb lattice. First of all, owing to the nontrivial Berry phase of the electron system [4,5], the QH effect in graphene displays half-integer plateaus and thus differs from what is observed in other conventional 2D systems [2,3,6–10]. In addition, graphene is an ambipolar material, and opposite QH chiralities can be obtained on the same sample by simply tuning the carrier density: for instance ambipolarity was exploited to investigate Klein tunneling [11-13] and the QH physics in graphene p-n junctions [14–16]. Finally, graphene implements a stand-alone one-atom-thick 2D electron system, and charge conduction essentially occurs at its surface. Differently from other materials, conducting electrons can thus be approached down to any small distance: this characteristic offers unique perspectives in view of the investigation of the local conduction properties, in particular in the context of quantum transport and QH physics. In order to take advantage of all these features, however, it is necessary to implement new methods to control the local carrier density in graphene, while retaining direct access to its surface and maintaining low-level disorder. To this end, here we investigate a buried split-gate architecture in which graphene constitutes the topmost layer of the device. We demonstrate that good mobility can be obtained using single-crystal monolayer graphene grown by chemical vapor deposition (CVD) [10,17]. The successful control of edge trajectories in the QH regime is demonstrated by the observation of fractional quantized resistance values, akin to what was recently demonstrated in a conventional top split-gate architecture (i.e., a device in which the split-gate is placed on top of the graphene) [18]. Owing to the different electrostatics of the device, though, lever arm values observed here for back and local gates are markedly different with respect to previous works [18,19].

The observed experimental behavior is described within the framework of the Landauer-Büttiker formalism by assuming the presence of three distinct filling factors: in the bulk of the sample  $(v_{BG})$ , above the split-gate electrodes  $(v_{SG})$ , and in the quantum-point-contact opening  $(v_{OPC})$ . In order to support the consistency of this interpretation and to provide a more general modeling framework, we present numerical calculations where the electrostatic potential landscape induced by the electrodes is directly obtained by solving the Poisson equation, and no assumptions are made on local filling factors. In the quantum scattering problem, decoherence and equilibration are introduced in the present model only in regions where different edge modes co-propagate [16,20]. Numerical results reproduce the experimental data and confirm that transport in our devices is governed by currents flowing along the edges of regions with three distinct filling factors.

# II. EXPERIMENTAL METHODS

In our sample, four split gates [indicated as SG1 to SG4 in Fig. 1(a)] were patterned by electron beam lithography (EBL) on a Si/SiO<sub>2</sub> substrate (oxide thickness 300 nm). The width of the gate fingers is 500 nm, while their relative distance is 400, 600, 800, and 1000 nm for each split-gate pair, respectively. The local gating structure was then buried under a PMMA layer, which was spin-coated on the sample and played the role of gate insulator. A 200  $\mu$ m × 200  $\mu$ m region centered on the split gates was then cross-linked by a high-dose e-beam exposure (15 000  $\mu$ C/cm<sup>2</sup>), while the rest of the PMMA was

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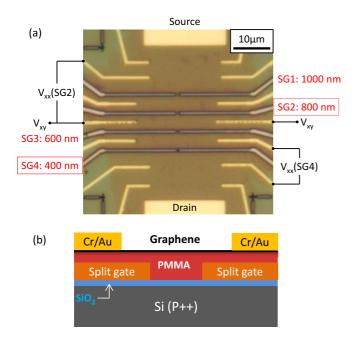


FIG. 1. (a) Optical micrograph of the device. The four split gates are indicated as SG1 to SG4. SG2 and SG4 were investigated in the present work. The contacts that were used to measure the longitudinal voltage drop  $V_{xx}$  across SG2 and SG4 are indicated, as well. The transversal voltage drop  $V_{xy}$  was measured in the center of the device. (b) Cross-sectional sketch of the device.

dissolved in acetone. The final thickness of the PMMA was measured to be 150 nm.

Single-crystal monolayer CVD graphene was grown on oxidized Cu foil using a cold-wall CVD reactor [17]. To minimize the transfer-induced contamination, it was removed from the growth substrate using electrochemical delamination [21] and then transferred on top of the PMMA and precisely aligned to the split-gate structures [10]. As final step, metallic contacts (Cr/Au: 10 nm/60 nm) to the graphene flake were defined by EBL and thermal evaporation. A cross section of the complete buried split-gate architecture is shown in Fig. 1(b). For the present work, only the 400-nm-wide and the 800-nm-wide split gates were investigated, indicated as SG4 and SG2, respectively, in Fig. 1(a). Measurements were performed using a four-terminal lock-in technique in a <sup>3</sup>He closed-cycle system with base temperature of 250 mK. The longitudinal and transversal resistances are defined as  $R = V_{ii}/I_{SD}$ , with  $V_{ii}$ the voltage drop measured between contacts i and j, and  $I_{SD}$ the applied source-drain current (10 nA for all measurement).

### III. RESULTS

The electrostatic action of the various gates on the carrier density in graphene is illustrated in Fig. 2(a) showing the longitudinal resistance as a function of back-gate ( $V_{BG}$ ) and split-gate ( $V_{SG}$ ) voltages, at B=0 T. The impact of the back gate is clearly visible, and a resistance maximum at  $V_{BG}\approx 15$  V is observed for every value of  $V_{SG}$ . This trend is also visible from the cross-sectional plot in Fig. 2(b), which was obtained along the vertical dashed green line in Fig. 2(a) at  $V_{SG}=0$  V. This resistance maximum corresponds to the

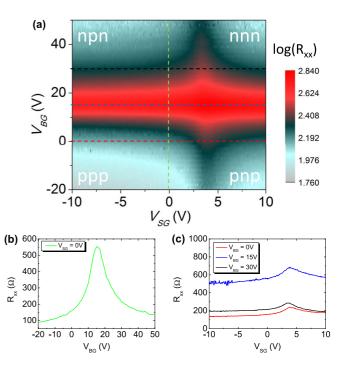


FIG. 2. (a) Measured longitudinal resistance  $R_{xx}$  as a function of  $V_{BG}$  and  $V_{SG}$ . Note the logarithmic scale for  $R_{xx}$ . (b)  $R_{xx}$  as a function of  $V_{BG}$  for fixed  $V_{SG} = 0$  V. (c)  $R_{xx}$  as a function of  $V_{SG}$  for  $V_{BG} = 0$  V, 15 V, and 30 V. Split-gate width 400 nm, B = 0 T, T = 270 mK.

charge neutrality point (CNP), or Dirac point, in the bulk of the graphene flake.

It is well known that the carrier concentration n in graphene varies with  $V_{BG}$  as  $n \approx C_{BG}|V_{BG} - V_{\rm Dirac}|/e$  [22]. Here  $C_{BG}$  and e are the gate capacitance per area and the elementary charge, respectively. The gate capacitance for a 300 nm SiO<sub>2</sub> oxide is  $C_{BG0} = 11.5 \, {\rm nF/cm^2}$  [22]. Here, we need to consider the additional 150 nm thickness of the PMMA layer, which has approximately the same dielectric constant value of SiO<sub>2</sub>. We therefore set  $C_{BG} \approx C_{BG0}/1.5 = 7.67 \, {\rm nF/cm^2}$ . This yields an intrinsic carrier density of  $7.2 \times 10^{11} \, {\rm cm^{-2}}$  at  $V_{BG} = 0 \, {\rm V}$  (hole doping), in good agreement with what is typically observed for this kind of CVD graphene [10]. The mobility  $\mu$  of the device was determined from  $\mu = 1/ne\rho$ , where  $\rho$  is the resistivity at  $B = 0 \, {\rm T}$ . A transport mobility, away from the Dirac point, of  $15 \, 300 \, {\rm cm^2/(V \, s)}$  was determined for  $V_{BG} = 0 \, {\rm V}$ .

The effect of  $V_{SG}$  on the carrier density in graphene is not as pronounced, but a peak in  $R_{xx}$  can also be observed for every given value of  $V_{BG}$ . Selected profiles are shown in Fig. 2(c) and correspond to the horizontal dashed lines in Fig. 2(a). The resistance maximum is always observed at  $V_{SG} \approx 4$  V, regardless of the value of  $V_{BG}$ . This behavior can be interpreted as due to the local modulation of the carrier density in the regions immediately above the split-gate structure. Also in this case, the resistance peaks when graphene is tuned to the CNP. However, given the limited graphene area controlled by the split gates, the magnitude of the peak is markedly smaller than the one observed for the back-gate sweep.

In addition, it should be noted that the two values (4 V and 15 V) are in good agreement with the different expected capacitive couplings between the split gate and graphene, and

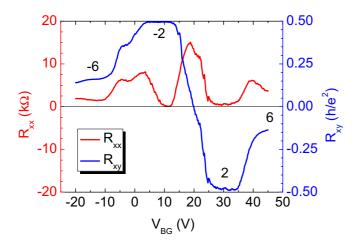


FIG. 3. Longitudinal  $(R_{xx})$  and transverse  $(R_{xy})$  resistance in the bulk of the graphene flake as a function of back-gate voltage  $V_{BG}$  at  $B=10\,\mathrm{T}$  and  $V_{SG}=0\,\mathrm{V}$ .

between the back gate and graphene. In the former case, the dielectric insulation is just due to the 150-nm-thick PMMA layer; in the latter, capacitive coupling is mediated by a stack of 300 nm of SiO<sub>2</sub> and, again, 150 nm of PMMA.

Differently from what was obtained in recent experiments using a top-gate architecture [14–16], the position of the resistance maximum as a function of  $V_{SG}$  does not depend on the value of  $V_{BG}$ . This indicates that back gate and split gates are independent, and no cross talk is observed between them. This is because the back-gate voltage is screened by the buried metallic split gate, which is inserted in between the back gate and the graphene layer. To a good approximation, in our devices the bulk carrier density is only controlled by  $V_{BG}$ , while the density in correspondence to the split gates is only controlled by  $V_{SG}$ . On the other hand, in top-gated devices the local carrier density in proximity of the split-gate structure is affected by both back-gate and top-gate voltages.

The occurrence of QH states in the bulk of the graphene flake can be inferred from the longitudinal resistance  $R_{xx}$  at a magnetic field B=10 T, shown in Fig. 3, where  $V_{BG}$  was swept from -20 V to +45 V while keeping  $V_{SG}=0$  V. QH

plateaus at filling factors  $\nu = -6, -2, +2$ , and +6 are observed in this  $V_{BG}$  range [23] and confirm that the flake is a monolayer and that the graphene quality is good.

The full evolution of the longitudinal resistance  $R_{xx}$  across the 400-nm-wide QPC as a function of both  $V_{SG}$  and  $V_{BG}$  is shown in Fig. 4(a): various fractionally quantized regions can be spotted in the color plot. As argued in the following, they can be understood in terms of carrier density configurations leading to three different filling factors: the bulk filling factor  $v_{BG}$  that is controlled by  $V_{BG}$ , the filling factor in correspondence to the split-gate fingers  $v_{SG}$  in turn controlled by  $V_{SG}$ , and the filling factor in the constriction region—or split-gate opening— $v_{QPC}$  that is driven by both gates.

In contrast to the results obtained with n-p-n junctions [14–16,19,20,24] or quantum point contacts (QPCs) defined by a top split gate [18], where the filling factor under the top gate depends both on the back-gate and top-gate voltage, in our geometry with the split gate buried in PMMA under the graphene ribbon, the filling factor over the split-gate  $\nu_{SG}$  is independent of the back-gate voltage. The split gate screens the potential of the back gate; hence the lines separating subsequent filling factors are vertical. In Fig. 4(a) the horizontal dashed lines indicate the threshold for subsequent filling factors  $\nu_{SG}$  in the bulk of the device, and the vertical ones, the filling factors  $\nu_{SG}$  over the split gate.

Whenever  $v_{SG}$  and  $v_{BG}$  are equal, the resistance is zero: charge transport through the device occurs without backscattering. Indeed, Fig. 4(a) shows four rectangular regions that show value zero and align in the diagonal, corresponding to  $v_{BG} = v_{SG}$ .

The values in the other regions are between 0 and 1 whenever these two filling factors take different values. This phenomenology was also observed in local top-gate [14–16] and top split-gate [18] devices. The resistance values are given by [18,24]

$$R_{xx} = \frac{h}{e^2} \frac{|\nu_{BG} - \nu_{SG}|}{|\nu_{BG}||\nu_{SG}|}.$$
 (1)

Note, however, that in the experimental results of Fig. 4, some of the rectangles are divided into two regions with different resistance values. We can explain this by noticing

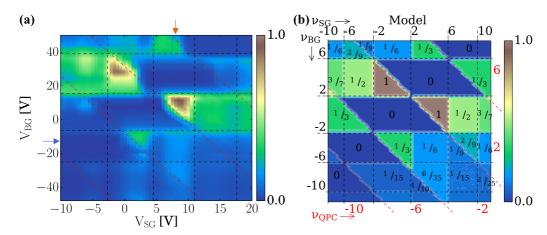


FIG. 4. (a) Longitudinal resistance  $R_{xx}$  (in units of  $h/e^2$ ) measured across the 400-nm-wide QPC as a function of the back-gate ( $V_{BG}$ ) and split-gate ( $V_{SG}$ ) voltage biases at B=10 T. (b) Analytical results obtained with Eqs. (3)–(6) reproduce well the observed resistance plateaus. The arrows in (a) indicate where the cross sections of Figs. 9(a) and 9(b) were taken.

that the filling factor  $v_{QPC}$  in the middle of the 400-nm-wide QPC may differ from  $v_{SG}$ . For the case  $v_{QPC} = v_{BG}$ , the QPC is open for all edge states coming from the source, and the resistance is zero. When  $v_{QPC} = v_{SG}$ , the device behaves like a unipolar or bipolar junction, and the resistance takes the values given by Eq. (1).

In the most general case, when all three filling factors have a different value, the resistance deviates from the value given by Eq. (1). We can see in Fig. 4(a) that in many rectangles there are two different plateaus whose values will be calculated in the following section. The red dashed lines in Fig. 4 indicate the transition between subsequent  $v_{QPC}$ : these lines are inclined, since the filling factor in the constriction opening depends on both back-gate and split-gate voltages. One can see that for a fixed set of the three filling factors the resistance is more or less constant. At the transition between subsequent  $v_{BG}$ , it rises because the Fermi energy is close to the Landau level, and instead of perfect edge states propagating in the device, we have a significant backscattering, giving rise to a higher resistance.

The possibility to achieve a  $v_{QPC} \neq v_{BG}$  is further supported by the fact that the above-cited deviations are only observed for narrow split gates while a different behavior occurs for instance in the case of the 800-nm-wide split-gate devices (see Supplemental Material [38]). Indeed, for widegap split gates, the fringe field of the finger electrodes is not

expected to be sufficiently strong to induce a filling factor different from  $v_{BG}$  in the middle of the constriction. This heuristic assumption will be better justified by the numerical simulations reported in Sec. V.

# IV. DISCUSSION

The experimental resistance pattern of Fig. 4(a), as well as the results of the Schrödinger-Poisson simulation reported in the next section, can be explained in a simple model based on current conservation and edge-mode equilibration. In the model we consider a graphene device including a back gate and a split gate which can induce regions with various filling factors:  $v_{BG}$  in the bulk,  $v_{SG}$  above the split-gate electrodes, and  $v_{QPC}$  in the QPC opening (see Fig. 5). In the experiment, longitudinal resistances are obtained in a four-wire scheme by measuring the longitudinal voltage drop  $V_{xx}$  in the presence of a current bias  $I_{SD}$  between the source to drain contacts. Four leads connected to the graphene device are thus included in the model in order to reproduce the experimental results. The resulting device geometry, including the contacts and the various local filling factors, is shown in Fig. 5.

For the calculation of resistances using the Landauer-Büttiker formalism, we calculate the values of conductance  $G_{pq}$  for the electron flow from terminal q to p. Next we build a G matrix [25]

$$G = \begin{pmatrix} G_{12} + G_{13} + G_{14} & -G_{12} & -G_{13} \\ -G_{21} & G_{21} + G_{23} + G_{24} & -G_{23} \\ -G_{31} & -G_{32} & G_{31} + G_{32} + G_{34} \end{pmatrix}$$
(2)

and invert it to obtain the matrix  $\mathbf{R} = \mathbf{G}^{-1}$ . The longitudinal resistance  $R_{12,34}$  is obtained with the current flowing from lead 1 into 2 with the voltage drop measured between leads 3 and 4, and equals  $R_{31} - R_{32}$  [25], where  $R_{ij}$  is the  $\mathbf{G}^{-1}$  matrix element of ith row and jth column. Due to the symmetry in our system,  $G_{24} = G_{31}$  and  $G_{34} = G_{21}$ . For B > 0 and  $v_{BG} > 0$ , all carriers from lead 3 flow into lead 1 with the Lorentz force keeping the current at the left edge of the sample with respect to the direction of the charge flow:  $G_{13} = v_{BG}e^2/h$ ,  $G_{23} = 0$ . We also assume that all carriers from lead 2 flow into lead 4, which gives  $G_{32} = 0$ ,  $G_{12} = 0$ . Finally,  $G_{14} = 0$ , as the carriers from lead 4 can never reach lead 1.

Depending on the relative chirality of the bulk, split-gate, and QPC regions, there are four regimes to be considered, as shown in Fig. 6. For the calculation of the conductance matrix elements, we proceed in a similar way as in Ref. [16] on n-p-n junctions, which however covered a simpler case with the edges only between pairs of regions of varied  $\nu$ . The numerical modeling (see the next section) indicates that here mode mixing needs to cover the border lines between the bulk and split-gate regions. In Ref. [19] for a top split-gate the anomalous Hall plateaus could be explained by the equilibration of the N=0 Landau level. The results for the present sample call for mixing of all the subbands.

In the first case [Fig. 6(a)] transmission of  $v_{QPC}$  edge modes of the incident  $v_{BG}$  modes occurs. Therefore, the resistance is the same as in a bipolar junction with filling factor  $v_{QPC}$  in the middle area, and it equals

$$R_{12,34} = \frac{h}{e^2} \frac{|\nu_{BG}| - |\nu_{QPC}|}{|\nu_{BG}||\nu_{QPC}|}.$$
 (3)

For the special case shown in Fig. 6(a), Eq. (3) gives  $R_{12,34} = 1/15$ .

In the second case [Fig. 6(b)], edge mode mixing occurs, and the current in the spots marked by purple rectangles is partitioned equally between available modes. The probabilities of the current going into one of the directions shown in Fig. 6(b)

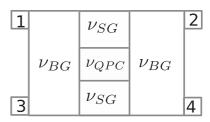


FIG. 5. Sketch of the device geometry adopted in the Landauer-Büttiker model. Four contacts and regions with three distinct filling factors  $v_{BG}$ ,  $v_{SG}$ , and  $v_{QPC}$  are assumed.

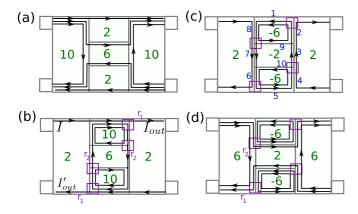


FIG. 6. Edge connectivities as a function of the three filling factors  $v_{BG}$ ,  $v_{SG}$ , and  $v_{QPC}$ . Each sketch implies a different edge propagation, splitting, and equilibration configuration and thus a different analytical formula for the longitudinal resistivity. Configurations with opposite chiralities are not reported explicitly. Black arrows indicate the currents for each of the spin-degenerate available transport modes and their chirality. In order to provide a specific example, the green numbers indicate one of the possible filling factor combinations giving rise to the sketched connectivity scheme. In panels (b), (c), and (d), edge equilibration in co-propagating modes plays a crucial role: the purple squares indicate the points where current branches. In the model current is assumed to partition equally on all available channels; i.e., edge modes are assumed to completely equilibrate before the branching.

are  $r_1 = \frac{|\nu_{BG}|}{|\nu_{SG}|}$  and  $r_2 = \frac{|\nu_{QPC}| - |\nu_{BG}|}{|\nu_{SG}| - |\nu_{BG}|}$ . We label the current at each side of the split gate by  $1,2,\ldots,10$  as shown in Fig. 6(c). The currents satisfy the following relations:  $I_2 = (1-r_1)I_1$ ,  $I_3 = r_2I_2$ ,  $I_9 = (1-r_2)I_2$ ,  $I_6 = (1-r_1)I_5$ ,  $I_7 = r_2I_6$ , and  $I_{10} = (1-r_2)I_6$ . Assuming that a current I flows in the first lead, we find the following current conservation rules:  $I+I_8 = I_1$ ,  $I_7+I_9 = I_8$ ,  $I_3+I_{10} = I_4$ , and  $I_4 = I_5$ . We construct a system of 10 equations and find the outflowing currents  $I_{\text{out}} = I_1 - I_2$  and  $I'_{\text{out}} = I_5 - I_6$ . Then, the conductances are  $G_{21} = \frac{2e^2}{h} |\nu_{BG}| I_{\text{out}}/I$  and  $G_{31} = \frac{2e^2}{h} |\nu_{BG}| I'_{\text{out}}$ . Inserting this information into the G matrix and inverting it, we find

$$R_{12,34} = \frac{h}{e^2} \frac{(|\nu_{QPC}| - |\nu_{BG}|)}{|\nu_{BG}||\nu_{QPC}|},\tag{4}$$

which is equivalent to a unipolar junction with a filling factor satisfying  $|\nu_{QPC}| > |\nu_{BG}|$  in the middle area. For the special case shown in Fig. 6(b), Eq. (4) yields  $\frac{1}{3}$ .

The remaining two cases are shown in Figs. 6(c) and 6(d). The resistances are (for the calculation see Supplemental Material [38])

$$R_{12,34} = \frac{h}{e^2} \frac{(|v_{SG}| + |v_{BG}|)(|v_{QPC}| + |v_{BG}|)}{|v_{BG}|[(|v_{SG}| - |v_{BG}|)|v_{QPC}| + 2|v_{BG}||v_{SG}|]}$$
(5)

for the case presented in Fig. 6(c), and

$$R_{12,34} = \frac{h}{e^2} \frac{(|\nu_{SG}| + |\nu_{BG}|)(|\nu_{QPC}| - |\nu_{BG}|)}{|\nu_{BG}|[(|\nu_{SG}| - |\nu_{BG}|)|\nu_{QPC}| - 2|\nu_{BG}||\nu_{SG}|]}$$
(6)

for the case shown in Fig. 6(d).

Summarizing, the resistance is given by Eqs. (3)–(6) for the four configurations of the filling factors shown in Fig. 6. Note that always  $|v_{QPC}| \le |v_{SG}|$ . Figure 4(b) shows a schematic plot with the calculated filling factors in each part of the device. There is a good agreement observed with the experimental data of Fig. 4(a).

## V. SCHRÖDINGER-POISSON MODEL

The previous section described a simple model based on the Landauer-Büttiker formalism that can explain the observed resistance pattern, provided one makes the assumption of three independent filling factors. In this section, we shall show that such an assumption can indeed be directly derived from a Schrödinger-Poisson model of the device [26,27]. Based on the resulting potential landscape, the quantum transport problem is solved numerically. The calculation is performed using a wave-function matching method and yields the actual current paths and  $G_{pq}$  values from the scattering wave functions at the Fermi level. These parameters are then used to calculate the longitudinal resistance.

# A. Description of the model

The numerical calculation is performed on a graphene region with zigzag horizontal and armchair vertical edges. The constriction region has a width of 197.2 nm and a length of 443.2 nm. In the simulation, the leads are 24.6 nm long and 17.4 nm wide, which corresponds to a width of 40 atoms across the ribbon. This gives a total length of the device with contacts of 492.4 nm. For the simulation we use the scaling approach of Ref. [28], with a scaling factor  $s_f$ . The qualitative results of the model do not change when  $s_f$  is increased beyond 4 and only depend on the configuration of the filling factor regions. Simulations were thus performed with  $s_f = 4$ . The simulated system is thus smaller than the real one: this is compensated by performing calculation with (i) smaller voltages in order to induce the same electric field values as in the larger device (see discussion below) and (ii) larger values of the external magnetic field that drive the same number of available edge channels.

In order to evaluate the electrostatic potential energy experienced by the electron gas within the graphene device, we solved the Poisson equation for the considered geometry with the electron density given by the Thomas-Fermi approximation [29] and found that the influence of the space charge for the profile of the potential landscape is negligible. The potential is thus determined by the Laplace equation for the system of electrodes given in Fig. 7. We consider a computational box of  $492.4 \times 197.2 \times 246 \text{ nm}^3$  in the  $x \times y \times z$  direction, respectively, where the size in x includes the scattering region and the left and right leads. The back gate and the split gate are placed at z = 0 and z = 27.6 nm, respectively, and the nanoribbon at z = 37.4 nm. The split gate (size of each gate finger 165.2 nm in x and 68.1 nm in y) is placed in the middle of the ribbon's length, with a spacing between the gate fingers of 61 nm. At each electrostatic gate we use Dirichlet boundary conditions for applied potentials and at the side and top walls of the computational box Neumann boundary conditions with zero electric field, which at the top side of the box is justified by

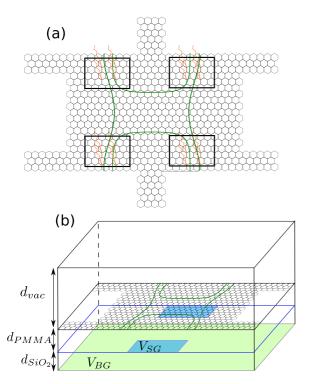


FIG. 7. Schematic drawing of the studied system. (a) Scheme of the nanoribbon with four horizontal zigzag leads, two vertical armchair Büttiker probes, and four Büttiker probes connected to the interior of the nanoribbon near the n-p junctions, highlighted in orange. Green lines show schematic isolines of the potential. (b) The computational box for the Laplace problem. Voltage  $V_{BG}$  is applied to the back gate (bottom of the computational box), and  $V_{SG}$  to the split gate colored in blue. The dimensions used for the simulation are  $d_{SiO_2} = 27.6$  nm,  $d_{PMMA} = 9.8$  nm,  $d_{vac} = 208.6$  nm.

the charge neutrality of the system, and at the lateral sides by the symmetry of the system far from the split gate. The value of  $d_{\rm vac}$  is chosen so that the potential within the graphene layer does not change any further upon increasing the box height.

For the calculations, we use the tight-binding Hamiltonian

$$H = \sum_{\{i,j\}} (t_{ij}c_i^{\dagger}c_j + \text{H.c.}) + \sum_i V(\mathbf{r}_i)c_i^{\dagger}c_i, \tag{7}$$

where  $V(\mathbf{r}_i)$  is the external potential at  $\mathbf{r}_i$ , the position of the *i*th atom, and the first summation runs over the nearest neighbors. The magnetic field is taken into account by Peierl's substitution in the hopping parameter,

$$t_{ij} = t \exp\left(\frac{2\pi i}{\phi_0}\right) \int_{\mathbf{r}_i}^{\mathbf{r}_j} \mathbf{A} \cdot \mathbf{dl},\tag{8}$$

where t is the hopping parameter and  $\phi_0 = \frac{h}{e}$  the flux quantum. For a magnetic field perpendicular to the graphene plane  $\mathbf{B} = (0,0,B)$ , we use a Landau gauge  $\mathbf{A} = (-yB,0,0)$ . As already mentioned, a scaling approach is used [28] with scaling condition  $a = a_0 s_f$  and  $t = t_0/s_f$ , where the scaling factor is  $s_f = 4$ ,  $t_0 = -2.7$  eV is the unscaled hopping parameter, while  $a_0 = 2.46$  Å is the graphene lattice constant. The rescaled magnetic field is  $B = B_0 s_f^2$ , with  $B_0$  being the magnetic field characterizing the real sample. The ratio of the ribbon width l to magnetic length  $l_B$  equals  $l/l_B = 56.76$ , with magnetic length

 $l_B = \sqrt{\frac{\hbar}{eB_0}} = 26 \text{ nm}/\sqrt{B_0[\text{T}]}$ . Zero temperature is assumed. We determine the filling factor from the number of Landau levels below the Fermi energy.

To solve the scattering problem, we use wave function matching (WFM). The details of the computational method are described in Ref. [30]. The transmission probability from terminal l to mode m in terminal k is

$$T_{kl}^{m} = \sum_{n} \left| t_{mn}^{kl} \right|^{2}, \tag{9}$$

with  $t_{mn}^{kl}$  being the probability amplitude for the transmission from the mode n in terminal l to mode m in the terminal k.

Figure 5 shows the labeling of the leads in the model system. To compute  $R_{12,34}$ , we construct a conductance matrix **G** of dimension N-1, where N is the number of terminals, and calculate it as a sum over the modes:

$$G_{kl} = G_0 \sum_{m} T_{kl}^m, \tag{10}$$

where  $G_0 = \frac{2e^2}{h}$  is the conductance quantum. Then, we use the following formula to relate the current  $I_k$  in terminal k to the voltages in all terminals:

$$I_k = \sum_{l} G_{kl}(V_k - V_l),$$
 (11)

where  $V_k$  and  $V_l$  are voltages in terminals k and l, respectively. The resistance calculation proceeds as explained in Sec. IV.

In view of a comparison between numerical results and actual experimental data, it is crucial to comment on the magnitudes of the gate voltages in relation with the scaling method adopted in the model. Indeed transport properties are simulated in a *scaled* atomistic model. As a consequence, gates were assumed to be closer to the graphene plane than in the actual devices, which reduces the number of mesh elements in the finite difference solver. In particular, in the simulation the dimensions of the system are approximately 10 times smaller than in the experiment. The gate-voltage-to-energy conversion factor is inversely proportional to the distance between the electron confinement area and the gates. Hence, because of the reduced distance between the gates and the graphene, the gate voltages inducing a specific filling factor distribution are accordingly smaller.

## **B.** Numerical results

When transport is fully coherent, no equilibration occurs between co-propagating edge modes and the model cannot correctly reproduce the observed behavior (see Supplemental Material [38]). In order to induce partitioning of the current, dephasing Büttiker virtual probes were introduced, as visible in Fig. 7(a). These are used as voltage probes; i.e., a zero net current flow is assumed at each of the probes. Electrons entering the probe equilibrate in the reservoir, and emerge from it with a different phase. The choice of the positions of such artificial probes is crucial for edge-mode mixing. According to the theoretical models [14,16,20], equilibration in the bipolar junction takes place along the n-p interface, and in the case of a unipolar junction, shown in Fig. 6(b), along the edge in the central, split-gate region. For the latter case, the addition

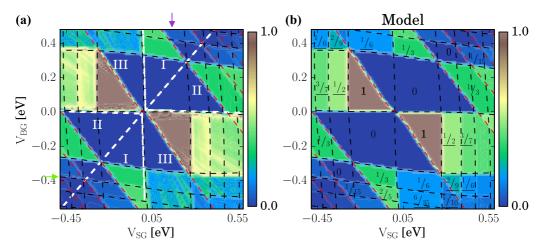


FIG. 8. (a) Simulated longitudinal resistance (in units of  $h/e^2$ ) as a function of  $V_{BG}$  and  $V_{SG}$ . (b) Resistance values calculated according to Eqs. (3)–(6) (in units of  $h/e^2$ ). The nonlinear shape of the boundaries of regions at fixed  $v_{BG}$  is caused by the limited size of the computational box which cause the potential in the leads to slightly depend on  $V_{SG}$ .

of voltage probes connected to the region over the split-gate results in resistance values that are in very good quantitative agreement with the values given by Eq. (4). For the former case, no configuration of the probes in the plane of the graphene nanoribbon gives values close to what is predicted by Eqs. (5) or (6). However, we obtained results that agree with experiment for the probes connected to the interior of the nanoribbon near the n-p junctions [31,32], as shown in Fig. 7(a). The position and length of the probes is set so that most of the currents flowing along the junction can reach the probes.

The final position and size of the probes is discussed in the following. Two probes are armchair nanoribbons of 35 atoms width (i.e., they approximately have the same width as the leads) connected to the region over the split gate, and are assumed to be semi-infinite in the y direction. Further four probes are semi-infinite in the z direction and attached to the ribbon in the vicinity of the split gate, marked in orange in Fig. 7(a) and consisting of 60 zigzag chains attached to the graphene plane within four areas marked schematically by black rectangles in Fig. 7(a) (see Supplemental Material [38] for further details).

We calculate  $R_{12,34}$  as a function of potentials  $V_{BG}$  and  $V_{SG}$ at the back gate and split gate, respectively (see Fig. 7). The calculated resistance is presented in Fig. 8(a). The horizontal dashed lines indicate the transition between subsequent filling factors  $v_{BG}$  in the bulk of the nanoribbon, the vertical ones between filling factors  $v_{SG}$  in the split gate, and the red dashed lines between filling factors  $v_{OPC}$  in the middle of the QPC. Following the nomenclature of Ref. [16], the plot is divided into sectors, depending on the relationship between the filling factors. In the sectors between dashed white lines labeled with I, where the filling factors have the same sign  $v_{BG}v_{SG} > 0$ and  $v_{BG} \ge v_{SG}$ , the device is in the edge state transmission regime. In the sectors labeled II, partial equilibration occurs. As shown in Sec. IV, in both aforementioned regimes, the device behaves like a unipolar junction with the resistance governed by  $v_{OPC}$ . In the most unique case, in sectors III, it is governed by the full equilibration process. Whereas in sectors I and II the resistance is independent of  $v_{SG}$ , in sector III it depends on all three filling factors. This is consistent with what was observed in the experimental data [see Fig. 4(a)].

The corresponding model values obtained with the Landauer-Büttiker formalism discussed in Sec. IV are shown in the 2D plot in Fig. 8(b). The underlined numbers are the values for  $v_{BG} \neq v_{SG} \neq v_{QPC}$  in the full equilibration regime. There is a perfect agreement between the two plots in Figs. 8(a) and 8(b) for region I, which would be the case even without any Büttiker probes. In the edge-state transmission regime, no equilibration occurs, and only  $v_{OPC}$  modes out of incoming  $v_{BG}$  modes can pass the split gate; therefore no phase randomizing is needed to obtain the expected resistance. In regime II the resistance also coincides with the model values, as the high magnetic field used for the simulation forces all the incoming electrons to the side probes, where they equilibrate. In sector III, where equilibration occurs along the BG-SG interface, the agreement with the model data is good for  $v_{OPC} = v_{SG}$ , but the simulated values for  $v_{OPC} \neq v_{SG}$  exceed slightly the model ones. The reason is that the magnetic field perpendicular to the plane of the ribbon does not push the electrons into the probes in z, so that not all electrons enter the probes and a small fraction of electrons does not equilibrate. For  $v_{OPC} = v_{SG}$ , for which the edge-state partitioning takes place at the point where the junction interface meets the edge, this does not yield a large deviation from the model resistance values. However, in the more complex case of  $v_{OPC} \neq v_{SG}$ , the random partitioning is expected to take place also between the SG and QPC regions, the effectiveness of which is somewhat smaller in the simulation.

In order to underline the excellent agreement between experiment, model, and simulation more thoroughly, we plot cross sections of the longitudinal resistance. In Figs. 9(a) and 9(b) cross sections of the experimental data are plotted with orange and blue lines for the  $V_{SG}$  or  $V_{BG}$  values indicated by arrows of the same color in Fig. 4(a). The dashed line shows the value of resistance resulting from the model. The resistance reaches plateaus or at least gets close to the expected value for almost every set of  $v_{BG}, v_{SG}, v_{QPC}$  in the cross sections. A particularly good agreement for  $V_{SG} = 8$  V is seen for the

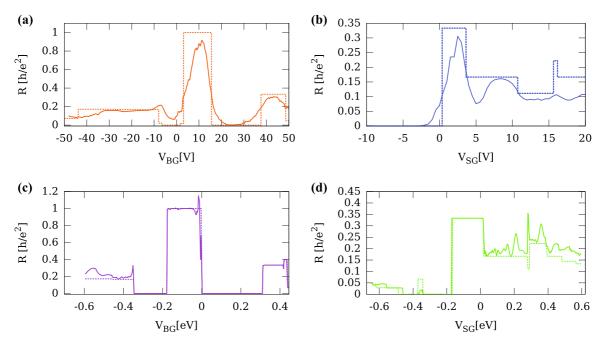


FIG. 9. The cross sections of the experimental [(a), (b)] and the simulated [(c), (d)] data, indicated by arrows in Figs. 4 and 8. The color of the arrow corresponds to the line color. Dashed lines show the model values of resistance given by Eqs. (3)–(6). (a)  $v_{SG} = 2$ ,  $V_{SG} = 8$  V, (b)  $v_{BG} = -6$ ,  $V_{BG} = -13$  V, (c)  $v_{SG} = 2$ ,  $V_{SG} = 0.135$  eV, (d)  $v_{BG} = -6$ ,  $V_{BG} = -0.39$  eV.

plateaus  $\frac{1}{10}$ ,  $\frac{6}{35}$ ,  $\frac{1}{6}$ , i.e., with  $v_{SG} \neq v_{BG} \neq v_{QPC}$ . For the cross section at  $V_{BG} = -13$  V, the resistance gets close to the model values  $\frac{1}{3}$ ,  $\frac{1}{6}$ , and  $\frac{1}{9}$ , but it does not reach the plateau  $\frac{2}{9}$  for  $v_{SG} = 6$ ,  $v_{BG} = -2$ , and  $v_{QPC} = 2$ , and the plateau  $\frac{1}{6}$  for  $v_{SG} = 10$ ,  $v_{BG} = -6$ , and  $v_{QPC} = 2$ . A too small resistance is obtained for almost every case in the partial equilibration regime.

In Figs. 9(c) and 9(d), cross sections of the simulated data are presented for  $V_{SG}$  indicated by arrows in Fig. 8(a) with the same color as the plot line. The agreement with the model is very good, and only in the case of three different filling factors in the full-equilibration regime, there is a small discrepancy. However, even in this case the curve follows the model line. The shape of the simulated and measured curves clearly is similar, one difference being the energies at which subsequent bands enter the transport, and the slope of the red dashed contours in Fig. 4, which gives rise to the occurrence of different sets of the three filling factors.

#### VI. SUMMARY AND CONCLUSIONS

We have analyzed the behavior of buried split-gate graphene devices which are suitable for scanning-probe-microscopy experiments. Differently from studied experimental configurations [18], the measurements of the longitudinal resistance taken at B=0 indicated a screening of the back gate by the split gate within the QPC areas: this implies that no cross talk between the two is observed. Moreover, the measurements taken in the QH regime as well as the numerical simulation yield a resistance pattern indicating the clear presence of regions with three distinct filling factors. The resistance pattern can be explained with the mode equilibration of

the edge currents that involves all three regions of varied filling factors and all modes participating in the current flow. The demonstration of a buried gate geometry accompanied by its detailed examination by magnetotransport will enable further progress in scanning probe (STM, SKPM, SNOM, scanning gate, etc.) experiments aiming to gain understanding of quantum transport in 2D materials. In particular, we are now working on scanning gate microscopy experiments of these devices in order to study details of the edge channel transport characteristic of the QH regime, similar to our previous studies on III-V materials [33–37].

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