

## Conducting channel at the LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interface

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Localization of electrons in the two-dimensional electron gas at the LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interface is investigated by varying the channel thickness in order to establish the nature of the conducting channel. Layers of SrTiO<sub>3</sub> were grown on NdGaO<sub>3</sub> (110) substrates and capped with LaAlO<sub>3</sub>. When the SrTiO<sub>3</sub> thickness is  $\leq 6$  unit cells, most electrons at the interface are localized, but when the number of SrTiO<sub>3</sub> layers is 8–16, the free carrier density approaches  $3.3 \times 10^{14} \text{ cm}^{-2}$ , the value corresponding to charge transfer of 0.5 electrons per unit cell at the interface. The number of delocalized electrons decreases again when the SrTiO<sub>3</sub> thickness is  $\geq 20$  unit cells. The  $\sim 4$  nm conducting channel is therefore located significantly below the interface. The results are explained in terms of Anderson localization and the position of the mobility edge with respect to the Fermi level.

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The two-dimensional electron gas (2DEG) at the interface between the band insulators LaAlO<sub>3</sub> and SrTiO<sub>3</sub> (Ref. 1) continues to stimulate the interest of condensed matter researchers. It exhibits a variety of unexpected properties such as superconductivity,<sup>2</sup> magnetism,<sup>3</sup> spin-orbital coupling,<sup>4</sup> and electronic phase separation.<sup>5–9</sup> Recent observation of high mobility at low temperatures ( $> 5 \times 10^4 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ )<sup>10</sup> and the fabrication of millions of transistors on a single chip<sup>11</sup> have highlighted the importance of these oxide interfaces both from fundamental and applied perspectives. The 2DEG is thought to result from an electron transfer to the interface between the polar oxide (LaAlO<sub>3</sub>) and the nonpolar oxide (SrTiO<sub>3</sub>), which is necessary to avoid a divergence of the energy associated with the electric field.<sup>12</sup> A charge transfer of 0.5 electrons per interface unit cell (uc) or  $3.3 \times 10^{14} \text{ cm}^{-2}$  should be required to compensate the electric field in polar LaAlO<sub>3</sub> and avert a polar catastrophe.<sup>12</sup> However, a major puzzle is that the experimentally observed carrier densities at low temperatures for the 2DEG in fully oxidized samples<sup>3,5,13</sup> are an order of magnitude lower than expected. Furthermore, it is unclear where exactly at the interface the conduction electrons are located, since the LaAlO<sub>3</sub> is usually grown on a SrTiO<sub>3</sub> substrate. One proposed explanation is that these “disappearing” electrons are localized within the first SrTiO<sub>3</sub> layers that are closest to the interface, where Ti  $3d_{xy}$  subbands have lower energy than the other Ti  $3d$  orbitals.<sup>14–16</sup> According to the theoretical calculations, the mobile electrons responsible for the transport properties of the 2DEG are farther away ( $\geq 4$  uc) from the interface.<sup>15</sup> While experimental results<sup>12, 17–19</sup> have shown that the 2DEG can penetrate some distance from the interface into the SrTiO<sub>3</sub> layer, the exact location of the conduction electrons has not been determined. Since the conventional way to fabricate LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interfaces is to grow LaAlO<sub>3</sub> layers on SrTiO<sub>3</sub> substrates, it had not been possible to determine the location of the conducting channel.

A drawback with conventional LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interfaces<sup>1–13</sup> is that losses in the SrTiO<sub>3</sub> substrate limit the potential application of the 2DEG in high frequency devices.<sup>20</sup> In order to overcome this limitation and expand the applicability of 2DEG, LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interfaces have been fabricated on other substrates such as silicon,<sup>21</sup> NdGaO<sub>3</sub> (110),<sup>22</sup> (LaAlO<sub>3</sub>)<sub>0.3</sub>(Sr<sub>2</sub>AlTaO<sub>6</sub>)<sub>0.7</sub> (001),<sup>22–24</sup> and DyScO<sub>3</sub> (110).<sup>22</sup> Such an approach may permit a demonstration of the novel *topological superconductivity* recently predicted to appear in two-layer interacting Rashba systems, which might be fabricated by growing LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interfaces on LaAlO<sub>3</sub> substrates.<sup>25</sup> The main aim of the present Rapid Communication is to understand how the physical properties of the 2DEG vary with the thickness of the SrTiO<sub>3</sub> layer at the interface, and to study the transport properties of the electrons involved.

We have grown a LaAlO<sub>3</sub>/SrTiO<sub>3</sub> heterostructure on NdGaO<sub>3</sub> (110) substrates. The crystal structure of NdGaO<sub>3</sub> is indexed on an orthorhombic ( $\sqrt{2}a_0, \sqrt{2}a_0, 2a_0$ ) type cell with  $a = 5.433 \text{ \AA}$ ,  $b = 5.503 \text{ \AA}$ , and  $c = 7.716 \text{ \AA}$ . When indexed on the pseudocubic cell ( $a_0, a_0, a_0$ ), the in-plane lattice constants  $a_0$  for the pseudocubic lattices of SrTiO<sub>3</sub> (100), NdGaO<sub>3</sub> (110), and LaAlO<sub>3</sub> (100) are 3.905, 3.858, and 3.790  $\text{\AA}$ , respectively. It is therefore possible to grow epitaxial LaAlO<sub>3</sub>/SrTiO<sub>3</sub> (100) interfaces on NdGaO<sub>3</sub> (110) substrates. Ideally, a LaAlO<sub>3</sub> (100) substrate would be the best candidate due to its capacity to reduce the lattice-mismatch-induced strain at the interface, and its low loss tangent at high frequencies.<sup>20,26</sup> However, problems of crystal twinning<sup>27</sup> and unstable surface termination<sup>28</sup> in LaAlO<sub>3</sub> set a very stringent limit, as for using a LaAlO<sub>3</sub>/SrTiO<sub>3</sub>/LaAlO<sub>3</sub> structure for the observation of topological superconductivity,<sup>25</sup> and this leads us to choose NdGaO<sub>3</sub> (110) instead. At a conventional LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interface, the lattice mismatch between LaAlO<sub>3</sub> and SrTiO<sub>3</sub> is 3%, and it is responsible for the interface strain. However, when the SrTiO<sub>3</sub> layer

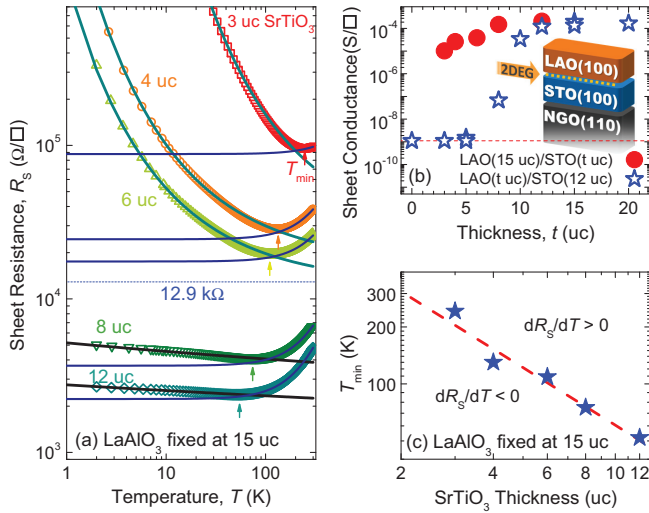


FIG. 1. (Color online) (a)  $R_S$ - $T$  curves for LaAlO<sub>3</sub>/SrTiO<sub>3</sub>/NdGaO<sub>3</sub> heterostructures with fixed LaAlO<sub>3</sub> thickness (15 uc) and different SrTiO<sub>3</sub> thicknesses (from 3 to 12 uc). The blue, dark cyan, and black lines are fits using electron-electron scattering, modified VRH with a two-dimensional Coulomb gap, and weak localization models, respectively. The arrows indicate the upturn temperatures  $T_{\min}$ . (b) Room-temperature sheet conductivity as a function of LaAlO<sub>3</sub> thickness for a fixed SrTiO<sub>3</sub> thickness of 12 uc, and also as a function of SrTiO<sub>3</sub> thickness for a fixed LaAlO<sub>3</sub> thickness of 15 uc. The red dashed line is the measurement limitation. The inset is the schematic view for layer structures in a LaAlO<sub>3</sub>/SrTiO<sub>3</sub>/NdGaO<sub>3</sub> [LAO(100)/STO(100)/NGO(110)] heterostructure, in which the 2DEG exists at the LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interface. (c) “Metal-insulator” phase diagram of LaAlO<sub>3</sub>/SrTiO<sub>3</sub>/NdGaO<sub>3</sub> heterostructure vs SrTiO<sub>3</sub> thickness with a fixed LaAlO<sub>3</sub> thickness of 15 uc. The slope of the red dashed line is  $-1$ .

is grown on NdGaO<sub>3</sub> (110), the large mismatch between LaAlO<sub>3</sub> and SrTiO<sub>3</sub> will be partially transferred from the LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interface to the SrTiO<sub>3</sub>/NdGaO<sub>3</sub> interface. To investigate the depth dependence of the 2DEG, we varied the SrTiO<sub>3</sub> thickness  $t$  from 3 to 25 uc during the fabrication of LaAlO<sub>3</sub>/SrTiO<sub>3</sub>/NdGaO<sub>3</sub> heterostructures, which were capped with 15 uc of LaAlO<sub>3</sub> (Fig. S1 in the Supplemental Material).<sup>29</sup> Before deposition, the NdGaO<sub>3</sub> substrates were annealed at 1050 °C in air for 2.5 h to obtain the atomically flat  $B$ -site terminated surfaces.<sup>30</sup> The growth parameters for both SrTiO<sub>3</sub> and LaAlO<sub>3</sub> layers are as follows: 1.8 J/cm<sup>2</sup> for laser energy, 760 °C for temperature, and  $2 \times 10^{-4}$  Torr for oxygen partial pressure during the deposition.

The results on temperature-dependent sheet resistance ( $R_S$ - $T$ ) are shown in Fig. 1(a). Although at high temperatures the resistance of all the LaAlO<sub>3</sub>/SrTiO<sub>3</sub>/NdGaO<sub>3</sub> samples is dominated by electron-electron scattering with an  $R_S \propto T^2$ , an upturn of sheet resistance invariably occurs below a temperature  $T_{\min}$  (where  $R_S$  is minimum). This upturn in  $R_S$ - $T$  has been also reported at LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interfaces grown on other substrates,<sup>21,23,24</sup> and it depends on the stoichiometry of LaAlO<sub>3</sub>.<sup>31</sup> Reducing the thickness of the SrTiO<sub>3</sub> layer raises  $T_{\min}$  and  $R_S$  at the same time. For the samples with 3, 4, and 6 uc SrTiO<sub>3</sub> layers, the  $R_S$  at 2 K is far above the quantum of resistance (12.9 k $\Omega$ , including spin

degeneracy). The  $R_S$ - $T$  curves for these samples diverge as the temperature is decreased, and they can be well fitted to  $R_S \propto \exp[(T_0/T)^{1/2}]$ , which suggests carrier localization in these heterostructures and modified variable-range hopping (VRH) with a soft two-dimensional Coulomb gap.<sup>32</sup> For comparison, the samples with a thicker SrTiO<sub>3</sub> (8 and 12 uc) have  $R_S$  less than half of 12.9 k $\Omega$ , and the  $R_S$ - $T$  curves are of the form  $1/R_S \propto A + B \ln T$  ( $A$  and  $B$  are constants) below  $T_{\min}$ , suggesting weak localization in two dimensions.<sup>33</sup> Given our experimental result that the SrTiO<sub>3</sub>/NdGaO<sub>3</sub> heterostructures prepared in  $10^{-4}$  Torr are insulating ( $R_S > 10^7 \Omega$ ), the conducting behavior ( $dR_S/dT > 0$  and only weak localization at low temperatures) seen in Fig. 1(a) must be due to the electrons at the LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interface.

This insulating SrTiO<sub>3</sub>/NdGaO<sub>3</sub> interface is different from the conducting one, which is found when NdGaO<sub>3</sub> is grown on a SrTiO<sub>3</sub> substrate.<sup>34</sup> In the former case, there is no observable conductivity, similar to the insulating interface, where the SrTiO<sub>3</sub> layers are grown on a LaAlO<sub>3</sub> substrate.<sup>35</sup> This can be ascribed to the loss of polar discontinuity at the interface. When NdGaO<sub>3</sub> or LaAlO<sub>3</sub> is not a freshly deposited polar layer, but the substrate itself which has been exposed to the ambient atmosphere, the surface charge is compensated by some external charge centers, as the surface of the NdGaO<sub>3</sub> or LaAlO<sub>3</sub> substrate becomes neutral. Hence, the polar discontinuity cannot be easily established at the interface when polar oxides are used as substrates. Similar results to those in Fig. 1(a) are observed when the NdGaO<sub>3</sub> (110) substrates are replaced by (LaAlO<sub>3</sub>)<sub>0.3</sub>(Sr<sub>2</sub>AlTaO<sub>6</sub>)<sub>0.7</sub> (001), which shows that a band-gap mismatch with the substrate is not a critical factor.

Moreover, the conducting behavior of the LaAlO<sub>3</sub>/SrTiO<sub>3</sub>/NdGaO<sub>3</sub> samples also depends greatly on the LaAlO<sub>3</sub> thickness, as shown in Fig. 1(b), where it is seen that 10–12 uc of LaAlO<sub>3</sub> are needed to make 12 uc of SrTiO<sub>3</sub> conducting. But this critical thickness of LaAlO<sub>3</sub> is larger than 4 uc that are commonly observed at conventional LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interfaces. Given that a higher critical thickness for the LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interfaces grown on other substrates is also observed by Bark *et al.*, who found that with 50 uc SrTiO<sub>3</sub> the critical LaAlO<sub>3</sub> thickness is around 15 uc,<sup>22</sup> this result could imply an important role of the strain in this phenomenon. Figure 1(c) shows the variation of  $T_{\min}$  with SrTiO<sub>3</sub> thickness  $t$ . The temperature  $T_{\min}$  separates the regions with  $dR_S/dT > 0$  for the higher temperatures and thicker SrTiO<sub>3</sub> layers, and  $dR_S/dT < 0$  on the opposite side. The dependence of  $T_{\min}$  on SrTiO<sub>3</sub> thickness can be described by  $T_{\min} \sim 1/t$ , which is consistent with a temperature-dependent mean free path or relaxation time signifying small energy transfer scattering in the 2DEG.<sup>36–38</sup>

The temperature dependence of sheet carrier density ( $n_S$ ) and mobility ( $\mu_H$ ) are plotted in Figs. 2(a) and 2(b), respectively. Compared to conventional LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interfaces grown on SrTiO<sub>3</sub> substrates,<sup>3,5</sup> the interfaces grown epitaxially on NdGaO<sub>3</sub> with an 8–16 uc SrTiO<sub>3</sub> layer show an  $n_S$  independent of temperature below 100 K that is one order of magnitude larger, and close to the value of  $3.3 \times 10^{14}$  cm<sup>-2</sup>, which corresponds to 0.5 electrons per unit cell at the interface. It can be argued that the reason for the temperature independence is the clamping effect of the NdGaO<sub>3</sub> substrate,

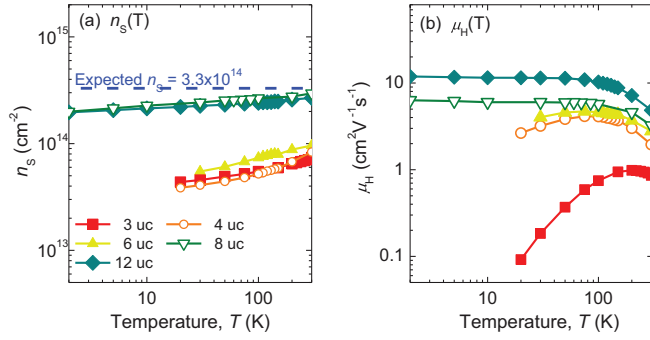


FIG. 2. (Color online) (a) The carrier density  $n_s$  and (b) mobility  $\mu_H$  as a function of temperature for samples with different SrTiO<sub>3</sub> thicknesses, from 3 to 12 uc, keeping the LaAlO<sub>3</sub> thickness at 15 uc.

which prevents the SrTiO<sub>3</sub> layer from undergoing the structural transitions that occur at low temperatures,<sup>5,39</sup> thus avoiding the strong localization of carriers at low temperatures, which is widely observed in the 2DEG on SrTiO<sub>3</sub> substrates. This result supports the view that the SrTiO<sub>3</sub> phase transitions are important for determining the low temperature 2DEG properties, carrier localization in particular.<sup>40</sup> Also the absence of temperature dependence in mobility when the free carrier concentration is high suggests that electron-electron scattering is dominant. The main point here is that these characteristics, the temperature-independent value of  $n_s$  and the high density of free carriers approaching the ideal value predicted by the polar catastrophe model, are observed only when the number of SrTiO<sub>3</sub> monolayers is  $\geq 8$ . This implies that the formation of a mobile 2DEG requires at least 8 uc ( $\sim 3$  nm) of SrTiO<sub>3</sub> for the conducting channel, which should be regarded as the minimal propagating depth for the 2DEG and is consistent with previous experiments performed on conventional LaAlO<sub>3</sub>/SrTiO<sub>3</sub> heterostructures,<sup>12,17</sup> but this time the opposite limit, i.e., carrier localization induced in SrTiO<sub>3</sub> layers  $\leq 6$  uc thick, has been directly observed.

In order to illustrate further the effect of SrTiO<sub>3</sub> thickness on the 2DEG, room-temperature  $n_s$  and  $\mu_H$  as a function of SrTiO<sub>3</sub> thickness  $t$  are plotted in Figs. 3(a) and 3(b). The abrupt enhancement of  $n_s$  from  $0.9 \times 10^{14}$  to  $2.9 \times 10^{14}$  cm<sup>-2</sup> is observed in a very small window of SrTiO<sub>3</sub> thicknesses, i.e., from 6 to 8 uc. The value of  $n_s$  seems to saturate for  $t = 8$ –16 uc, and then falls to  $0.8 \times 10^{14}$  and  $0.6 \times 10^{14}$  cm<sup>-2</sup> at 20 and 25 uc. This is also consistent with the results of Bark *et al.*, who observed a low value of  $n_s = 0.5 \times 10^{14}$  cm<sup>-2</sup> at 50 uc.<sup>22</sup> On the other hand, in Fig. 3(b), a linear increase of room-temperature  $\mu_H$  with SrTiO<sub>3</sub> thickness is observed from 3 to 12 uc, which proves that the SrTiO<sub>3</sub> layer is truly the conducting channel for the 2DEG. On further increasing the SrTiO<sub>3</sub> thickness to 16 and 25 uc, a nearly constant  $\mu_H$  (close to that for the conventional LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interface at room temperature) is attained. According to the above data, the SrTiO<sub>3</sub> thickness required for a mobile 2DEG is around 8–16 uc.

A rough estimate of the width  $t$  of the 2DEG at the interface can be obtained by considering its energy per unit area [Fig. 3(c), and Fig. S9 in the Supplemental Material].<sup>41</sup> Generally, the electrons can lower their energy by spreading out deeper into the SrTiO<sub>3</sub>, provided the states are available.

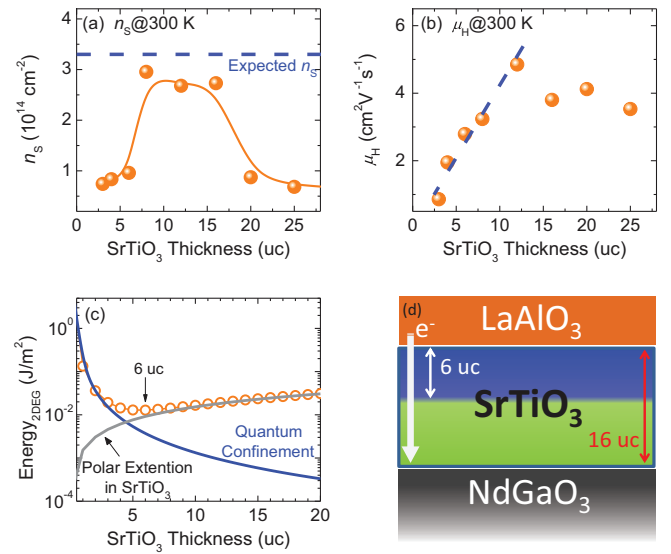


FIG. 3. (Color online) The SrTiO<sub>3</sub> thickness-dependent  $n_s$  and  $\mu_H$  at 300 K are shown in (a) and (b), respectively. (c) The calculation of 2DEG energy per unit area of interface. (d) Schematic view of the LaAlO<sub>3</sub>/SrTiO<sub>3</sub>/NdGaO<sub>3</sub> heterostructure showing at low temperatures the SrTiO<sub>3</sub> thickness range for a mobile 2DEG is around 8–16 uc (green area), and below 6 uc the carriers are localized (blue area).

However, there is an energy penalty to be paid because the polarization fields extend into the SrTiO<sub>3</sub>, hence the total energy to confine the 2DEG in SrTiO<sub>3</sub> can be written as

$$E_{2\text{DEG}} \approx n\hbar^2/2mt^2 + \sigma^2 t/24\epsilon_0\epsilon, \quad (1)$$

where  $n$  is the electron density per unit area,  $m$  is the electron mass,  $\sigma$  is the sheet charge density, and  $\epsilon$  is the dielectric constant of SrTiO<sub>3</sub>. Minimizing this energy with respect to  $t$ , we find the minimum at

$$t = [24\epsilon_0\epsilon n\hbar^2/m\sigma^2]^{1/3}, \quad (2)$$

with  $n = 3.3 \times 10^{14}$  cm<sup>-2</sup>,  $\sigma = 50 \mu\text{C cm}^{-2}$ , and  $\epsilon = 300$ , a value that depends little on temperature in SrTiO<sub>3</sub> thin films,<sup>42</sup> and this gives us a SrTiO<sub>3</sub> thickness of 2.2 nm or 6 uc, which is close to our minimal SrTiO<sub>3</sub> thickness of 8 uc for a delocalized 2DEG.

We propose that the electron transport is dominated by Anderson localization, which is related to the two-dimensional nature of the channel. The delocalized carrier density depends on the effective position of the mobility edge ( $E_M$ ) with respect to the Fermi level ( $E_F$ ). When the SrTiO<sub>3</sub> layer is very thin, the electrons are localized because of the potential fluctuations induced by charge disorder due to ionic interdiffusion at the interface. This ionic interdiffusion usually involves the first 1–2 uc from the interface, and it will create a mobility edge, localizing the states at the bottom of the band. As the SrTiO<sub>3</sub> thickness increases, these potential fluctuations are screened, and the mobility edge drops rapidly to a lower energy. The Fermi energy falls less rapidly with thickness, varying as  $1/t$  for a constant density of states. At about 8 uc of SrTiO<sub>3</sub>,  $E_F$  exceeds  $E_M$  and we see the onset of electron delocalization, and metallic conductivity at low temperatures. Our experiments clearly prove that the conducting electrons are

located appreciably below the LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interface of our films. The reduction in carrier density again in SrTiO<sub>3</sub> layers thicker than 16 uc could be understood in several ways. As the 0.5 electrons per Ti extend into SrTiO<sub>3</sub> layer farther from the interface, the Fermi level  $E_F$  falls towards the bottom of the band due to the increase of available electronic states and it approaches the  $t_{2g}$  band mobility edge for the bulk. This arises from defects such as oxygen vacancies that are distributed throughout the SrTiO<sub>3</sub>, which is more disordered when it is an epitaxially grown layer rather than a single-crystal substrate. Also, there is a tendency for strain relaxation and associated defect production in these thicker SrTiO<sub>3</sub> layers, which can raise the mobility edge again and localize the carriers. A third possibility is the polarization of the distorted SrTiO<sub>3</sub> layer, which can compensate the polarization catastrophe at the interface.<sup>22</sup>

We can model the number of mobile electrons with thickness quite nicely with a single band having a constant density of states. The total number of available states increases in proportion to the number of layers, leading to a Fermi level (relative to the bottom of the rectangular conduction band)

$$E_F = a/t, \quad (3)$$

where  $t$  is the SrTiO<sub>3</sub> thickness and  $a$  is a constant. Then we model the mobility edge to vary due to two effects—one falling off exponentially with  $t$ , due to the disorder in the interface layer, and the other a constant low-energy mobility edge due to residual disorder in the SrTiO<sub>3</sub>, giving

$$E_M = b \exp(-t/t_0) + c, \quad (4)$$

where  $b$ ,  $t_0$ , and  $c$  are all constants. At room temperature, we roughly assume that there is a baseline for the delocalized carrier density  $n_S$  due to the thermal activation. Hence,  $n_S$  can be evaluated by

$$n_S(E_F) = \begin{cases} d & (E_F < E_M), \\ d + n \left[ \frac{E_F - E_M}{E_F} \right] & (E_F \geq E_M), \end{cases} \quad (5)$$

where  $d$  is a constant baseline [assumed to be  $0.5 \times 10^{14} \text{ cm}^{-2}$ , which is the carrier density for 50 uc SrTiO<sub>3</sub> (Ref. 22)] and  $n$  is the carrier density for the ideal 2DEG,  $3.3 \times 10^{14} \text{ cm}^{-2}$ . Based on these, we show the fit to both  $E_F$  and  $E_M$  as a function of thickness in Fig. 4(a). Because  $E_F$  falls much more slowly with thickness and  $E_M$  is higher than  $E_F$  at low thickness, there are two crossover points of  $t_1$  and  $t_2$ , indicating free carriers in the thickness range  $t_1 < t < t_2$ . The fitted curve for the delocalized carriers is also quite consistent with the experimentally observed values in Fig. 4(b). A reasonable screening length  $t_0$  of about 1–2 unit cells is seen for the mobility edge, the details are of which is given in Fig. S10 in the Supplemental Material.<sup>43</sup>

In conclusion, we have shown that the thickness of the SrTiO<sub>3</sub> layer in epitaxially grown LaAlO<sub>3</sub>/SrTiO<sub>3</sub> heterostructures is critical for determining the 2DEG transport properties. Samples with the thinnest SrTiO<sub>3</sub> exhibit a low carrier density at room temperature, a robust insulating ground state, and modified variable-range hopping transport behavior at low

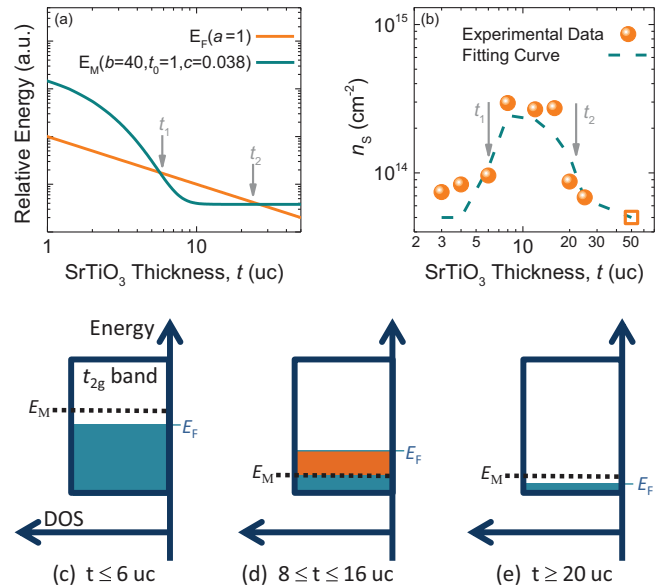


FIG. 4. (Color online) (a) Fitted  $E_F$  and  $E_M$  as a function of SrTiO<sub>3</sub> thickness. (b) The comparison on experimentally observed  $n_S$  and fitted  $n_S$  as a function of SrTiO<sub>3</sub> thickness. The open square in (b) is taken from Ref. 20 as a comparison. The details on controlling the fitting parameters are discussed in Fig. S10 (Ref. 43). Schematic 2D-density of states (DOS) (number of available electronic states per unit interface area) vs energy for the  $t_{2g}$  band are shown with SrTiO<sub>3</sub> thickness  $\leq 6$  uc in (c), 8–16 uc in (d), and  $\geq 20$  uc in (e). Electrons below  $E_M$  are localized and denoted by blue, while electrons which are below  $E_F$  but beyond  $E_M$  are delocalized and denoted by orange. In these sketches, the total area below  $E_F$  is fixed for each thickness, indicating the total number of carriers at the LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interface is fixed.

temperatures, due to the Anderson localization of the 2DEG arising from the random interface potential with ionic interdiffusion. When the SrTiO<sub>3</sub> thickness reaches 8–16 uc, the carrier density increases to almost the expected 0.5 electrons per interface Ti site, and the mobility saturates at the conventional room-temperature value. However, the carrier density falls again for the thicker layers to the conventional value found for single-crystal SrTiO<sub>3</sub> substrates. Most of the Ti 3d electrons are localized at the very bottom of the 3d band, below the mobility edge. We are therefore able to explain the observed behavior on a localization model where the position of the Fermi level and the mobility edge depend on the SrTiO<sub>3</sub> layer thickness. The study of these thin epitaxially grown layers has enabled us to describe the role of localization and to define the extent of the conducting region at the LaAlO<sub>3</sub>/SrTiO<sub>3</sub> interface. It shows how to tailor the oxide interface to optimize the two-dimensional conduction, which will be of importance for oxide electronics.

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