Electron spin resonance observation of charge carrier concentration in organic field-effect transistors during device operation

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Charge carrier concentration in operating organic field-effect transistors (OFETs) reflects the electric potential within the channel, acting as a key quantity to clarify the operation mechanism of the device. Here, we demonstrate a direct determination of charge carrier concentration in the operating devices of pentacene and poly(3-hexylthiophene) (P3HT) by field-induced electron spin resonance (FI-ESR) spectroscopy. This method sensitively detects polarons induced by applying gate voltage, giving a clear FI-ESR signal around g=2.003 in both devices. Upon applying drain-source voltage, carrier concentration decreases monotonically in the FET linear region, reaching about 70% of the initial value at the pinch-off point, and stayed constant in the saturation region. The observed results are reproduced well from the theoretical potential profile based on the gradual channel model. In particular, the carrier concentration at the pinch-off point is calculated to be $\beta/(\beta+1)$ of the initial value, where β is the power exponent in the gate voltage ($V_{\rm gs}$) dependence of the mobility (μ), expressed as $\mu \propto V_{\rm gs}^{\beta-2}$, providing detailed information of charge transport. The present devices show $\beta=2.6$ for the pentacene and $\beta=2.3$ for the P3HT cases, consistent with those determined by transfer characteristics. The gate voltage dependence of the mobility, originating from the charge trapping at the device interface, is confirmed microscopically by the motional narrowing of the FI-ESR spectra.

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I. INTRODUCTION

Organic field-effect transistors (OFETs) using π conjugated molecules and polymers are attracting considerable attention owing to their potential applications for low-cost, flexible, and large area electronic circuits. 1-4 Performance of OFETs has been improved remarkably, showing the mobility higher than 1 cm²/Vs, even in polycrystalline thin films. This can be ascribed to the highly sophisticated design of the semiconductor materials as well as the development of fabrication techniques such as chemical treatment of the device interface by using self-assembled monolayers (SAMs). 5-15 On the other hand, understanding of the basic physical processes, such as charge injection or transport, as well as the electronic state of charge carriers at the semiconductor/insulator interface is still under progress. Thus the microscopic observation of charge carriers during the device operation is an essential subject in understanding the operation principle of the device, which may also contribute to design highly efficient devices.

In principle, device operation of OFETs is understood based on the conventional model using gradual channel approximation (GCA). 4,16,17 According to the model, a potential distribution is induced within the channel by applying gatesource and source-drain voltages, $V_{\rm gs}$ and $V_{\rm ds}$, respectively, dominating the FET output current. The shape of the potential profile has been experimentally observed in the operating OFET by using scanning Kelvin probe microscopy (SKPM), giving linear and superlinear distributions corresponding to the linear and the saturation regions of FET output

characteristics, respectively. The potential profile also gives detailed information of charge transport, such as contact resistance, 18-21 charge trapping at grain boundaries, 20-22 as well as charge concentration dependence of the mobility. 23-25 However, SKPM provides no spectroscopic information of charge carriers. Since the charge distribution within the channel is proportional to the potential distribution within the framework of GCA, determination of charge carrier concentration by the spectroscopic method is a promising alternative approach to finding the operation principle of the device together with the microscopic information of charge carriers.

Field-induced electron spin resonance (FI-ESR) spectroscopy, developed by our group, is a particularly suitable method for this purpose owing to its high sensitivity $(<10^{11} \text{ spins})$ to detect the spin of accumulated carriers at the insulator interface.²⁶ So far, this method has been applied to various OFETs of poly(3-alkylthiophene), ^{26–30} pentacene, ^{31–33} thienothiophene-based high-mobility materials, 34,35 and rubrene single crystals, ³⁶ etc., giving microscopic information such as spin-charge relation, carrier wave function, motional effect of charge carriers, and local molecular orientation at the device interface. In particular, direct determination of spin concentration within the channel during device operation played a crucial role in clarifying the operation mechanism of OFETs, as was demonstrated for P3HT transistors using top-contact geometry,³⁷ and subsequently, for rubrene single-crystal transistors with bottom-contact geometry; ³⁸ the spin concentration exhibited a clear decrease by applying $V_{\rm ds}$ together with $V_{\rm gs}$ in the linear region, whereas no $V_{\rm ds}$ dependence was observed for the saturation region. This result agreed qualitatively with that expected from the change of potential profiles. However, in the previous study of the P3HT transistor, the FI-ESR signal of charge carriers accumulated underneath the top electrodes overlapped with that of channel carriers, resulting in slight underestimation of the modulation effect of channel carriers by the application of drain voltage, as discussed in this paper.

In this study, we applied the FI-ESR technique to the operating OFET of pentacene fabricated on the SiO₂ insulator, which has better surface flatness than Al₂O₃ or parylene insulators adopted in the earlier FI-ESR studies of pentacene. 31,32 In order to obtain the accurate spin concentration within the channel, we fabricated the top-contact FETs with the length of the top electrodes to be sufficiently small compared to the channel length. Furthermore, the spin concentration underneath the electrodes was subtracted from the total concentration. Thus obtained spin concentration of channel carriers (polarons) exhibited a clear V_{ds} dependence in the linear region, reaching \sim 70% of the initial value at the pinch-off point, whereas no $V_{\rm ds}$ dependence was observed in the saturation region. The result was reproduced well by the theoretical potential profile considering the power-law dependence of the mobility on the charge concentration. The concentration-dependent mobility was further confirmed from the transfer characteristics of the same device. Similar results were also obtained for P3HT transistors. Moreover, the observed FI-ESR line width exhibited an enhanced motional narrowing for higher spin concentrations, as in the case of pentacene transistors fabricated on parylene insulators, 32,33 providing microscopic evidence for faster carrier motion for higher charge concentration due to the charge trapping at the interface.

The organization of the paper is as follows. The device fabrication methods and experimental details are described in Sec. II. We first present the basic FI-ESR results of pentacene transistors obtained without applying $V_{\rm ds}$ in Sec. III A. In this section, gate voltage dependence of the spin concentration of pentacene π -electron carriers as well as the motionally narrowed ESR line width are presented. In addition, local molecular orientation of pentacene at the insulator interface is confirmed by the angular dependence of the g value. These basic characteristics of pentacene at the SiO₂ interface are compared with those reported by earlier studies using Al₂O₃ or parylene insulators, which shows that the FET channels of the present devices are well organized and suitable for the analysis. The main results of $V_{\rm ds}$ dependence of the spin concentration are presented in Sec. IIIB, both for pentacene and P3HT devices. The results are compared with those expected from the theoretical potential profile considering gate voltage dependence of the mobility. The conclusions are given in Sec. IV.

II. EXPERIMENTAL

Pentacene powder was purchased from Aldrich Co. Ltd. and was used without further purification. Pentacene thin films with typical thickness of 100 nm were vapor-deposited on the n^+ -Si wafer (10–20 Ω cm, $\langle 100 \rangle$ axis, 0.2 mm thick) with

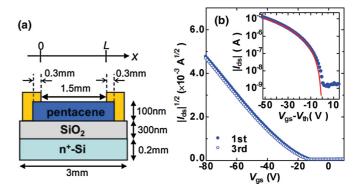


FIG. 1. (Color online) (a) Schematic illustration of the device structure and typical dimensions of the pentacene FET. The x axis indicates the position within the channel from the source electrode edge. (b) Transfer characteristics of a pentacene FET obtained by the first (solid circles) and the third (open circles) scans. The drain voltage is $V_{\rm ds} = -50$ V. Inset shows the logarithmic plot of the drain current with respect to the effective gate voltage of $V_{\rm gs} - V_{\rm th}$, with a threshold voltage of $V_{\rm th} = -20$ V. The solid curve represents the fitting by Eq. (4) with $\beta = 2.6$ for the saturation regime.

a 300-nm-thick thermally grown SiO₂ insulator treated with hexamethyldisilazane. Au electrodes (30 nm) were vapordeposited on the pentacene layer to form the top-contact geometry. Figure 1(a) shows the schematic illustration of the device. Typical dimensions of the device were channel length (L) of 1.5 mm and channel width (W) of 20 mm. The length of the film underneath a top electrode was 0.3 mm, much smaller than L. The P3HT transistors were prepared in the same way as the pentacene device except for the drop-cast process to form thin films. Field-effect transistor characteristics were measured by using a Keithley 2612A source measure unit. Figure 1(b) shows typical transfer characteristics of a pentacene FET. We obtain standard device operation with the mobility (μ) and threshold voltage (V_{th}) of 0.1 cm²/Vs and -20 V, respectively, as determined following the conventional procedures using GCA in the saturation regime.¹⁶ The maximum mobility was $\mu_{\rm max} = 0.6 \ {\rm cm^2/Vs}$ at room temperature in this study. On the other hand, the mobility exhibits certain gate voltage dependence, as is discussed later. The operational stability of the device is satisfactory with a small shift of V_{th} between the first and third scans, as shown in Fig. 1(b).

Electron spin resonance measurements were performed by using a Bruker E-500 spectrometer equipped with TE_{011} cylindrical cavity. The microwave frequency was directly monitored by the frequency counter at each scan. The magnetic field was recorded with the accuracy of ± 0.01 G by using a nuclear magnetic resonance teslameter to determine the precise g values. The FI-ESR signal of injected carriers was clearly obtained by subtracting the background ESR signal at $V_{\rm gs}=30$ V from that obtained under the negative bias voltage. The spin concentration was determined within the error of 10% for the high $V_{\rm gs}$ region from the twice-integration of the first-derivative ESR signal calibrated by that of ${\rm CuSO_4\cdot 5H_2O}$.

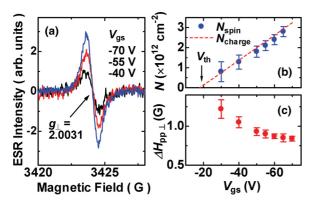


FIG. 2. (Color online) Gate voltage dependence of (a) first-derivative FI-ESR spectra and (b) spin concentration ($N_{\rm spin}$; solid circles) together with the charge concentration ($N_{\rm charge}$; dashed line) and (c) peak-to-peak line width obtained for a pentacene FET without applying $V_{\rm ds}$. The magnetic field is applied perpendicular to the substrate.

III. EXPERIMENTAL RESULTS AND DISCUSSIONS

A. FI-ESR measurements on metal-insulator-semiconductor configuration ($V_{\rm ds}=0$)

In this subsection, we show the basic FI-ESR characteristics of the pentacene FET fabricated on the SiO_2 insulator. The results compare well with those reported by the previous studies using Al_2O_3 or parylene insulators, which shows that the present devices are suitable for the analysis carried out in Sec. III B.

Figure 2(a) shows the gate voltage dependence of the first-derivative FI-ESR spectra obtained with the source and drain electrodes short-circuited. In this case, the device acts as a metal-insulator-semiconductor (MIS) capacitor. The external magnetic field is perpendicular to the substrate. We observe clear FI-ESR signals of π -electron carriers of pentacene around g = 2.003. Figure 2(b) shows the $V_{\rm gs}$ dependence of the accumulated spin concentration $(N_{\rm spin})$ at the interface. Here, $N_{\rm spin}$ increases linearly with a finite threshold voltage of $V_{\rm th} = -20 \, \rm V$, consistent with that determined by the transfer characteristics in Fig. 1(b). The obtained spin concentration agrees well with the charge concentration $(N_{\rm charge})$, as shown by the dashed line in Fig. 2(b). Here, $N_{\rm charge}$ is calculated from the relation $eN_{\text{charge}} = C_i |V_{\text{gs}} - V_{\text{th}}|$, where e is the elementary charge and C_i is the capacitance of the insulator ($C_i = 11.3 \text{ nF/cm}^2$). The agreement between N_{spin} and N_{charge} indicates that all the injected charge carries have spin 1/2, that is, polarons. This result is independent of the insulator material.31,32

As the spin concentration increases, peak-to-peak ESR line width $(\Delta H_{\rm pp})$ exhibits clear narrowing, as shown in Fig. 2(c). Similar behavior has been reported by Matsui *et al.* in the pentacene FET with parylene insulator.³¹ They ascribed this behavior to the motional narrowing of the ESR spectrum reflecting the trap-and-release process in the charge transport. The deeply trapped carriers with long trapping time, which is dominant when the charge concentration is small, give large line widths. On the other hand, shallowly trapped carriers with short trapping time become dominant for higher charge concentration regions, giving prominently narrowed ESR

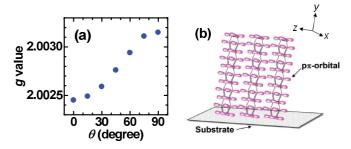


FIG. 3. (Color online) (a) Angular dependence of the g value obtained with the gate voltage of -70 V. Here, θ denotes the angle between the magnetic field and substrate plane. (b) Schematic illustration of the average orientation of the pentacene molecules at the SiO₂ interface together with the direction of $p\pi$ orbital and the definition of the principal axes of the g tensor.

signals. The present result is also interpreted in the same way. The enhanced narrowing of the ESR signal in the present study for higher gate voltages provides microscopic evidence that the charge transport is affected by the trap state. The effect of charge trapping is of fundamental importance in understanding charge transport of organic thin films having the structural or energetic disorders. ^{2,4}, ^{39–44} A remarkable consequence of this effect is a gate voltage dependence of the mobility, as has been reported for a number of OFETs. This effect is explicitly considered as a phenomenological power-law dependence of the mobility on the charge concentration in the operating devices in the next section.

The FI-ESR signal of pentacene exhibits a clear anisotropy between the data obtained with the magnetic field parallel (||) and perpendicular (\bot) to the substrate, indicating a preferential molecular orientation at the insulator interface. Figure 3(a) shows the angular dependence of the g value. Here, θ denotes the angle between the magnetic field and substrate plane. The g value exhibits a minimum at $\theta = 0^{\circ}$ and a maximum at $\theta = 90^{\circ}$, as in the cases of previous FI-ESR studies of pentacene FETs fabricated on Al₂O₃ or parylene insulators.^{31,32} The observed angular dependence is described well by the anisotropy of the g tensor of π electrons of pentacene, whose principal axes are shown in Fig. 3(b). Here, the z axis is parallel to the $p\pi$ orbital, the x axis is parallel to the C-H bond or molecular short axis, and the y axis is parallel to the molecular long axis. Recently, a DFT calculation of the g values was performed for pentacene, which results in $g_x = 2.0027$, $g_y = 2.0031$, and $g_z =$ 2.0025 with the orthorhombic anisotropy of $g_y > g_x > g_z^{45}$ The experimentally determined g_{\perp} value of 2.0032 agrees well with the calculated g_y value. In addition, anisotropy of $g_{\perp} \sim g_y = 2.0031 > g_{\parallel} \sim 1/2(g_x + g_z) = 2.0026$ is consistent with experimental observation. These facts clearly indicate that the molecular long axis (y axis) with maximum g value points almost perpendicular to the SiO₂ plane, as schematically shown in Fig. 3(b).

B. Charge carrier concentrations in operating devices $(V_{ds} \neq 0)$

Upon applying the drain voltage together with the gate voltage, we observe a clear decrease of the FI-ESR intensity, as shown in Fig. 4. Here, we apply a magnetic field parallel to the substrate, where the line width becomes smallest due

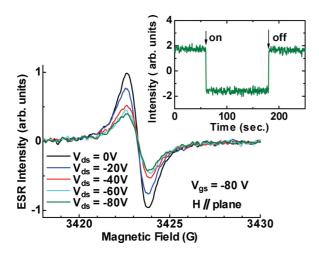


FIG. 4. (Color online) Drain voltage dependence of the first-derivative FI-ESR spectra of a pentacene FET obtained with the applied gate voltage of -80 V. The magnetic field is parallel to the substrate. Inset shows the time response of the FI-ESR intensity of upper peak position with respect to the application and termination of the drain voltage of -80 V.

to the small hyperfine interaction of the $p\pi$ orbital, 46,47 to improve the S/N ratio. The change of the signal intensity takes place reversibly within the time resolution of the present experiment (300 ms), as shown in the inset. This behavior clearly indicates that the total carrier number decreases by applying drain voltage, reflecting the change of charge distribution within the channel. In order to extract the spin concentration within the channel, we subtracted the spin concentration underneath the top electrodes from the total spin concentration: $C_i | V_{gs} - V_{th} | L_s W$ for the source electrode and $C_i | V_{\rm gs} - V_{\rm th} - V_{\rm ds} | L_d W$ for the drain electrode in the linear region. Here, L_s and L_d denote the length of the source and drain electrodes overlapping with the organic layer along the channel, respectively $[L_s = L_d = 0.3 \text{ mm}, \text{ as shown in}]$ Fig. 1(a)]. In the saturation region, the carrier concentration underneath the drain electrode is expected to be zero.

Figure 5(a) shows the $V_{\rm ds}$ dependence of the spin concentration within the channel normalized by the value at $V_{\rm ds}=0$ together with the corresponding output current. In the low $|V_{\rm ds}|$ region, the spin concentration exhibits a gradual decrease as the voltage increases. It reaches 72% of the initial value at $V_{\rm ds}=-60$ V, which corresponds to the pinch-off point $V_p(=V_{\rm gs}-V_{\rm th})$ using $V_{\rm gs}=-80$ V and $V_{\rm th}=-20$ V. On the other hand, it exhibits no $V_{\rm ds}$ dependence for higher voltages. Thus the spin concentration exhibits clearly different $V_{\rm ds}$ dependence between the linear $(|V_{\rm ds}|<|V_p|)$ and saturation $(|V_{\rm ds}|>|V_p|)$ regions. Then we compare this result with that expected from the change of the potential profile.

According to the GCA, local carrier concentration Q(x) at a certain position x within the channel is formulated by using local potential V(x) and the effective gate bias $(V_{\rm gs}-V_{\rm th})$ as $Q(x)=C_i\{V(x)-(V_{\rm gs}-V_{\rm th})\}=C_iV_{\rm eff}$. Here, $V_{\rm eff}$ is the effective gate potential within the channel. The definition of x is shown in Fig. 1(a). In the case of $V_{\rm ds}=0$, a uniform charge density of $C_i|V_{\rm gs}-V_{\rm th}|$ is induced within the channel by the applied gate bias. When the finite drain voltage is applied, a potential distribution is induced along the channel, which

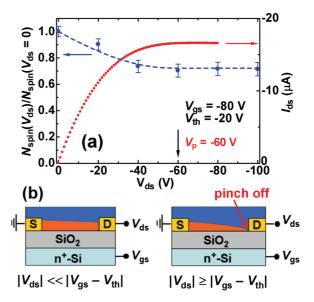


FIG. 5. (Color online) (a) Drain voltage dependence of the normalized spin concentration of a pentacene device obtained with the applied gate voltage of -80 V, together with the FET output current for the same device. The dashed curve represents the calculated charge concentration from the potential profile by using Eq. (7) with $\beta=2.6$. See text for details. The downward arrow shows the expected pinch-off voltage. (b) Schematic illustration of the carrier distribution, or identically potential profile, induced within the channel in the linear (left) and saturation (right) regions. Here, we show the illustration of the bottom-contact device for simplicity, which is valid because the contribution of the carriers underneath the electrodes of the top-contact device is subtracted in this study. The pinch-off region is formed around the drain electrode in the saturation region shown by the arrow.

decreases the total charge concentration within the channel. The shape of the potential distribution is nearly linear when the condition $|V_{\rm ds}| \ll |V_{\rm gs} - V_{\rm th}|$ is satisfied, as schematically shown in Fig. 5(b), because the sheet conductance is almost uniform in this condition, and the lateral electric field $(E_x = -dV(x)/dx)$ along the channel is also almost uniform to fulfill the current continuity condition. In this case, the carrier concentration decreases almost linearly to the applied drain voltage as in Fig. 5(a).

On the other hand, when $|V_{\rm ds}|$ becomes comparable to $|V_{\rm gs}-V_{\rm th}|$, there are no accumulated carriers around the drain electrode, and the channel is pinched off. In the pinch-off condition, the sheet conductance decreases prominently from source to drain, and hence, the lateral electric field grows toward the drain electrode in order to satisfy the current continuity condition. In other words, the small number of carriers around the drain electrode is highly accelerated by the large lateral field so as to keep the constant current within the channel. As a result, the shape of V(x) becomes nonlinear with larger gradient around the drain electrode. The shape of such nonlinear potential profile has been experimentally observed by SKPM techniques. 18,19,23-25 It is also noteworthy that the growth of the lateral electric field around the drain electrode at the pinch-off point has been observed by the second harmonic generation spectroscopy in the operating pentacene transistors.⁴⁸ In the saturation region of $|V_{\rm ds}|>|V_{\rm gs}-V_{\rm th}|$, the excess drain voltage is consumed in the narrow charge-depleted region around the drain electrode, and hence, there are no further changes of the potential shape or carrier concentration as well as the output current.

As mentioned above, a large change of the potential is induced within the channel in the pinch-off condition with boundary conditions of $V_{\rm eff}|_{x=0}=V_{\rm gs}-V_{\rm th}$ at the source electrode and $V_{\rm eff}|_{x=L}=0$ at the drain electrode. If the potential drops linearly from source to drain as in the linear region, the spin concentration decreases to exactly 50% from the initial value at $|V_{\rm ds}|=|V_p|$.³⁷ On the other hand, the observed spin concentration is clearly higher than 50%, indicating that the charge distribution is superlinear with a steep drop around the drain electrode, as schematically illustrated in Fig. 5(b), consistent with the above expectations.

Since the shape of the potential profile (or carrier distribution) reflects the local conductivity through the current continuity condition as mentioned above, the observed carrier concentration in Fig. 5(a) provides microscopic information of transport properties. In the following, we calculate the change of charge carrier concentration in the operating transistors based on the GCA. Neglecting the diffusion current, the local drain current $I_{\rm ds}(x)$ is described as 16

$$I_{\rm ds}(x) = W\mu Q(x)E_x = -W\mu C_i V_{\rm eff} \frac{dV_{\rm eff}}{dx}.$$
 (1)

Here, we assume that the mobility depends on the position x through the charge concentration dependence, as is expected microscopically by the enhanced motional narrowing of the FI-ESR line width for higher carrier concentrations in Fig. 2(c). In this study, we adopt a power-law dependence of the mobility on the effective gate potential^{24,49}

$$\mu = f_0 V_{\text{eff}}^{\beta - 2},\tag{2}$$

where f_0 is a prefactor. Such a power-law dependence of the mobility is expected in the case that the charge transport is dominated by the variable range hopping (VRH) process among the trap states. ⁴⁰ Moreover, it is also adopted empirically in the multiple trap-and-release (MTR) model. ^{41,42} The identification of these charge transport processes is not the scope of this paper, and we use Eq. (2) as a phenomenological expression of the mobility in the present device. By integrating Eq. (1) from source [x = 0, V(x) = 0] to drain [x = L, $V(x) = V_{ds}$], the drain current is described as ^{24,49,50}

$$I_{\rm ds} = -\frac{WC_i}{L} \frac{f_0}{\beta} [(-V_{\rm gs} + V_{\rm th})^{\beta} - (V_{\rm ds} - V_{\rm gs} + V_{\rm th})^{\beta}]. \quad (3)$$

In the saturation region, $V_{\rm ds}$ is replaced by $V_{\rm gs}-V_{\rm th}$ in Eq. (3). Then we obtain the expression

$$I_{\rm ds} = -\frac{WC_i}{L} \frac{f_0}{\beta} (-V_{\rm gs} + V_{\rm th})^{\beta}, \tag{4}$$

for the saturation region.

The conventional expressions of the drain current are obtained by putting $\beta=2$ in Eqs. (3) and (4) with the constant mobility corresponding to f_0 . Equation (4) actually fits our experimental transfer curve, as shown by the solid line in the inset of Fig. 1(b). The fitting parameters are $\beta=2.6\pm0.1$ and $f_0=0.06~{\rm cm^2/V^{(\beta-1)}}{\rm s}$. The obtained value of β , larger than

2, gives evidence for the effective gate voltage dependence of the mobility.

Then we calculate the potential profile and the total carrier number within the channel. By using Eq. (3), the local current at the position x becomes

$$I_{ds}(x) = -\frac{WC_i}{x} \frac{f_0}{\beta} \{ (-V_{gs} + V_{th})^{\beta} - [V(x) - V_{gs} + V_{th}]^{\beta} \}.$$
(5)

From the current continuity condition of $I_{ds}(x) = I_{ds}$, the local potential is solved as²⁴

$$V(x) = V_{gs} - V_{th} + \left\{ (-V_{gs} + V_{th})^{\beta} - \frac{x}{L} [(-V_{gs} + V_{th})^{\beta} - (V_{ds} - V_{gs} + V_{th})^{\beta}] \right\}^{\frac{1}{\beta}}.$$
 (6)

This equation actually fits the experimental potential profile as determined from SKPM technique. ^{23–25} The total carrier number is then obtained by integrating the local carrier concentration within the channel as

$$eN_{\text{charge}} = W \int_{0}^{L} C_{i} V_{\text{eff}} dx$$

$$= LWC_{i} \frac{\beta}{\beta + 1} \frac{(-V_{\text{gs}} + V_{\text{th}})^{\beta + 1} - (V_{\text{ds}} - V_{\text{gs}} + V_{\text{th}})^{\beta + 1}}{(-V_{\text{gs}} + V_{\text{th}})^{\beta} - (V_{\text{ds}} - V_{\text{gs}} + V_{\text{th}})^{\beta}}.$$
(7)

When $V_{\rm ds}$ approaches 0, Eq. (7) converges to $-LWC_i(V_{\rm gs} V_{\rm th}$), as expected. The dashed curve in Fig. 5(a) shows $V_{\rm ds}$ dependence of the carrier number calculated by Eq. (7) in the linear region of $|V_{\rm ds}| \leqslant 60$ V, which is normalized by the value at $V_{\rm ds}=0$. We adopted the parameter values of $\beta = 2.6$ and $V_{\text{th}} = -20 \text{ V}$, which are determined from the transfer characteristics and the $V_{\rm gs}$ dependence of the spin concentration, respectively. Note that the dashed curve in Fig. 5(a) is extended to the saturation region with the same value at $V_{\rm ds} = V_p (= -60 \text{ V})$ because no further change of carrier number is expected in this region. The agreement between the experimental and calculated values is satisfactory. Here, the ratio of the spin concentration at the pinch-off point and at $V_{\rm ds} = 0$ acts as a simple measure of the β value; it becomes $\beta/(\beta+1)$ as readily expected from Eq. (7). In the case of $\beta = 2$ for the uniform mobility, the spin concentration becomes 67%. In the present pentacene device, the experimentally determined value of 72% agrees well with the case of $\beta = 2.6$. It is also noteworthy that the pinch-off voltage is reasonably determined from the shape of the $V_{\rm ds}$ dependence of the spin concentration, as shown by the downward arrow in Fig. 5(a). It shifted consistently by changing the applied gate voltage following the relation $V_p = V_{\rm gs} - V_{\rm th}$.

In order to further confirm the validity of the discussion above, we also applied the FI-ESR technique to the operating P3HT transistor. Figure 6 shows the $V_{\rm ds}$ dependence of the FI-ESR signal obtained by applying negative gate voltage of $V_{\rm gs}=-10$ V. The magnetic field is perpendicular to the substrate. At $V_{\rm ds}=-0$ V, we observe a clear

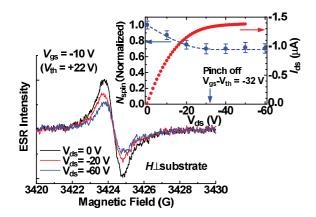


FIG. 6. (Color online) Drain voltage dependence of the first-derivative FI-ESR spectra of a P3HT FET obtained with the applied gate voltage of -10 V. The magnetic field is perpendicular to the substrate. Inset shows the $V_{\rm ds}$ dependence of the spin concentration together with the FET output current for the same device. The dashed curve represents the calculated charge concentration from the potential profile by using Eq. (7) with $\beta = 2.3$. The downward arrow shows the expected pinch-off voltage.

FI-ESR signal of positive polarons on the chain around g=2.003. The observed g value corresponds well with that expected for the molecular short axis, as determined in the uniaxially oriented P3HT ultrathin transistors, ²⁹ indicating that the P3HT chain takes edge-on orientation at the insulator interface to form the lamellar structure. By applying drain voltages, a clear decrease of the FI-ESR intensity is observed as in the case of the pentacene device. Here, the applied gate voltage is sufficiently small not to induce the spinless states, which has been observed in the high gate voltage region in previous FI-ESR studies due to the formation of spinless bipolarons (or polaron pairs). ^{26–28,37} Thus, $N_{\rm spin}$ agrees with the charge concentration.

The inset of Fig. 6 shows the $V_{\rm ds}$ dependence of $N_{\rm spin}$ normalized by the value at $V_{\rm ds}=0$ V together with the FET output current. Here, $N_{\rm spin}$ decreases to 70% of the initial value in the saturation region. The transfer characteristics of the same device was fitted well by Eq. (3) in this device, and the extracted parameters were $f_0=7\times 10^{-3}~{\rm cm^2/V^{(\beta-1)}s}$, $\beta=2.3$, and $V_{\rm th}=22$ V. By using these parameters, we calculated the drain voltage dependence of the carrier number by Eq. (7) as shown by the dashed curve. The agreement between the experimental and calculated results is quite satisfactory. In addition, the pinch-off voltage, shown by the downward arrow, takes different values as that determined for the pentacene device in Fig. 5(a) due to the different $V_{\rm gs}$ and $V_{\rm th}$ values.

It is also noteworthy that similar $V_{\rm ds}$ dependence of the spin concentration has recently been observed in the operating rubrene single-crystal transistor with bottom-contact geometry. Note that the bottom-contact device has no carriers underneath the electrodes, and all the FI-ESR signals come from the channel carriers. The carrier concentration decreases to $\sim 60\%$ of the initial value in the saturation region, higher than 50%, indicating that the potential profile is also nonlinear, consistent with this study. On the other hand, the carrier concentration ratio is clearly smaller than the present thin film devices of $\sim 70\%$. This may indicate that the gate voltage dependence of the mobility

is negligible in the rubrene device, although the carrier concentration ratio is even smaller than the expected value in the case of $\beta=2$. This result is supported by the transfer characteristics showing a linear $I_{\rm ds}^{1/2}$ - $V_{\rm gs}$ dependence,³⁶ as well as the narrow and $V_{\rm gs}$ -independent FI-ESR line width in the rubrene devices.³⁸ These facts may indicate that the rubrene single-crystal transistor, fabricated by the lamination technique, produces few deep trap states, resulting in the high charge carrier mobilities.

The above results provide direct evidence that the device characteristics are intrinsically dominated by the change of the carrier concentration within the channel, which reflects the change of potential profile, both in the thin film and single crystalline transistors. This indicates the validity of GCA in understanding the operation principle of these organic transistors. The FI-ESR spectroscopy is thus a useful method to obtain device parameters, such as carrier concentration, threshold voltage or pinch-off voltage, gate-voltage dependence of the mobility, as well as the dynamical feature of the carriers. On the other hand, further details of the charge transport, such as the microscopic origin of the trap state as well as the identification of the transport model, are still unclear in this study. In this context, application of this method at low temperatures may be an interesting subject to understand these properties, which is left open for further studies.

IV. CONCLUSION

In this paper, we demonstrated the change of charge carrier concentration in the operating OFETs of pentacene and P3HT through the direct determination of spin concentration by using the FI-ESR technique. The FI-ESR signals of positive polarons were clearly observed by applying negative gate biases in both devices. Upon applying drain voltage, the carrier concentration decreased in the linear region, reaching \sim 70% of the initial value at the pinch-off point, and stayed constant in the saturation region. The change of the carrier concentration reflects the change of the charge distribution, or identically the potential profile based on the gradual channel approximation, within the channel. Here, V_{ds} dependence of the carrier concentration was reproduced well by the potential calculation considering the power-law dependence of the mobility on the effective gate potential as $\mu \propto V_{\rm eff}^{\beta-2}$, as was also confirmed from the transfer characteristic of the device. The spin concentration decreases to $\beta/(\beta+1)$ of the initial value at the pinch-off voltage, providing a simple measure to estimate β as well as the pinch-off voltage. Obtained values of β were $\beta = 2.6$ for a pentacene device and $\beta = 2.3$ for a P3HT device, consistent with those obtained by the transfer characteristics. The gate voltage dependence of the mobility, which reflects a carrier trapping at the device interface, was further confirmed microscopically from the enhanced motional narrowing of the FI-ESR signal for higher gate voltages.

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