Charged impurity scattering in top-gated graphene nanostructures

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We study charged impurity scattering and static screening in a top-gated substrate-supported graphene nanostructure. Our model describes how boundary conditions can be incorporated into scattering, sheds light on the dielectric response of these nanostructures, provides insights into the effect of the top gate on impurity scattering, and predicts that the carrier mobility in such graphene heterostructures decreases with increasing top dielectric thickness and higher carrier density. An increase of up to almost 60% in carrier mobility in ultrathin top-gated graphene is predicted.

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Introduction. The electron mobility in single-layer graphene (SLG) has been demonstrated to be as high as 200 000 cm² V⁻¹ s⁻¹,¹ and is central to many of its potential nanoelectronic applications. However, in most graphene-based heterostructures, SLG must be physically supported by an insulating dielectric substrate such as SiO₂, and the carrier mobility in such structures is about one order of magnitude lower¹ as a result of scattering with impurities, defects, and surface roughness. This reduction in carrier mobility can be further exacerbated when a top gate, consisting of a layer of high- κ dielectric material such as HfO₂ or Al₂O₃ overlayed with metal, is deposited on SLG.^{2–4}

Experiments suggest that the dominant factor limiting electrical transport is scattering by charged impurities, which are believed to be located at or near the SLG-substrate interface.⁵ Adam and co-workers have successfully explained the linearity of the conductivity with respect to carrier density as a consequence of charged impurity scattering.^{6,7} However, their theory is limited to the simple geometry of SLG supported by a substrate. In more realistic graphene heterostructures, the SLG is encapsulated between the substrate and the top gate, 5,8,9an arrangement which offers better local electrostatic control than the bottom gate and is probably required for large-scale integration. On the other hand, top gates modify the dielectric environment of the SLG by introducing image charges along the various interfaces. Thus, given the significance of impurity scattering, it is important to treat this process theoretically by accounting fully for these geometrical effects in order to optimize the design of SLG-based nanoelectronics.

Jena and Konar¹⁰ suggest that charged impurity scattering in low-dimensional semiconductor nanostructures can be damped by coating them with high- κ dielectrics. It has been shown^{11,12} that placing a higher- κ overlayer (e.g., ice in Ref. 11 and nonpolar liquid in Ref. 12) on SLG can lead to a weakening of the Coulombic scattering forces and to an improvement in the conductivity (although Ponomarenko and co-workers using a high- κ liquid overlayer were not able to find any significant improvement¹³). The deposition of dielectric films, such as HfO₂ or Al₂O₃, on graphene has been found to lead to the degradation of electron mobility.^{2,14} This has been attributed to the roughening of the graphene surface. However, using atomic layer deposition (ALD), Fallahazad and co-workers^{5,8} have been able to deposit ultrathin high- κ dielectric materials on SLG with much less roughness and observe a significant improvement in the carrier mobility. Hollander and co-workers were also able to see an increase of the Hall mobility in epitaxial graphene with thinner top gate dielectrics.¹⁵

In this Rapid Communication, we extend the theory of Adam and co-workers to the more complicated geometry of top-gated SLG, and apply it to study the effect of the top gate on impurity scattering, emphasizing the dependence of the mobility on the top dielectric thickness. Our principal finding is that a thinner top gate leads to a higher carrier mobility because of the stronger image-charge effect in the gate. This agrees with the scaling trend found by Fallahazad and co-workers⁸ as well as Hollander and co-workers.¹⁵ This mobility improvement due to stronger gate screening was also suggested for conventional metal-oxide-semiconductor systems by Gamiz and Fischetti.¹⁶ A general formula for charged impurity scattering in SLG heterostructures is provided.

The outline of the paper is as follows. We derive the Thomas-Fermi screening and the electron-impurity Coulombic interaction, taking into account the boundary conditions imposed by the substrate and the top gate. The matrix element for electron-impurity interaction is calculated and used to compute the momentum relaxation rate, which determines the conductivity and carrier mobility. We then compute the carrier mobility for different top dielectrics, gate thickness, and carrier densities.

Method. The model of the top-gated graphene nanostructure consists of a SLG sheet sandwiched between two oxide layers with the interfaces coplanar with the *x*-*y* plane. The substrate consists of the semi-infinite region z < 0, while the top oxide spans the region $h \le z < h + t_{ox}$, where *h* is the height of the empty space between the two oxide layers and t_{ox} is the thickness of the top oxide layer. The region $z \ge h + t_{ox}$ is assumed to be composed of a semi-infinite ideal metal. Thus, all field lines terminate at $z = h + t_{ox}$. For simplicity, we assume that the SLG floats at a height of z = d = h/2 halfway between the oxide layers. The structure is shown in Fig. 1. The dielectric constants of the top and bottom oxides are ϵ_{tox} and ϵ_{box} , respectively.

To describe the static dielectric screening of a charged impurity in SLG, we start from the Poisson equation for the *screened* scalar potential Φ_{scr} :

$$-\nabla^2 \Phi_{\rm scr}(\mathbf{R}, z) = \frac{1}{\epsilon_0} [\rho_{\rm imp}(\mathbf{R}, z) + \rho_{\rm scr}(\mathbf{R}, z)], \qquad (1)$$

 Metal

 Top oxide
 Image charge
 tox ↓

 Charged impurity

 Graphene
 d↓

 Bottom oxide

FIG. 1. (Color online) Basic model used in our calculation. The SLG is an infinitely thin layer suspended in the middle of the vacuum gap between a semi-infinite substrate and a top oxide layer of thickness t_{ox} . The dielectric is capped with metal, which we assume to be a perfect conductor. The charged impurity on the SLG has image charges under and above it in the substrate and top gate, respectively.

where ρ_{imp} is the impurity charge and ρ_{scr} represents the screening (polarization) charge. Here **R** represents the coordinate on the *x*-*y* plane, while *z* is the coordinate along the perpendicular direction. The integral form of Eq. (1) is

$$\Phi_{\rm scr}(\mathbf{R},z) = \Phi(\mathbf{R},z) + \int d\mathbf{R}' \, dz' \, G(\mathbf{R}z,\mathbf{R}'z')\rho_{\rm scr}(\mathbf{R}'z'), \quad (2)$$

where $G(\mathbf{R}z, \mathbf{R}'z')$ is the Green function that satisfies the equation $-\nabla^2[\epsilon(\mathbf{R}, z)G(\mathbf{R}z, \mathbf{R}'z')] = \delta(\mathbf{R} - \mathbf{R}', z - z')$ with the proper boundary conditions. The bare potential $\Phi(\mathbf{R}, z)$ is defined as $\Phi(\mathbf{R}, z) = \int d\mathbf{R}' dz' G(\mathbf{R}z, \mathbf{R}'z')\rho_{imp}(\mathbf{R}'z')$. The second term on the right-hand side of Eq. (2) represents the screening charge distribution. The bare and screened potentials can be written in terms of their Fourier components: $\Phi(\mathbf{R}, z) = \sum_{\mathbf{Q}} \phi_Q(z) e^{-i\mathbf{Q}\cdot\mathbf{R}}$ and $\Phi_{scr}(\mathbf{R}, z) = \sum_{\mathbf{Q}} \phi_Q^{scr}(z) e^{-i\mathbf{Q}\cdot\mathbf{R}}$. The two-dimensional Fourier transform of Eq. (2) yields the following expression for the z-dependent part of the Fourier-transformed screened potential:

$$\phi_Q^{\rm scr}(z) = \phi_Q(z) + \int dz' \, G_Q(z,z') \rho_Q^{\rm scr}(z'), \qquad (3)$$

where **Q** is the two-dimensional in-plane wave vector. Equation (3) can be solved if we express the polarization charge ρ_Q^{scr} as a function of the screened scalar potential. Thus, we express the screening charge as

$$\rho_Q^{\rm scr}(z) = e^2 \Pi(Q) f(z) \phi_Q^{\rm scr}(z), \tag{4}$$

where $\Pi(Q)$ is the in-plane static polarizability and f(z) governs the polarization charge distribution in the perpendicular direction. We require f(z) to be normalizable to unity, i.e., $\int dz f(z) = 1$. For convenience, we choose $f(z) = \delta(z - d)$. Physically, this implies that the SLG is idealized as an infinitely thin sheet of polarized charge. Combining Eqs. (3) and (4), we obtain the expression

$$\phi_{Q}^{\rm scr}(z) = \phi_{Q}(z) + e^{2} \int dz' G_{Q}(z,z') \Pi(Q) f(z') \phi_{Q}^{\rm scr}(z').$$
(5)

After some algebra, Eq. (5) yields

$$\phi_Q^{\rm scr}(z) = \phi_Q(z) + \frac{e^2 G_Q(z,d) \Pi(Q)}{1 - e^2 G_Q(d,d) \Pi(Q)} \phi_Q(d).$$

Since we are only concerned about the in-plane scattering potential $\phi_Q^{\text{scr}}(d)$, we have

$$\phi_{\mathcal{Q}}^{\rm scr}(d) = \frac{\phi_{\mathcal{Q}}(d)}{1 - e^2 G_{\mathcal{Q}}(d, d) \Pi(\mathcal{Q})}.$$
 (6)

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Therefore, the generalized expression for the static two-dimensional screening function is $\epsilon(Q) = 1 - e^2 G_Q(d,d) \Pi(Q)$. In the simplest case of suspended SLG, we have $G_Q(d,d) = \frac{1}{2\epsilon_0 Q}$ and $\Pi(Q) = -\frac{2E_F}{\pi \hbar^2 v_F^2}$, ⁶ giving us $\epsilon(Q) = 1 + \frac{Q_s}{Q}$, where $Q_s = \frac{e^2 E_F}{\epsilon_0 \pi \hbar^2 v_F^2}$, E_F , and v_F are the inverse screening length, the Fermi level, and the Fermi velocity, respectively. Note that the screening function is determined by its electrostatic environment through $G_Q(d,d)$, the electrostatic Green function.

In order to solve Eq. (6) and find the screening function $\epsilon(Q)$, we need an explicit expression for the electrostatic Green function $G_Q(z,z')$. This Green function incorporates the details of the electrostatic environment around the SLG. For a source in the empty space between the top and bottom oxide layers, i.e., for $0 < z' \leq h$, $G_Q(z,z')$ satisfies the equation

$$-\epsilon \left(\frac{\partial^2}{\partial z^2} - Q^2\right) G_Q(z, z') = \delta(z - z').$$

This implies that we can always express $G_Q(z,z')$ as a linear combination of e^{Qz} and e^{-Qz} , or $\cosh(Qz)$ and $\sinh(Qz)$. The Green function has to satisfy the following continuity conditions at the interfaces z = 0, z = h, and $z = h + t_{ox}$: $G_Q(h + t_{ox}, z') = \lim_{z \to -\infty} G_Q(z, z') = 0$, $G_Q(h^+, z') = G_Q(h^-, z')$, $G_Q(0^+, z') = G_Q(0^-, z')$, $\epsilon_{tox} \frac{\partial G_Q(z=h^+, z')}{\partial z} = \epsilon_0 \frac{\partial G_Q(z=h^-, z')}{\partial z}$, and $\epsilon_0 \frac{\partial G_Q(z=0^+, z')}{\partial z} = \epsilon_{box} \frac{\partial G_Q(z=0^-, z')}{\partial z}$. In addition, we have the following conditions at z = z': $G_Q(z = z'^-, z') = G_Q(z = z'^+, z')$ and $\epsilon_0 \frac{\partial}{\partial z'} G_Q(z = z'^-, z') - \epsilon_0 \frac{\partial}{\partial z'} G_Q(z = z'^+, z') = 1$. After some straightforward but lengthly algebra we obtain for $G_Q(0 < z \le h, 0 < z' \le h)$ the following expression:

$$G_{Q}(z,z') = \frac{1}{2\epsilon_{0}Q} \left\{ e^{-Q|z-z'|} + \frac{\lambda_{t}\lambda_{b}e^{-2Qh}}{1-\lambda_{t}\lambda_{b}e^{-2Qh}} \left[e^{-Q(z-z')} + e^{Q(z-z')} \right] - \frac{\lambda_{t}e^{Q(z+z'-2h)} + \lambda_{b}e^{-Q(z+z')}}{1-\lambda_{t}\lambda_{b}e^{-2Qh}} \right\},$$
(7)

where $\lambda_t = \frac{\epsilon_{\text{tox}} \coth Q_{t_{ox}} - \epsilon_0}{\epsilon_{\text{tox}} \coth Q_{t_{oy}} + \epsilon_0}$ and $\lambda_b = \frac{\epsilon_{\text{box}} - \epsilon_0}{\epsilon_{\text{box}} + \epsilon_0}$. In the region $h \leq z < h + t_{ox}$, making use of the continuity of the Green function and the fact that it has to terminate at $z = h + t_{ox}$, we can easily write the Green function as $G_Q(z > h, z') = G(h, z') \frac{\sinh Q(h + t_{ox} - z)}{\sinh Q t_{ox}}$. Similarly, for z < 0 we have $G_Q(z < 0, z') = G(0, z')e^{+Qz}$.

The scattering potential due to a single *bare* point charge on the SLG at the origin satisfies the equation $-\epsilon_0 \nabla^2 \Phi(\mathbf{R}, d) =$



FIG. 2. (Color online) Dependence of the carrier mobility (μ) on top gate dielectric thickness (t_{ox}) for SiO₂, Al₂O₃, and HfO₂. The mobility decreases with increasing t_{ox} . This decrease is greater at $n = 10^{12}$ cm⁻² than at $n = 5 \times 10^{12}$ cm⁻².

 $e^2\delta(\mathbf{R},d)$. The corresponding two-dimensional Fourier transform of $\Phi(\mathbf{R},d)$ is $\phi_Q(d) = e^2G_Q(d,d)$. Upon including the screening function from Eq. (6), we obtain the *generalized* expression for the screened Coulomb scatterer:

$$\phi_Q^{\rm scr}(d) = \frac{e^2 G_Q(d,d)}{1 - e^2 G_Q(d,d) \Pi(Q)} \,. \tag{8}$$

The matrix element of the scattering potential of randomly distributed screened impurity charge centers in SLG is $|\langle V_{s\mathbf{K},s\mathbf{K}'}\rangle|^2 = |\phi_{|\mathbf{K}-\mathbf{K}'|}^{\rm scr}(d)|^2(1 + \cos\theta_{\mathbf{K}\mathbf{K}'})/2$, where $\theta_{\mathbf{K}\mathbf{K}'}$ is the angle between the wave vectors \mathbf{K} and \mathbf{K}' , and s = +1 (-1) for the conduction (valence) band. The transport scattering rate is given by $1/\tau(E_{s\mathbf{K}}) = (n_{\rm imp}/h) \int d\mathbf{K}' |\langle V_{s\mathbf{K},s\mathbf{K}'}\rangle|^2 [1 - \cos\theta_{\mathbf{K}\mathbf{K}'}]\delta(E_{s\mathbf{K}} - E_{s\mathbf{K}'})$, where $n_{\rm imp}$ is the impurity density, and the conductivity σ is approximated by $\sigma = e^2 v_F^2 D(E_F)\tau(E_F)/2$, where $D(E_F)$ is the density of states at the Fermi level. The impurity-limited mobility is taken to be^{6,7}

$$\mu = \frac{\sigma}{en} = \frac{e^2 v_F^2}{2en} D(E_F) \tau(E_F), \qquad (9)$$

where $n = E_F^2 / (\pi \hbar^2 v_F^2)$ is the zero-Kelvin approximation to the carrier density.

Results and discussion. Using Eq. (9), we estimate the impurity-limited mobility of top-gated SLG for different carrier densities, top gate dielectrics, and dielectric thickness. In particular, the dependence of the mobility on t_{ox} is studied in detail. For simplicity, we assume that the doping level is $\geq 10^{12}$ cm⁻² because charge inhomogeneity ("puddles") is a phenomenon at/near the Dirac point and is not captured in our theory.

We consider three top gate dielectrics, SiO₂ ($\kappa = 3.9$), Al₂O₃ ($\kappa = 12.5$),⁸ and HfO₂($\kappa = 22.0$).⁵ The bottom dielectric is assumed to be SiO₂. t_{ox} is varied from 1 to 12 nm. The electron mobility is calculated for two values of the carrier density, 1×10^{12} cm⁻² and 5×10^{12} cm⁻². The results are shown in Fig. 2. We set the impurity density to be $n_{imp} = 0.5 \times 10^{12}$ cm⁻². In Fig. 2, we notice that the dependence of the mobility on t_{ox} is stronger for $n = 1 \times 10^{12}$ cm⁻² than for





FIG. 3. (Color online) Dependence of the mobility on carrier density. The mobility decreases with carrier density, and the extent of the decrease is greater with smaller top gate dielectric thickness.

 $n = 5 \times 10^{12}$ cm⁻². As expected, the higher- κ oxides yield a larger mobility because the higher effective permittivity weakens Coulombic interactions (electron-electron and electron-impurity) in graphene. Although the weaker electron-electron interaction results in less screening charge around the impurity, the bare charge on the impurity is reduced to a greater extent, resulting in an overall diminished screened impurity charge.

We also find that the mobility increases with decreasing t_{ax} , in qualitative agreement with the experimental results in Ref. 8. In our simulations, this increase in mobility can be substantial. For example, in Fig. 2 at $n = 10^{12}$ cm⁻², the mobility for a 1.0-nm SiO₂ top gate is almost 60% higher than that for a 12.0-nm top gate. This scaling trend has been attributed to the increase in defect concentration with greater t_{ox} .⁸ However, our model also reproduces this trend because with a decreasing tox the image "anticharge" at the metal-dielectric interface gets closer to the impurity charge and weakens its scattering potential. This implies that ultrathin top gates can be used to compensate for large charged impurity densities. The carrierdensity dependence of the mobility is calculated for different top gate dielectrics at $t_{ox} = 2$ and 12 nm. The carrier density is varied from 10^{12} to 10^{13} cm⁻². The results are shown in Fig. 3. In general, the mobility decreases with increasing carrier density, in contrast to the results in Refs. 6 and 7 which suggest that mobility should be independent of carrier density. This decrease is more acute in the 2-nm case than in the 12-nm case and for higher- κ oxides. To understand why, we look at Eq. (9). In suspended SLG, the Fermi level E_F is proportional to $n^{1/2}$. The scattering time $\tau(E_F)$ is also proportional to $n^{1/2}$ because $G_O(d,d)$ in Eq. (8) scales as 1/Q. Thus, μ is independent of *n*. On the other hand, in top-gated SLG, $G_O(d,d)$ is almost constant over a large range of Q values, and hence, $\tau(E_F)$ is approximately independent of *n* at low carrier densities. Thus, μ scales roughly as $n^{-1/2}$. Thus, the trend shown in Fig. 3 should be viewed as low-density/thin-oxide enhancement of the mobility above its gate-unscreened value thanks to the screening effect of the image charges induced in the gate by the impurities.

The improvement of mobility with a thinner top oxide layer highlights the compensating effect of having a metal

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layer in close proximity to SLG. It has been suggested that voltage fluctuations induced by charged impurities in the substrate lead to local electrostatic doping and are responsible for the formation of puddles in graphene near the charge neutral point.^{17,18} We propose that doping inhomogeneities can be "smoothened out" through the use of an ultrathin high- κ top-gate structure since the image charges can partially neutralize the effects of the charged impurities. Unintentional doping from the substrate can also be reduced, leading to an improvement of carrier transport in top-gated SLG.

Conclusions. We have studied charged impurity scattering and static screening in top-gated SLG nanostructures. The image charge effect dampens Coulombic interaction, resulting in weaker unscreened charged impurity scattering and static screening. The carrier mobility is found to increase with decreasing top gate dielectric thickness and decreasing carrier density. Our theory suggests that if charged impurity scattering is a significant factor in limiting electronic transport in SLG, then having a top metal gate with an ultrathin dielectric can compensate for the effect of the charged impurities. This offers a simple and practical design strategy to improve the impurity-limited mobility in SLG heterostructures.

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