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## **Decoupling graphene from SiC(0001) via oxidation**

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(Received 24 March 2010; revised manuscript received 2 June 2010; published 27 July 2010)

When epitaxial graphene layers are formed on SiC(0001), the first carbon layer (known as the "buffer layer"), while relatively easy to synthesize, does not have the desirable electrical properties of graphene. The conductivity is poor due to a disruption of the graphene  $\pi$  bands by covalent bonding to the SiC substrate. Here we show that it is possible to restore the graphene  $\pi$  bands by inserting a thin oxide layer between the buffer layer and SiC substrate using a low temperature, complementary metal-oxide semiconductor-compatible process that does not damage the graphene layer.

DOI: [10.1103/PhysRevB.82.041411](http://dx.doi.org/10.1103/PhysRevB.82.041411)

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Following its experimental realization by Novoselov *et al.*, [1](#page-3-0) graphene, one, or a very few, layers of carbon in hexagonal  $sp<sup>2</sup>$ -hybridized sheets, has been the subject of intensive investigation. Its unique electronic properties $2,3$  $2,3$  have attracted great interest owing to potential applications in nanoelectronics[.4,](#page-3-3)[5](#page-3-4) Graphene films can be produced in a variety of ways, e.g., exfoliation of samples from pyrolytic graphite,<sup>1</sup> chemical-vapor deposition  $(CVD)$ , <sup>6-[8](#page-3-6)</sup> or sublima-tion of Si from SiC(0001) substrates.<sup>9–[12](#page-3-8)</sup> From the standpoint of compatibility with current device fabrication processes, Si sublimation from SiC(0001) is particularly appealing: wafersized graphene films of controlled thickness can be grown directly on a semi-insulating substrates. However, graphene growth on  $SiC(0001)$ , <sup>[13](#page-3-9)</sup> either by sublimation of Si or by carbon CVD, has a serious drawback. The first graphene layer, while easy to grow uniformly, is nonconductive.  $14,15$  $14,15$ Thus from an electronic point of view, this layer is not graphene at all, but rather a "buffer layer" on which additional, electrically active graphene must be grown. Band structure measurements by angle-resolved photoemission spectroscopy<sup>16</sup> and first-principles calculations<sup>14[,15](#page-3-11)</sup> show disruption of the buffer layer  $\pi$  bands by strong covalent bonding to the SiC substrate.

Recently, it was shown that annealing in hydrogen at temperatures above 600 °C can decouple the buffer layer from the SiC substrate, resulting in the appearance of the graphene band structure[.17](#page-3-13) Here we describe a low-temperature oxidation process that accomplishes the same decoupling. When the buffer layer is exposed to oxygen at  $250 \degree C$ , an oxide layer is formed *between* the buffer layer and the SiC(0001) substrate. We show that this ultrathin layer—containing about 1.2 oxygen atoms per SiC unit cell—is sufficient to decouple the buffer layer from the substrate, restoring the  $\pi$ -band structure characteristic of free-standing graphene. We correlate the existence of graphenelike  $\pi$  bands with the appearance of the  $\pi$  plasmon in electron-energy loss spectroscopy (EELS).

Although it perhaps seems counterintuitive to attempt to improve the conductivity of a structure by oxidation, the formation of a  $SiO<sub>2</sub>$  decoupling layer between the graphene and the SiC substrate has a fair amount of *a priori* thermodynamic and kinetic plausibility. The free energy of formation of  $SiO<sub>2</sub>$  is more negative than that of  $CO<sub>2</sub>$  by approximately 100 kcal/mole at 500 K. Thus if a buffer layer/SiC structure

were oxidized under such conditions so as to achieve thermodynamic equilibrium, essentially all of the oxygen reacted would be in the form of  $SiO<sub>2</sub>$ . In addition, it has been shown that  $SiO<sub>2</sub>$  can be formed on SiC via exposure to oxygen at relatively modest temperatures.<sup>18</sup> Of course, this offers no guarantee that the desired structure can be synthesized since equilibrium is not achievable under practical oxidation conditions. In fact, the equilibrium products of SiC oxidation are undesirable, as graphitic carbon, presumably highly disordered, would be produced in equimolar amounts to the  $SiO<sub>2</sub>$ . Instead the ideal to be sought is a kinetic regime in which graphene remains inert to the oxidant (e.g.,  $O_2$ ), SiC is oxidized to produce sufficient  $SiO<sub>2</sub>$ , and the carbon liberated from the oxidation of SiC is oxidized and carried away. While these requirements appear quite stringent, graphene is well known for its chemical inertness, and, as we show, no more than the equivalent of one or two monolayers of  $SiO<sub>2</sub>$ is required to decouple the film from the substrate. In addition there is precedent for oxidation selectivity such as we desire between the graphene and the nascent carbon formed by SiC oxidation: when carbon nanotubes are grown from alcohol precursors it is believed that one of the roles of the oxygen is to scavenge any amorphous carbon formed in the pyrolytic process.<sup>19</sup> In what follows we describe a lowtemperature, high-pressure oxidation process that accomplishes these goals, decoupling the buffer layer from the SiC substrate and restoring the  $\pi$  bands to a substantially unperturbed condition.

The synthesis of epitaxial graphene layers on  $SiC(0001)$ was carried out in an ultrahigh vacuum system equipped with low-energy electron microscopy (LEEM).<sup>11-[13](#page-3-9)</sup> The graphene layers were formed at elevated temperature while the surface was imaged with LEEM. Details on the preparation of clean, flat  $SiC(0001)$  samples are given elsewhere.<sup>11[,12](#page-3-8)</sup> Two different synthesis approaches were used, yielding essentially identical results. In the first process, the sample is annealed above 1300 °C in a background pressure of disilane until the SiC decomposes, creating 1–3 ML of carbon in a controlled manner. In the second process, a small amount of ethylene (e.g.,  $1 \times 10^{-7}$  Torr) is added to the disilane background below the temperature at which SiC decomposes. A buffer layer film limited to a single carbon layer can be formed with this CVD approach. This latter method has the advantage that the CVD process is self-limiting, yielding a single graphene

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FIG. 1. (Color online) Electron-energy loss spectra recorded from (a) a thick graphite flake placed on SiC(0001) (black), (b) a graphene buffer layer on SiC(0001) before oxidation (blue/dark gray), and (c) the same buffer layer after oxidation (red/gray). All spectra were recorded using 33 eV electrons.

buffer layer, with no possibility of producing additional graphene, which would complicate the analysis of the experiments. However, the nucleation rate of the graphene buffer layer during CVD growth is difficult to control and the domain size of the CVD films can be significantly smaller than that of films grown by thermal decomposition. After synthesis, the graphene layer thickness was verified using the LEEM reflectivity method developed by Hibino *et al.*[20](#page-3-17) The LEEM instrument employed for these experiments includes an energy filter, enabling us to obtain EELS spectra *in situ* from the same area of the surface that is imaged. $2<sup>1</sup>$  A focused ion-beam system was used to mill out alignment marks on the SiC substrate before graphene synthesis, which allowed us to obtain EELS spectra and images from a specific area of the surface, remove the sample from the LEEM chamber for oxidation, return it to the LEEM chamber and collect LEEM images and EELS spectra from exactly the same area of the sample. For reference, an EELS spectrum from an exfoliated graphite flake placed on  $SiC(0001)$  is shown in Fig. [1.](#page-1-0)

The incident electron energy was 33 eV and the scattering geometry was such that both the incident and scattered beams were approximately normal to the surface. The feature near 6.2 eV loss energy corresponds to the surface  $\pi$  plas-mon of graphite.<sup>22–[24](#page-3-20)</sup> A spectrum obtained under identical scattering conditions from a graphene buffer layer synthesized on SiC via CVD is also shown. As expected, owing to the disruption of the graphene  $\pi$  bands, no plasmon loss features are observed, confirming that the electronic structure of the covalently bonded buffer layer is different from that of graphene.

Following LEEM image collection and selected area EELS characterization, the sample was removed from the LEEM and atomic force microscopy (AFM) images were obtained from the same area in which the EELS spectra were obtained. It was then introduced into an oxidation chamber connected to an x-ray photoemission (XPS) spectrometer. The sample was oxidized in 1 atm of  $O_2$  at 250 °C for 5 s. The effect of the oxidation on the sample was characterized using XPS, as illustrated in Fig. [2.](#page-1-1)

In each panel, the bottom spectra show the Si 2*p* and C 1*s* core levels of the buffer layer sample after growth but prior

OIDA *et al.* PHYSICAL REVIEW B **82**, 041411(R) (2010)

<span id="page-1-1"></span>

FIG. 2. XPS spectra of the (a) Si  $2p$  and (b) C 1*s* core levels from a buffer layer grown on SiC(0001). The bottom spectrum in each panel is from the buffer layer before oxidation. The middle spectra are from the buffer layer after oxidation. The top spectra are from a thick graphene film grown on SiC(0001) via hightemperature sublimation.

to oxidation. The Si 2*p* region shows a single peak corresponding to Si in SiC. (Some slight tailing to higher binding energy is observed due to a small amount of oxide present on this air-exposed sample.) The C 1s region shows a peak at 283.42 eV binding energy corresponding to carbidic carbon and a second peak shifted to higher binding energy by 1.81 eV, corresponding to buffer layer carbon. After oxidation, the spectra in the middle rows are obtained. The Si 2*p* spectrum shows a weak satellite feature at higher binding energy, which we attribute to the formation of oxidized Si. The O 1*s* spectrum, not shown, confirms the uptake of oxygen by the system.) From the area of the weak satellite feature, we estimate the thickness of this oxidized layer to be surprisingly thin: no more than a few angstroms. Ion-scattering measurements in which the oxide thickness is precisely determined are described below. For these oxidation conditions, the growth of the oxide saturates within seconds and there is little qualitative difference between samples oxidized for 5 s or 1 h. In the C 1*s* spectrum, the graphene buffer layer peak has shifted by approximately 0.26 eV toward lower binding energy with respect to the carbidic carbon peak. This is hardly surprising, since, as we shall show below, the valence electronic structure of the graphene layer has undergone a dramatic change. However we note that the graphenic carbon intensity is unchanged from the unoxidized sample, indicating that within sensitivity of the XPS measurements (about 5%) the graphene layer is not chemically attacked by the oxygen. We note that if the oxidation is carried out at the same pressure but at higher temperatures (e.g., 300 $\degree$ C) the graphene is significantly etched.

In addition to the changes in shape in the Si 2*p* and C 1*s* spectra upon oxidation, both spectra shift rigidly to lower binding energy. This is indicative of a band bending effect caused by the introduction of negative charge in the surface region. We compare this oxidation-induced band bending of the middle row of Fig. [2](#page-1-1) with the band bending resulting from growing graphene multilayers on SiC made via SiC decomposition, shown in the top panel. We note that the band bending induced by adding electrically active graphene onto the surface, shown in the top panel, is similar to the band bending produced by low-temperature oxidation. An obvious interpretation of this coincidence is that following

## DECOUPLING GRAPHENE FROM SiC(0001) VIA OXIDATION

## ) VIA OXIDATION **PHYSICAL REVIEW B 82**, 041411(R) (2010)

<span id="page-2-0"></span>

FIG. 3.  $[(a)$  and  $(c)]$  28 eV LEED patterns and  $[(b)$  and  $(d)]$ AFM images recorded from a graphene buffer layer on SiC(0001)  $[(a)$  and  $(b)]$  before and  $[(c)$  and  $(d)]$  after oxidation of the SiC substrate. The AFM images are nearly identical while the diffraction patterns show that strong coupling to the SiC lattice is lifted by the oxidation.

oxidation the graphene has become electronically decoupled from the substrate, and has become electrically active, exhibiting roughly the same local chemical environment (e.g., doping level) as few-layer graphene. After oxidation, subsidiary experiments were performed which showed that flash heating to 1200 °C causes the oxide to decompose. Analysis of the C 1*s* spectra of these samples before and after flashing shows no significant difference in the intensities of the graphenic carbon peaks before and after flashing, indicating that the graphene layer is unaffected by this process, an observation we shall exploit below.

Selected-area low-energy electron diffraction (LEED) and *ex situ* AFM images, recorded from the same area of the surface before and after oxidation, are shown in Fig. [3.](#page-2-0) The AFM images are virtually identical, demonstrating that the graphene is not consumed during the oxidation of the substrate. Note that the defect features, such as the small holes in the graphene layer (arising from the high nucleation rate of the graphene during CVD), have identical shapes and sizes before and after oxidation. This "edge graphene" would certainly be the most reactive feature of the buffer layer and even it is apparently unaffected. In addition, we observe no features which could be attributed to silicon oxide on the surface (e.g., hillocks or protrusions), which suggests that the oxide formed is subsurface, as desired. While AFM suggests no significant change in the surface morphology, the LEED patterns are quite different, suggesting a decoupling of the buffer layer from the substrate. Before oxidation, the expected  $6\sqrt{3} \times 6\sqrt{3}$  diffraction pattern of the buffer layer is observed. The fractional-order spots arise from double diffraction from the SiC(0001) and graphene lattices,<sup>9</sup> consistent with a strong coupling of the graphene buffer layer to the substrate. However, following oxidation, the fractionalorder spots are extinguished. The pattern corresponds to a

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FIG. 4. (Color online) Medium-energy ion-scattering spectra for graphene/SiC(0001) (a) before and (b) after oxidation. Contributions from carbon, silicon, and oxygen have been shifted and plotted on the same depth scale. After oxidation, the oxygen leading edge occurs deeper than the carbon edge, showing that the oxygen is subsurface.

superposition of diffraction from graphene and from SiC(0001), indicative of a weaker coupling to the substrate, e.g., due to the formation of a thin amorphous oxide layer between the graphene layer and the substrate. Similar changes in the LEED pattern are observed during H intercalation.<sup>17</sup>

EELS spectra recorded after oxidation suggest that the buffer layer has adopted the electronic structure of graphene. The EELS spectrum [Fig.  $1(c)$  $1(c)$ ] exhibits a loss feature at 6.2 eV, where none was present before [Fig.  $1(b)$  $1(b)$ ]. This feature occurs at the same energy as the  $\pi$ -plasmon feature seen on the graphite flake [Fig.  $1(a)$  $1(a)$ ]. From this observation we conclude that the oxidation process has indeed decoupled the buffer layer from the substrate and restored the graphenelike  $\pi$  bands.

The XPS and AFM data suggest that the oxidation process results in a very thin oxide layer under the buffer layer. In order to directly determine both the oxygen content and the location of the oxygen relative to the graphene, we performed structural measurements using medium-energy ion scattering  $(MEIS).^{25}$  $(MEIS).^{25}$  $(MEIS).^{25}$  Using this high-resolution form of Rutherford backscattering, we measured the depth profiles for oxygen, carbon, and silicon, verifying that the oxygen accumulates in a thin layer *underneath* the graphene. For these experiments  $SiC(0001)$  samples with a 1-2 graphene layers were prepared via sublimation and characterized using XPS. In Fig. [4](#page-2-1) we show typical data for a sample before and after oxidation, taken using a normally incident beam of 100 keV protons. The data for each element have been replotted on a depth scale and the intensities have been normalized to the cross sections. Before oxidation [Fig.  $4(a)$  $4(a)$ ], a large surface carbon peak is seen, caused by the graphene layer. For this sample, the graphene film thickness corresponded to roughly 2 ML (the buffer layer and one additional carbon layer). Deeper into the sample, the carbon intensity drops to the same level as the subsurface silicon peak, representing the

contribution from the outermost layers of the SiC substrate. Only a small oxygen peak is observed due to ambient exposure during transfer to the MEIS system. After oxidation [Fig.  $4(b)$  $4(b)$ ], a more pronounced oxygen peak is observed. Note that the leading edge of the oxygen signal occurs deeper than the carbon edge, demonstrating that the oxygen located below the surface. The carbon peak has a slightly smaller intensity in the oxidized spectrum, which we attribute to nonuniformity in the graphene film thickness, consistent with XPS measurements which suggest that the thickness varies from 1–2 ML over this sample. Quantitative modeling of the spectrum supports the idea of an oxidesupported graphene film; we were able to accurately fit the results (solid lines in Fig. [4](#page-2-1)) with of the unoxidized sample as 1.9 layers of Gr/SiC(001) and after oxidation as 1.9 layers of Gr/3.4 Å  $SiO_2/SiC(0001)$ . Note that this derived oxide thickness assumes a stoichiometric  $SiO<sub>2</sub>$  layer, which is un-

OIDA *et al.* PHYSICAL REVIEW B **82**, 041411(R) (2010)

likely. The absolute oxygen density,  $1.5 \times 10^{15}$  cm<sup>-2</sup>, corresponds to about 1.2 oxygen atoms per SiC unit cell.

In summary, we have shown that the covalent bonding of the graphene buffer layer to the  $SiC(0001)$  substrate can be lifted by the insertion of an ultrathin  $(\sim 3 \text{ Å})$  oxide layer between the graphene and the substrate. The activated buffer layer exhibits the  $\pi$ -plasmon characteristic of graphene, showing that the band structure of graphene has been largely recovered. The low-temperature oxidation method offers potential advantages for the device fabrication. It is simple to implement, can be carried out on prefabricated devices (i.e., with metal contacts in place), and is compatible with conventional complementary metal-oxide semiconductor processes.

This work was supported by DARPA under Contract No. FA8650–08-C-7838 through the CERA program.

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