All electrical measurement of spin injection in a magnetic *p*-*n* junction diode

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Magnetic p-n junction diodes are fabricated to investigate spin-polarized electron transport. The injection of spin-polarized electrons in a semiconductor is achieved by driving electrons from a ferromagnetic injector (Fe), into a bulk semiconductor (n-GaAs) via Schottky contact. For detection, a diluted magnetic semiconductor (p-GaMnAs) layer is used. Clear magnetoresistance was observed only when a high forward bias was applied across the p-n junction.

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Spin electronics has attracted growing interest in recent years, because it is envisioned that the utility of the spin and charge properties of the electron would open new perspectives to semiconductor device technology.¹⁻⁴ One essential requirement for the spintronic devices is the efficient injection of spin-polarized carriers. Spin injection from a ferromagnetic metal into a semiconductor is attractive, because ferromagnetic metals such as Fe and Co have a relatively high Curie temperature. Spin LED structures provide a way to study the spin injection.⁵ The spin polarized carriers injected from the ferromagnetic metals radiatively recombine in the semiconductor emitting circularly polarized light. It was found that Schottky or tunneling contacts between a metallic ferromagnet and a semiconductor can overcome the conductance mismatch obstacle and show carrier polarizations up to 30%.^{6,7}

However, from a device point of view, a major breakthrough still would be to have all electronic devices. Recently, magnetic p-n junction diodes have been proposed and theoretically analyzed.^{8,9} These devices whose electronic properties depend on the spin polarization of the carriers can offer opportunities to study the effective spin injection. Moreover, the structures of magnetic p-n junction diodes could also be used to study the spin injection in indirect band gap semiconductors, such as silicon, which are inaccessible to the standard detection schemes using spin-LED structures.¹⁰ In this paper, we demonstrate the fabrication of a magnetic p-n junction diode, in which the spin injection between ferromagnetic metals and semiconductors is measured all electrically.

A sketch of the band diagram for such a magneticnonmagnetic semiconductor p-n junction in contact with a ferromagnetic metal is presented in Fig. 1(a). The device performs as follows: a positive bias is applied between the p-GaMnAs and the ferromagnetic Fe layers. This places the magnetic p-n junction in forward bias and the Fe/GaAs Schottky barrier in reverse bias. Consequently the spinpolarized electrons are injected from Fe into the bulk n-GaAs via a Schottky contact. Afterwards, the spin-polarized electrons are extracted from the n-GaAs across the depletion layer into the p-GaMnAs. If the relative magnetizations of the two magnetic electrodes are changed from parallel to antiparallel, the magnetic p-n junction diode should display a GMR-like effect.

The preparation of the hybrid structure is started from the semiconductor heterostructure, which was grown on a semi-

insulating GaAs substrate by molecular beam epitaxy at growth temperature of 630 °C. It has the following layer sequence: 300 nm GaAs buffer layer/ 300 nm AlAs-GaAs superlattice/ 100 nm GaAs/ 50 nm Al_{0.72}Ga_{0.18}As/ 15 nm n^+ -GaAs (3×10^{18} cm⁻³)/ 50 nm n-GaAs (1×10^{16} cm⁻³)/ 10 nm n^+ -GaAs (3×10^{18} cm⁻³)/ 60 nm p-Ga_{0.94}Mn_{0.06}As. In the sample structure, the 50 nm n-GaAs is used as transport region for spin-polarized electrons. The 10 nm n^+ -GaAs layer with a Si doping density of 3×10^{18} cm⁻³ leads to a smaller depletion region between the p-GaMnAs and bulk n-GaAs layer. The other 15 nm n^+ -GaAs is used to control the Schottky barrier interface resistivity to overcome the conductance mismatch between Fe and GaAs.¹¹⁻¹³ The GaMnAs layer was grown at a growth temperature of 250 °C and shows a Curie temperature of 65 K.

In order to realize the device, we use the epoxy bonding and stop-etching technique (EBASE),^{14–16} which relies on the highly selective etching of GaAs and $Al_xGa_{1-x}As$ by suitable wet chemical etchant. The fabrication steps of the sample involve conventional optical lithography and lift-off procedure. A 100 nm Au film deposited on the GaMnAs

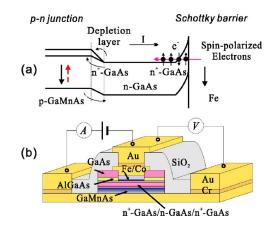


FIG. 1. (Color online) (a) Band-energy schemes for the magnetic p-n junction with a Fe/GaAs Schottky barrier. The p region (left) is magnetic GaMnAs layer, indicated by the spin splitting of the conduction band. Under applied forward bias, the spin-polarized electrons (solid circles) are injected from Fe (right) to the n-GaAs region (middle) and extracted across the depletion layer into the p region. Up and down arrows indicate the magnetizations of the two electrodes. (b) The cross section of the device geometry with four-probe measurements.

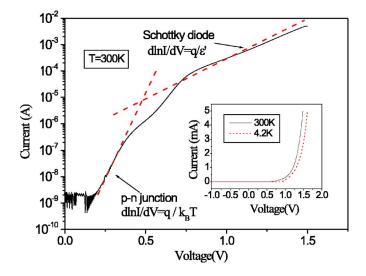


FIG. 2. (Color online) Logarithmic plot of forward *I-V* characteristic of the magnetic p-n junction diode at room temperature. The dashed lines represent the theoretical slopes of the curve, indicating the *I-V* characteristic dominated by the p-n junction and Schottky diode, respectively. The inset shows the *I-V* curves of the device at 4.2 K (dashed line) and room temperature (solid line).

layer is used as contact to the soft magnetic electrode. Before depositing the second contact, the original sample is inverted and epoxy bonded onto a new semi-insulating host substrate and cured at 80 °C for 4 hours. The original substrate and the 300 nm thick superlattice are removed then. After the 100 nm GaAs and 50 nm AlGaAs layers are selectively etched by the citric acid and 1% HF, respectively, the sample is transferred to the sputtering system immediately. The GaAs semiconductor surface is treated with H⁺ plasma to remove the oxide layer, then a 12 nm Fe layer and a 50 nm Co magnetic pinning layer¹⁷ are deposited on the n-GaAs layer as a hard magnetic electrode. Finally, photolithographic definition of a mesa structure provides access to the bottom voltage probes and the thick SiO₂ film deposition is used for electrical isolation. The cross section of the whole device for four-probe measurements is shown in Fig. 1(b). Due to the extreme selectivity of HF ($\geq 10^7$), the transport length of bulk *n*-GaAs was precisely defined by MBE growth only.¹⁸ The electric and magnetotransport characterizations of the ferromagnet based magnetic-nonmagnetic *p*-*n* junction were carried out at room temperature and at 4.2 K employing an HP semiconductor analyzer 4155A. The sample was mounted in a ⁴He cryostat with a superconducting coil and the magnetic field was aligned in the plane of the hybrid structure.

The *I-V* curves of the magnetic *p-n* junction diode measured at room temperature and 4.2 K are shown in the inset in Fig. 2. If we look closer at the logarithmic plot of the current vs applied voltage, the different slopes of the curve can be found, see Fig. 2. The device studied here can be treated as a stack of a *p-n* junction and a Schottky diode. For the *p-n* junction, the current can be expressed as $J = J_{s1}[\exp(qV_1/k_BT) - 1]$, where J_{s1} is the saturation current density, V_1 is the bias for the *p-n* junction, *q* is the magnitude of electronic charge, k_B is Boltzmann's constant and *T* is the

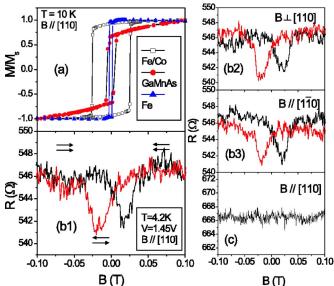


FIG. 3. (Color online) (a) SQUID magnetic measurements of the GaMnAs, Fe, and Fe/Co films at 10 K. (b) Magnetoresistance of the device (*p*-GaMnAs/*n*-GaAs/Fe/Co) plotted as a function of magnetic field in the plane. The in-plane magnetic field is along [110] (b1), perpendicular to [110] (b2) and along $[1\overline{10}]$ (b3). The alignment of the electrodes is indicated by arrows in (b1). (c) Magnetoresistance curve of the reference sample (*p*-GaMnAs/*n*-GaAs/Fe). Constant resistance is observed.

absolute temperature. Since a heavily doped n^+ -GaAs with a doping density of $N_d=3 \times 10^{18} \text{ cm}^{-3}$ was used for the Schottky contact, the tunneling of electrons through the barrier plays an important role in the transport process. For the Schottky diode under reverse bias, the current density can be expressed as $J=J_{s2} \exp(qV_2/\varepsilon')$, where J_{s2} is the saturation current density and V_2 is bias for the Schottky diode.¹⁹ From the definitions $\varepsilon' = E_{00} [(E_{00}/k_B T - \tanh E_{00}/k_B t)]^{-1}, E_{00}$ $=(qh/4\pi)(N_d/m^*\epsilon)$, where m^* is the effective mass and ϵ is the dielectric constant, we obtain $\varepsilon' = 78.5$ meV. Since ε' is 3 times as large as k_BT at room temperature, the voltage drop over the Schottky barrier increases faster than the voltage drop over the p-n junction, for increasing current in the device. At low voltage, the resistance of the Schottky barrier is much lower than that of the p-n junction. Consequently, the *I-V* characteristic is dominated by the p-n junction with a slope equal to q/k_BT . When the voltage is increased, the resistance of the Schottky barrier becomes comparable to the *p-n* junction and cannot be neglected, and at high voltage, the slope equals to q/ε' as shown in Fig. 2.

Figure 3(a) shows the magnetic hysteresis loops at 10 K (60 nm), (22 nm), for the GaMnAs Fe and Fe (12 nm)/Co (50 nm) layers. Using a Co layer to magnetically bias the Fe film, the coercivity of the 12 nm Fe layer is 30 mT, while the GaMnAs layer shows a coercivity of 3 mT. In the magnetic field range between these coercivities, the ferromagnets' magnetization can be switched to the antiparallel state. Figure 3(b1) shows the magnetoresistance curve of the device measured at 4.2 K, the applied magnetic field is in the plane and parallel to the [110] direction of GaMnAs. The negative magnetoresistance curve coincides reasonably

well with the distinct coercive fields of the magnetization curves of GaMnAs and Fe/Co layers. With forward applied bias of 1450 mV, a negative magnetoresistance of 1.02% is found. The detailed mechanism of the negative magnetoresistance is still not clear. It is probably due to the antiferromagnetic *s*-*d* exchange in GaMnAs.²⁰ For parallel magnetic configuration, the barrier for crossing the depletion layer is larger for the spin-up electrons (majority). Therefore, we find the negative magnetoresistance in our experiments.

In order to exclude the tunneling anisotropic magnetoresistance (TAMR) effect in our results,²¹ the angle dependence of the magnetoresistance was studied. In the measurements, the angle of the in-plane magnetic field was changed with respect to the [110] crystallographic direction. Two magnetoresistance curves with the magnetic field perpendicular to the [110] and along $[1\overline{10}]$ are shown in Figs. 3(b2) and 3(b3). Since the magnetoresistance does not change for different in-plane directions, we assume that the TAMR plays no important role here. As another test of our results, measurements were made on a reference sample with a 22 nm Fe layer in place of the 12 nm Fe/50 nm Co magnetic electrode. As shown in Fig. 3(a), the Fe layer without Co pinning has a similar coercivity to the GaMnAs layer. Thus, the magnetic configurations of these two electrodes cannot switch from parallel to antiparallel. The magnetoresistance is shown in Fig. 3(c), and the value of the resistance remains constant. This measurement of the reference sample gives further validation of our results.

The bias voltage dependence of the magnetoresistance was also studied, and the results are shown in Fig. 4. We find the magnetoresistance can only be found with high forward bias on the device. Theoretical analysis of the magnetic p-njunction shows that there is no spin injection at small biases, because the injection of polarized carriers is still smaller than the equilibrium carrier density. Typically the bias should be above 1 V, therefore spin-polarized carriers can be injected across the depletion layer.⁹ Our experimental results agree very well with the theory. Furthermore, we also find a peak of the negative magnetoresistance signal of 1.27% at 1400 mV bias. This effect can be explained by the interface resistance, which was analyzed by Fert.¹³ In his model, the highest magnetoresistance is obtained in the limit $r_N(t_N/l_{sf}^N) \ll r_b^* \ll r_N(l_{sf}^N/t_N)$, where r_N is the product of the semiconductor resistivity and the spin diffusion length (l_{sf}^{N}) , t_N is the semiconductor transport length and r_h^* is the interface resistance. Since the resistivities of GaMnAs and GaAs are almost the same in our device, the interface resistivity is

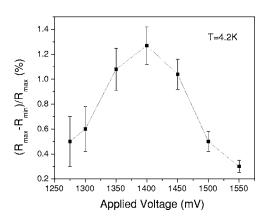


FIG. 4. Magnetoresistance signal versus voltage. The solid line is a guide to the eye.

dominated by the Schottky barrier between Fe and GaAs. When the voltage applied on the Fe/GaAs Schottky barrier varies, the interface resistivity is increased or decreased accordingly, hence the magnetoresistance reaches a maximum value in between. Currently, the magnetoresistance in our experiments is small, it attributes to the smaller conduction band spin splitting comparing to the valence band in GaMnAs. On the other hand, theoretical calculations pointed out that the spin injected by the biasing electrode in the magnetic p-n junction diode would be lower. Because in such a diode, the current is carried by the minority carriers, so that only a small spin current can build up the source spin. As a result, the spin injected from the ferromagnetic contact has a small effect on the charge properties of the diode, hence the magnetoresistance.⁹ Furthermore, the interdiffusion between the ferromagnetic metal and semiconductor may also play an important role in decreasing the magnetoresistance,¹⁵ which will be improved in our further experiments.

In conclusion, we have fabricated a magnetic p-n junction diode, in which the spin injection from ferromagnetic metals to semiconductors can be measured all electrically. Our study shows the spin-polarized electrons can only be injected when a high forward bias is applied on the p-n junction, which agrees well with the theoretical prediction. Furthermore, the bias dependence of the magnetoresistance has also been discussed.

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