Simulation of I-V hysteresis branches in an intrinsic stack of Josephson junctions in high- T_c superconductors

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I-V characteristics of the high- T_c superconductor Bi₂Sr₂Ca₁C₂O₈ shows a strong hysteresis, producing many branches. The origin of hysteresis jumps is studied by use of the model of multilayered Josephson junctions proposed by one of the authors (T.K.). The charging effect at superconducting layers produces a coupling between the next-nearest-neighbor phase differences, which determines the structure of hysteresis branches. It will be shown that a solution of phase motions is understood as a combination of rotating and oscillating phase differences, and that, at points of hysteresis jumps, there occurs a change in the number of rotating phase differences. Effects of dissipation are analyzed. The dissipation in insulating layers works to damp the phase motion itself, while the dissipation in superconducting layers works to damp relative motions of phase differences. Their effects to hysteresis jumps are discussed. [S0163-1829(99)04529-4]

I. INTRODUCTION

I-V characteristics of the high- T_c superconductor $Bi_2Sr_2Ca_1C_2O_8$ (BSCCO) shows a strong hysteresis, producing many branches.^{1–3} Many experiments have been reported to investigate properties of hysteresis branches,^{4–10} and it has turned out that there is rich physics in properties of *I-V* characteristics. Substructures are found in hysteresis branches, and are discussed in relation with phonon effects.⁸ The *d*-wave effect is also discussed in higher voltage regions, where the quasiparticle tunneling current becomes important.^{6,10}

The present paper is concerned with the origin of hysteresis jumps forming the main branches. Instead of attributing the origin of hysteresis branches to inhomogeneity of critical currents, there are several theoretical attempts to regard its origin as an intrinsic property of strong anisotropy in high- T_c superconductors. High- T_c superconductors have a layered crystal structure. Specifically, BSCCO has a strong anisotropy. Then a model of multilayered Josephson junctions, which has an alternating stack of superconducting and insulating layers, is considered as a proper model to describe electronic properties of those systems. One of the main differences from the case of a single junction is the appearance of an interlayer coupling among phase differences. Since the thickness of the superconducting layer is of the order of 3-6Å , influence among adjacent junctions is not negligible.

To explain the main branch structure, the following mechanisms are proposed. A mechanism of the interlayer coupling induced by the charging effect at the superconducting layer has been proposed by one of the authors (T.K.),¹¹ and a branch structure of *I*-*V* characteristics is obtained.¹² It

is pointed out in Ref. 13 that a nonequilibrium effect in superconducting layers also mediates an interlayer coupling, and *I-V* characteristics are discussed.^{14,9} When the magnetic field is applied, the inductive coupling becomes important, and its effects have been discussed.^{4,15–18}

In this paper, we investigate in detail the scheme of hysteresis jumps appearing in main branches without magnetic field. Since the inductive coupling among adjacent junctions does not exist, the charging effect at superconducting layers becomes important. A charging at a superconducting layer induces the change of electric fields in the neighboring insulating layers and induces an interlayer coupling, since the time derivative of the phase difference is related to the voltage. We will show that the origin of the multibranch structure is the nonlinear effect among phase differences through the charging effect. Various disturbances, such as dissipation in insulating layers, dissipation in superconducting layers, noise, and a surface proximity effect, affect schemes of hysteresis jumps. We will investigate how the schemes of hysteresis jumps are affected by the above-mentioned disturbances. The dissipation in the superconducting layer will be introduced phenomenologically through the charge relaxation. Schemes of hysteresis jumps are obtained by increasing applied current gradually up to certain values and then decreasing. Obtained equations show that the resistive dissipation in the insulating layer works to damp the motion of phase difference in each junction, while the charge relaxation in the superconducting layer works to damp relative motions of phase differences among neighboring junctions. This fact affects the formation of hysteresis jumps in the process of increasing and decreasing applied current. It will turn out that the dissipation in superconducting layer and noise enhance the occurrence of jumps in both increasing and de-

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creasing current, although the main branch structure is determined by the coupling through the charging effect. By comparing the result of numerical simulation with the experiment of a small stack,⁷ we will show the validity of the present model.

In the next section, the necessary formula for calculation is summarized. The dissipation in superconducting layers is introduced phenomenologically. In Sec. III, results of numerical simulation are presented. The origin of hysteresis jumps is investigated. It will be shown that, at a position of a jump, a stationary solution changes its form. We present a comparison with the experiment of Ref. 7 and show validity of the charging mechanism. Section IV is devoted to the conclusion.

II. JOSEPHSON-JUNCTION STACKS

In this section we summarize the multi-Josephsonjunction model of high- T_c superconductors.¹¹ Let us consider the N+1 superconducting layers, numbered from 0 to N. We denote the gauge-invariant phase difference of the (l-1)th and *l*th superconducting layer by $\varphi(l)$, and voltage by V(l). The widths of the insulating and superconducting layers are denoted by D and s, respectively. At the edges, the effective width of the superconducting layer may be extended due to the proximity effect into attached lead metals. The widths of the zeroth and Nth superconducting layers are denoted by s_0 and s_N , respectively. We assume that physical quantities are spatially homogeneous on each layer. This assumption is applicable in cases of no applied magnetic field and small sample size in the a-bdirection. According to the procedure presented in Ref. 11, we have the following equation for the total current J flowing through each junction as

$$\frac{J}{J_c} = j_c(l)\sin\varphi(l) + \beta \frac{V(l)}{V_0} + \frac{1}{\omega_p} \frac{\partial}{\partial t} \frac{V(l)}{V_0}.$$
 (1)

The current J is normalized by the critical current J_c , $j_c(l) = J_c(l)/J_c$ with $J_c(l)$ being the critical current for the *l*th junction. The time t is normalized by the inverse of the plasma frequency, $\omega_p = \sqrt{(2e/\hbar)(4\pi DJ_c/\epsilon)}$, with ϵ being the dielectric constant of the insulating layer. The voltage V(l) is normalized by $V_0 = \hbar \omega_p/2e$. The parameter β is given by $\beta = \sigma V_0/J_c D$. In this paper we assume

$$j_c(l) = 1 \text{ for all } l, \tag{2}$$

for simplicity. The relation between the time derivative of phases and voltages at junctions is given by

$$\frac{1}{\omega_p} \frac{\partial}{\partial t} \varphi(l) = \sum_{l'=1}^N A_{ll'} \frac{V(l')}{V_0}.$$
(3)

The matrix A is given by

where

$$\alpha = \frac{\mu^2 \epsilon}{sD},\tag{5}$$

with μ^2 being given by the relation between the charge density and scalar potential A_0 as¹⁹

$$4\pi\rho = -\mu^{-2} \left(A_0 + \frac{\hbar}{2e} \frac{\partial}{\partial t} \phi \right), \tag{6}$$

where ϕ is the phase of the order parameter defined on the superconducting layer. In the present analysis, we assume that the current flows only along the *c* axis, say, the *z* direction. Since the current j_z in superconducting layers is given by

$$\frac{4\pi}{c}j_z = -\lambda^{-2} \left(A_z - \frac{\hbar c}{2e} \frac{\partial}{\partial z} \phi \right), \tag{7}$$

the current conservation law leads to

$$\mu^{-2} \frac{1}{c} \frac{\partial}{\partial t} A_0 + \lambda^{-2} \frac{\partial}{\partial z} A_z = 0, \qquad (8)$$

under the condition

$$\left[\mu^{-2}\left(\frac{1}{c}\frac{\partial}{\partial t}\right)^2 - \lambda^{-2}\left(\frac{\partial}{\partial z}\right)^2\right]\phi = 0.$$
(9)

The gauge condition (8) is called the phason gauge,¹⁹ and is determined in such a way that the response of the current and charge to the space-time variation of phase appears in a gauge-invariant form. It is shown in Ref. 13 that a nonequilibrium effect caused by the tunneling current induces the dissipation effect in μ^{-2} . Since the superconducting layer is

thin, the current also receives effects from boundaries between superconducting and insulating layers, which may also work as dissipation to the superconducting current. When the effect of the *d*-wave superconductivity is considered, one also expects certain dissipation due to the presence of the gapless region. Such effects have not been studied much yet. Then we introduce phenomenologically the dissipation in a superconducting layer in a Lorentz form as

$$\mu^{-2} = \mu_0^{-2} \frac{1}{1 - i\omega\tau}.$$
 (10)

This corresponds to the following phenomenological form of charge relaxation:

$$\frac{\partial}{\partial t}\rho = -\frac{1}{\tau} \left[\rho + \frac{\mu_0^{-2}}{4\pi} \left(A_0 + \frac{\hbar}{2e} \frac{\partial}{\partial t} \varphi \right) \right]. \tag{11}$$

By considering the expression α in Eq. (5), we replace α by

$$\alpha \to \alpha (1 - i \,\omega \,\tau), \tag{12}$$

with α being given by Eq. (5) replacing μ^2 by μ_0^2 . From Eqs. (1) and (3), we have

$$\frac{1}{\omega_p^2} \frac{\partial^2}{\partial t^2} \varphi(l) = \sum_{l'} \left(A_{ll'} + A_{1ll'} \tau \frac{\partial}{\partial t} \right) \left[\frac{J}{J_c} - j_c(l') \sin \varphi(l') \right] -\beta \frac{1}{\omega_p} \frac{\partial}{\partial t} \varphi(l),$$
(13)

where A is the matrix A in Eq. (4) and A_1 is given as

$$A_{1} = \begin{pmatrix} \alpha \left(1 + \frac{s}{s_{0}} \right) & -\alpha & 0 & \cdots \\ -\alpha & 2\alpha & -\alpha & 0 & \cdots \\ 0 & -\alpha & 2\alpha & -\alpha & 0 & \cdots \\ & & & & \\ & & & & \\ & & & \\ &$$

As the above derivation shows, the charging effect at superconducting layers is the main mechanism inducing the interlayer coupling through the electromagnetic interaction, when the spatial homogeneity in the *a-b* plane is satisfied. A charge at a superconducting layer modifies electric fields in the neighboring insulating layers, and the change of electric fields affects the time derivative of phase differences. Phonon effect considered in Ref. 8 may be introduced through the frequency dependence of the dielectric constant ϵ , which is not considered in this paper. Equation (13) means that the dissipation in the insulating layer works as a damping of phase motions at each junction, and that the dissipation in the superconducting layer works as a damping of the relative phase motion. In the next section, we present results obtained by numerical simulation of Eq. (13).

III. RESULTS OF NUMERICAL SIMULATION

We have performed numerical simulations of Eq. (13) by use of the fifth order of the predictor corrector method. In all calculations, we choose the parameters $\alpha = 1.0$, $\beta = 0.2$. The time step dt is chosen as $\omega_p dt = 1.0 \times 10^{-3}$.

The parameter β is related to the conductivity of the insulating layer. Although it works to lead a system into a stationary state and determines the slope of the *I-V* characteristics, its effect to a scheme of hysteresis jumps is not sensitive in this parameter range. The parameter α plays an important role in determining hysteresis branches due to nonlinear effects among phase differences. The value of α in BSCCO was estimated in Ref. 11 as $1 < \alpha < 3$ and, by use of $\epsilon = 25$ for La_{2-x}Sr_xCuO₄, the value of $\alpha = 2.7$ was obtained. There is a discussion that ϵ for BSCCO may be smaller, and from a microscopic theory, α is estimated to be a much smaller value of 0.2.²⁰ Also it was reported that the equal spacing of *I-V* branches observed in experiments of BSCCO is reproducible, with a smaller choice of $\alpha \le 1.^{21}$ Here we use a typical value $\alpha = 1.0$.

By the use of Eqs. (3) and (1), the relation for obtaining a voltage for each junction is written as

$$\frac{1}{\omega_p} \frac{\partial}{\partial t} \varphi(l) = \sum_{l'} \left\{ (A - \tau \beta A_1)_{ll'} \frac{V(l)}{V_0} + \tau A_{1ll'} \left[\frac{J}{J_c} - j_c(l') \sin \varphi(l') \right] \right\}.$$
 (15)

The average of voltage $\overline{V}(l)$ are given by

$$\bar{V}(l) = \frac{1}{T_{max} - T_{min}} \int_{T_{min}}^{T_{max}} dt V(l).$$
(16)

The total dc voltage V are obtained by

$$V = \sum_{l=1}^{N} \bar{V}(l). \tag{17}$$

In Fig. 1, *I-V* characteristics are presented for N=10, $s_0/s=s_N/s=2$, $\tau\omega_p=0.1$. The current *J* is gradually in-



FIG. 1. *I-V* characteristics. Parameters are $\alpha = 1.0$, $\beta = 0.2$, N = 10, $s_0/s = s_N/s = 2.0$, and $\omega_p \tau = 0.1$.

creased with the step $dJ/J_c=0.01$ up to $J/J_c=2.0$, and then gradually decreased. The *I*-*V* curve shows the jump at J/J_c = 1.0 and, after two jumps, it linearly increases up to J/J_c = 2.0. In the current decreasing process, there appear three jumps when the current becomes smaller than $J/J_c=0.4$.

In Fig. 2, we show the branch structure in the I-V characteristics corresponding to Fig. 1. This is obtained by decreasing the current when a jump occurs in the current increasing process, and by increasing the current when a jump occurs in the current decreasing process. It should be noted that the positions of jumps labeled by 1 and 5 lie on the same branches, while the positions 2 and 4 are close but lie on the different branches.

In order to understand this branch structure, we show, in Fig. 3, the voltage distributions on each junction just after the jumps. Figure 3(a) is for the current increasing process and Fig. 3(b) is for the current decreasing process. For the line labeled 1, two junctions at the edges have higher voltage. The solution shows that the phases with higher voltages increase approximately linearly in time, corresponding to the phase-rotating motion, while other junctions have oscillating phase motions. In fact, when the time average of $\partial \varphi(l)/\partial t$ is plotted in Fig. 4, junctions with rotating and oscillating phases are clearly identified. As can be seen in Fig. 3, the number of junctions with rotating phase increases as 2, 4, and 6 due to the symmetric arrangement. The number of



FIG. 2. *I-V* characteristics and branch structure. Parameters are $\alpha = 1.0$, $\beta = 0.2$, N = 10, $s_0/s = s_N/s = 2.0$, and $\omega_p \tau = 0.1$.



FIG. 3. Voltage distribution. Parameters are $\alpha = 1.0$, $\beta = 0.2$, N = 10, $s_0/s = s_N/s = 2.0$, and $\omega_p \tau = 0.1$. (a) is for increasing current, and (b) is for decreasing current.

phase-rotating junctions is two for the voltage lines labeled by 1 and 5, four for 2 and 4. Lines 1 and 5 show the same pattern of voltage distribution and therefore form the same branch in Fig. 3, while lines 2 and 4 show a different pattern, though the number of the phase-rotating junctions are the same.

With this analysis, we see that the hysteresis jumps are associated with the change of the distribution pattern of rotating phase motions. If the lth junction has a rotating phase,



FIG. 4. Average $(1/\omega_p)(\partial \varphi/\partial t)$ distribution. Parameters are $\alpha = 1.0$, $\beta = 0.2$, N = 10, $s_0/s = s_N/s = 2.0$, and $\omega_p \tau = 0.1$. (a) is for increasing current, and (b) is for decreasing current.



FIG. 5. Slope *n* vs the number of rotating phases N_R . Slope *n* is defined by $\beta V/V_0 = nJ/J_c$. Parameters are $\alpha = 1.0$, N = 10, $s_0/s = s_N/s = 2.0$. Small dots correspond to all possible patterns, while larger circles are with restrictions presented in the text.

the time average of $\partial \varphi(l)/\partial t$ is constant and that of $\sin[\varphi(l)]$ is zero. If the *l*th junction has an oscillating phase, the time average of $\partial \varphi(l)/\partial t$ is zero and that of $\sin[\varphi(l)]$ is constant. Let us consider that $N_R(\geq 0)$ junctions have rotating phases. Considering the above facts, we can obtain the equation to determine the voltage-current relation from the time average of Eqs. (1) and (13) as

$$\beta \frac{V}{V_0} = N \frac{J}{J_c} - \sum_{l_0} j_c(l_0) \langle \sin \varphi(l_0) \rangle, \qquad (18)$$

$$\sum_{l_0'} A_{l_0 l_0'} j_c(l') \langle \sin \varphi(l_0') \rangle = a(l_0) \frac{J}{J_c},$$
(19)

where $\langle \cdots \rangle$ indicates time average, l_0 and l'_0 are the labels of junctions with oscillating phases,

$$a(1) = 1 + \alpha \frac{s}{s_0}, \quad a(N) = 1 + \alpha \frac{s}{s_N}, \quad a(l) = 1.$$
 (20)

As is shown in Fig. 3, even if the number of phase-rotating junctions is same, slightly different branches are formed according to the pattern of distribution of rotating phases. That is, branches are grouped by the number of phase-rotating junctions N_R and different patterns of distribution of phase-rotating junctions lead slightly shifted branches. Equations (18) and (19) are solved in a form

$$\beta \frac{V}{V_0} = n \frac{J}{J_c}.$$
(21)

In Fig. 5, we present *n* versus N_R , the number of rotating phases. The small dots are for all possible patterns. The edge junctions mostly rotate, and rotating phases have mostly oscillating phases in the next neighbors. Taking into account these facts, we have the larger circles with the restrictions, (1) pattern is symmetric, (2) two edges have rotating phases, and (3) there are no patterns with more than three successive rotating phases. From the figure we can see that branches appear roughly with equal spacing but that those at higher voltage distribute more densely. In the numerical simulation, it seems that only special patterns are realized. The simulated result in Fig. 2 gives n=3.21 for $N_R=2$, n=6.21 and 6.47



FIG. 6. *I-V* characteristics and branch structure for a finite stack. Parameters are $\alpha = 1.0$, $\beta = 0.2$, N = 10, $s_0/s = s_N/s = 2.0$. (a) $\omega_p \tau = 0.0$ without noise, (b) $\omega_p \tau = 0.1$ without noise, (c) $\omega_p \tau = 0.0$ with noise, and (d) $\omega_p \tau = 0.1$ with noise.

for $N_R = 4$, and n = 8.59 for $N_R = 6$. What stability conditions should be imposed in addition to Eqs. (18) and (19) is still an open question.

In order to see how the scheme of hysteresis jump changes with various conditions, we show, in the following, structures of branches in I-V characteristics. In these analyses, the current is increased up to $J/J_c = 2$ and decreased to zero. Branches are picked up when a voltage jump occurs. Figure 6 is for finite junction stack N=10, and $s_0=s_N$ =2.0 is chosen. The parameters are $\alpha = 1.0$, $\beta = 0.2$. In Fig. 6, (a) is for $\omega_p \tau = 0.0$ without noise, (b) is for $\omega_p \tau = 0.1$ without noise, (c) is for $\omega_p \tau = 0.0$ with noise, and (d) is for $\omega_p \tau = 0.1$ with noise. The noise is introduced through the small randomness of the time derivative of the phase at each time step, $\delta((1/\omega_p)[\partial \varphi(l)/\partial t]) = 1.0 \times 10^{-4}$. Figure 7 is for N=10 with the periodic-boundary condition. Figure 7(a) is for $\omega_p \tau = 0.0$ without noise, (b) is for $\omega_p \tau = 0.1$ without noise, (c) is for $\omega_p \tau = 0.0$ with noise, and (d) is for $\omega_p \tau$ =0.1 with noise.

When a noise is introduced, possible patterns are not necessarily symmetric, so that the number of branches increased. In addition, an instability to cause a hysteresis jump is easily enhanced. The effect of τ also enhances hysteresis jumps, which may be understood from the fact that it works as damping of the relative motion. Specifically, it enhances hysteresis jumps in the current increasing process.

In the periodic cases, this tendency is more enhanced as can be seen from comparison with Fig. 7(a) and others. In a long stack, where surface effects are less, we can say that the



FIG. 7. *I-V* characteristics and branch structure for a periodic stack. Parameters are $\alpha = 1.0$, $\beta = 0.2$, N = 10, $s_0/s = s_N/s = 2.0$. (a) $\omega_p \tau = 0.0$ without noise, (b) $\omega_p \tau = 0.1$ without noise, (c) $\omega_p \tau = 0.0$ with noise, and (d) $\omega_p \tau = 0.1$ with noise.

dissipation effect in the superconducting layer or noise play an important role in inducing hysteresis jumps.

In Fig. 8, we present the comparison with our numerical result and the experiment of Ref. 7. We choose N=8 from their arrangement of the sample. In order to explain the large extension of hysteresis branches in the current increasing



FIG. 8. *I-V* characteristics. Inset is reproduced from Ref. 7. The labels of the branches are identified with those of the inset. The numbers in the bracket are the numbers of rotating phases.

process, we assume that the critical currents J_c are reduced at the edges. We choose $J_c = 0.47$ meV, since the numerical analysis shows that the J_c is situated roughly at the middle of the second branch. From the data, we have $J_c(0)/J_c = 0.4$, $J_c(N)/J_c = 0.9$, and $J_c(l)/J_c = 1.0$. The parameter α is chosen 1.0, by assuming s=6 Å, D=6 Å, $\mu=2$ Å, and ϵ =10. In Ref. 22, it is reported that the longitudinal plasma energy is of order of 0.5 meV. Then V_0 is 0.25 meV. The β is estimated from branch 4; that is, branch 4 has four rotating phases and is on the line passing J=0.9 mA, V=60 meV. In Eq. (21) we choose n=6 from the result of Fig. 5, and β is estimated as $\beta = 0.05$. Since we do not want to introduce any other parameters, we set $s_0/s = 1.0$, $s_N/s = 1.0$. The parameter $\omega_p \tau$ is chosen as 0.5, which is obtained by trial and error to produce as many branches as possible. For example, if we set $\tau \omega_p = 0.0$, jumps to branches 3, 5, and 6 do not occur. The inset is from Ref. 7. The labels of branches correspond to those in the inset. The numbers in the brackets indicate the number of rotating phases. The values of J, at which hysteresis jumps occur, fit very well with experimental values of the inset. Hysteresis jumps are induced when certain instability develops in the nonlinear equation of Eq. (13). Though each junction, except for ones at edges, has the same J_c , a critical J for each branch is different and is controlled mainly by α . The agreement of this critical J values observed in Fig. 8 supports the validity of the present charging mechanism. In Fig. 8 branch 6 in the increasing current is short. How hysteresis jumps occur depends on the effects of the disturbances. There may be certain other noises to make the development of instability easier, depending on the experimental setup. In decreasing current, there are a few discrepancies. In numerical calculation, the I-V curve is straight, while the experiment shows a curvature. In addition, the positions of the jumps are different. Such discrepancy may relate effects of *d*-wave superconductivity, such as the presence of the quasiparticle tunneling, for example, but it is beyond the discussion of the present work.

IV. CONCLUSION

In this paper we have studied the *I-V* characteristic branches in high- T_c superconductors by the multilayered Josephson-junction model. We have shown that the charging effect in superconducting layers causes an interlayer coupling among phase differences and that hysteresis jumps are induced as a result of nonlinear effect among phase differences. The appearance of hysteresis branches is intrinsic in high- T_c superconductors. In this aspect, we note the experimental report that, even in a sample with negligible J_c inhomogeneity, a branch structure is observed.¹⁰

Due to the weak interlayer coupling arising from the charging effect, equations for phase differences form a nonlinear coupled differential equation. Its solution is classified by the number of rotating phases. The change of its number occurs intrinsically in this nonlinear equation, which leads to the formation of I-V characteristic branches. Although the number of rotating phases is the same, different patterns of distribution of rotating phases lead to slightly shifted branches. Which patterns are more easily realized among many possible patterns is still an open question and a future problem. In this system, the resistive dissipation in the insulating layer works as a damping of phase motion in each junction and the dissipation caused by the charge relaxation in superconducting layers works as damping of relative phase motions. The effects of dissipation, noise, and surface proximity to structure of hysteresis jumps in I-V characteristics are investigated. The dissipation in superconducting layer and noise enhance the occurrence of jumps in both increasing and decreasing current.

We have presented the comparison with the experiment in Fig. 8. The agreement of the critical J for each hysteresis branch shows the validity of the charging mechanism as the origin of the interlayer coupling.

Finally, we comment on effects of the *d*-wave superconductivity. The fact that the Josephson current is given by the phase difference is not modified in the *d*-wave superconduc-

tivity with a single component. Therefore, the main conclusions in the present paper are not modified. As was pointed out in the text, the *d*-wave superconductivity gives, due to the presence of a gapless region, an additional contribution to dissipation in superconducting layers and an additional quasiparticle tunneling current even in the low-voltage region. Effects of such contributions may be interesting future problems.

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