

Atomistic origin of metal versus charge-density-wave phase separation in indium atomic wires on Si(111)

Sun Kyu Song¹ and Han Woong Yeom^{1,2,*}

¹*Center for Artificial Low Dimensional Electronic Systems, Institute for Basic Science (IBS), Pohang 37673, Republic of Korea*

²*Department of Physics, Pohang University of Science and Technology (POSTECH), Pohang 37673, Republic of Korea*



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We investigate in atomic scale the electronic phase separation occurring in the well known quasi-one-dimensional (quasi-1D) charge-density wave (CDW) phase of an In atomic wire array on a Si(111) surface. The characteristic atomic scale defects, originating from excess In atoms, are found to be actively involved in the formation of the phase boundary between the metallic and the CDW phases by extensive analysis of scanning tunneling microscopy images at various temperatures. These particular defects flip the phase of the quasi-1D CDW to impose strong local constraints in the CDW correlation. We show that such local constraints and the substantial interwire CDW interaction induce local condensates of CDW and the phase separation between the metallic and the CDW phases. This work unveils the atomistic origin of the electronic phase separation, highlighting the importance of atomic scale structures of defects and their collective interaction in electronically inhomogeneous materials.

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Electronic phase separation (EPS) is the static coexistence of distinct electronic phases in a microscopic length scale at low temperature [1]. EPS has been recognized as an underlying mechanism of exotic electronic phenomena such as colossal magnetoresistance [2] and high-temperature superconductivity [3,4]. The investigation of EPS is not only crucial for understanding such exotic phenomena, but also necessary for manipulating competing electronic phases toward novel device applications. Naturally, in order to understand the emergence of EPS, the lateral microscopic investigation of electronically distinct phases is crucial. Indeed, microscopic studies have shed light on the origins of EPS in complex oxides such as strain [5–7], charge doping [8], and chemical disorder [9,10]. However, in spite of the important progress made by recent microscopic measurements [2,3,9,11–14], the understanding of the atomic scale role of individual dopants and defects has been largely limited [15]. This is due partly to the lack of measurements using a proper spectroscopic probes with atomic scale resolution and partly to multiple degrees of freedom intertwined in EPS for complex materials. For these reasons, the investigation of EPS in a simple model system with an atomic scale resolution can provide a new insight into EPS-related phenomena.

In this context, we focus on the coexistence between a metallic state and an insulating charge-density-wave (CDW) state [16–21]. The EPS in quasi-two-dimensional (quasi-2D) chalcogenide systems was reported to be induced by pressure [18], disorder [22], doping [19], or external excitations [21,23], and has been extensively discussed for novel electronic phenomena [16,21] and functionalities [23–25]. These

systems, however, also have various degrees of freedom involved including charge, lattice, spin, and strong electron correlation. On the other hand, the formation of EPS in a unique quasi-1D CDW system of an In atomic wire array on Si(111) can be a simpler model system without any magnetic ordering and substantial electron correlation involved [26]. The previous studies revealed that the existence of EPS in this system was stabilized by adsorbates [17] or vacancy defects [20]. However, the microscopic origin of EPS is not sufficiently clear since the previous studies did not provide detailed atomic scale connection between the phase landscape and individual adsorbates or vacancies.

In this paper, we investigate the atomic scale phase boundaries formed on the electronically phase separated In/Si(111) system using variable-temperature scanning tunneling microscopy (STM). We identify that two characteristic atomic scale defects, which are the most popular defects in this system, are actively involved in the phase boundaries. Atomically resolved STM images reveal that the local constraints on the CDW imposed by the characteristic atomic structure of the defects under the interwire CDW coupling hinders the formation of CDW domains, through which the electronic phase separation emerges. The atomic scale mechanism of EPS in a quasi-1D CDW system is, thus, unambiguously pinned down, and can be considered in various EPS systems based on charge orderings. These results point out the importance of detailed atomic scale measurements of phase boundaries and defects in understanding inhomogeneous electronic systems.

Our experiments were performed by a commercial ultrahigh vacuum STM equipped with a Joule-Thomson type cryostat (SPECS, Germany) at between 78 and 120 K. An *n*-type flat Si(111) substrate was cleaned by flash heating to 1500 K a few times. To grow the defective In atomic wire

*yeom@postech.ac.kr

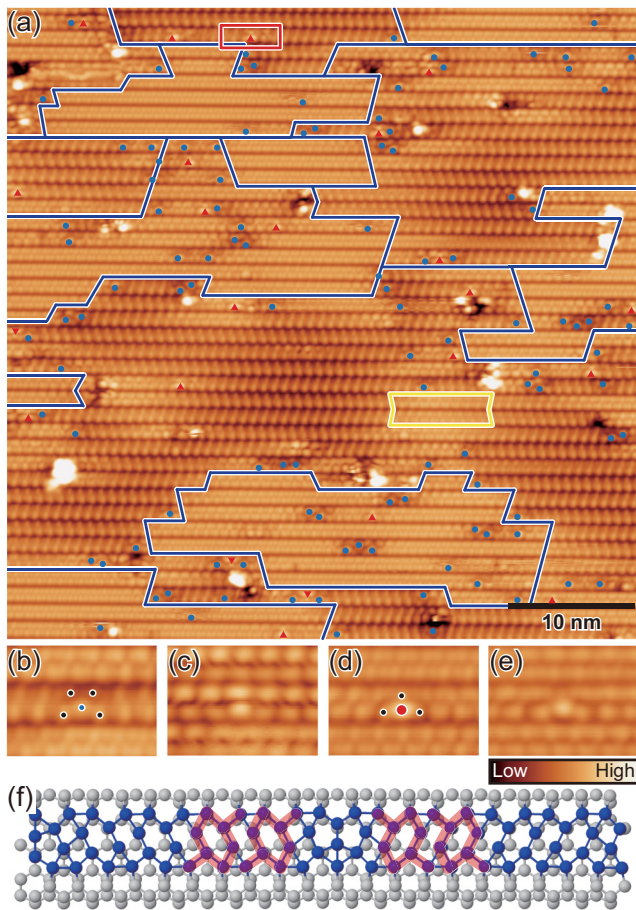


FIG. 1. (a) STM topographic images ($V_s = -0.5$ V) of the electronic phase separation in the In/Si(111) surface taken at 78 K with its pristine CDW transition at 130 K. Blue lines indicate domain boundaries between the metallic (bright) and the insulating CDW (dark) phases. The yellow box indicates two chiral solitons clustered. Blue dots and red triangles indicate characteristic the atomic-scale defects zoomed in (b) and (d), respectively [their empty state images with $V_s = 0.5$ V are shown in (c) and (e), respectively]. Dots indicate local structures of both defects for filled states. (f) Atomic structure model of the “M”-shaped defect shown in (b) and (c), which contains one excess In atom [27]. Blue (gray) balls indicates the indium (silicon) atoms. Red lines indicates the In hexagon structure characteristic of the CDW state.

array on Si(111), In atoms with a coverage greater than the optimum of one monolayer were thermally deposited on the clean Si(111) 7×7 surface kept at 600 K [26]. After deposition, the Si(111) 4×1 In surface was confirmed with low energy electron diffraction and STM. All STM images were obtained in the constant-current mode with the tunneling current of $I_t = 100$ pA and the sample bias of $V_s = \pm 0.5$ V for empty and filled states, respectively.

Figure 1(a) shows the electronically phase separated In atomic wire array on a Si(111) surface obtained at 78 K. The metallic 4×1 (brighter regions) and the insulating 8×2 phase coexist well below the transition temperature of 130 K. The difference of metallic and insulating domains is apparent even in the normal topographic measurement in filled states due to the clear structural difference between the two

phases and the different topographic contrast reflecting the existence of the band gap for the insulating phase. The atomically resolved STM image reveals clearly a substantial density of defects in various forms such as bright adatom clusters, dark vacancies, and atomic scale defects (blue dots and red triangles).

The EPS occurs in samples prepared with an unoptimized In coverage on purpose. In the present case, the key parameter is the density of the atomic scale defects, which is controlled by the In deposition or a high temperature post-annealing. The atomic scale defects have better defined structures compared to clusters of vacancies or adatoms. Two most well characterized and dominant atomic scale defects (“M” and “A” shapes) are zoomed in Figs. 1(b)–1(e), respectively. The populations of these defects are $\sim 69\%$ (“M”) and $\sim 17\%$ (“A”) of all defects (see Table S1 of the Supplemental Material [28]), as counted in STM images covering a large area of $250 \text{ nm} \times 250 \text{ nm}$. Note that about 90% of the defects are observed near the domain boundaries or within the metallic 4×1 domains ([28] Fig. S1). The other types of defects, the clusters of adatoms or vacancies, are located rather randomly, as indicated by a much smaller number, $\sim 41\%$, in a similar count. It straightforwardly suggests that the EPS or the stabilization of the metallic phase below the CDW transition temperature is mainly related to the atomic scale defects. In contrast, a previous STM study found that the EPS is enhanced by the extra annealing of the optimally prepared sample [20]. While this work focused on the large vacancies generated by the annealing, the direct relationship between local vacancy distributions and the domain landscape was not established at all. Instead, the density-functional theory (DFT) calculations in the same work showed that the global strain can stabilize the metallic phase [20]. However, it is not clear at all what kind of strain is actually imposed by the vacancies. We would like to point out that such an annealing would produce not only vacancies but also In adatoms, ejected during the formation of vacancies. The role of atomic scale defects for the EPS in the present system will be made unambiguous by more detailed microscopic observations discussed below.

In fact, the major atomic scale defects (“M”) in Figs. 1(b) and 1(c) were already reported as the so-called short phase-flip defect (PFD) in the previous studies [29,30]. These defects commonly force to flip the CDW unit cells along the wire with respect to an axis perpendicular to the wire due to their mirror symmetric structure. The atomic origin of the short PFD is revealed as excess In adatoms experimentally [30] and its atomic structure model was suggested by DFT calculations [27,30,31] [see Fig. 1(f)]. It is notable that both sides of this structure have almost the same atomic arrangement as one side of the indium hexagon of the CDW unit cell (see the guide lines in the figure). This arrangement and the mirror symmetric structure of the defect make possible the compact (just one atomic unit cell) matching with the neighboring CDW unit cells and the opposite CDW orientations on different sides of a defect, respectively. In terms of phonons, this defect pins the shear phonon mode between the upper and the lower In chains within a wire, which is crucial in the CDW distortion [27,32]. The shear phonon direction flips across this defect, which is the underlying mechanisms of the CDW phase pinning.

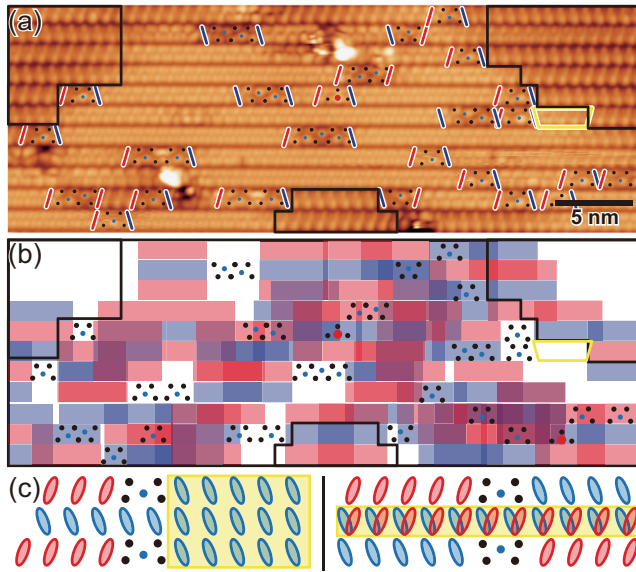


FIG. 2. (a) STM topographic images ($V_s = -0.5$ V) of a single 4×1 metallic domain and its domain boundaries with the 8×2 CDW phase (outlined by black lines) at 78 K. Dots indicate local protrusions of the atomic scale defects (PFDs) as defined in Figs. 1(b) and 1(d). Red and blue bars denote the opposite CDW orientations imposed by the PFDs. (b) Schematics of the frustrated CDW orientations due to the PFDs (indicated by the dots). Red and blue stripes indicate the presumed phase coherent segments of within a single wire with its CDW orientations fixed by the PFDs, following the orientation of red and blue bars in (a). (c) Schematics of frustrated CDW orderings to induce the electronic phase separation. Red and blue ovals indicate the CDW orientations and yellow region indicates the interwire or intrawire disorder.

In addition to this well known defect, the other atomic scale defect shown in Figs. 1(d) and 1(e) has not been actively discussed before due, probably to its small population. In clear contrast to the above defect, the CDW pinning effect of this defect is limited. Some of these defects are observed with a local CDW phase flip but the others are not ([28] Fig. S2). The origin of such a weak CDW phase pinning is not clear at present. In the following discussion, we mainly focus on the strong PFDs.

In considering the microscopic mechanism of the EPS induced by defects, we first note that the atomic scale defects discussed here are not charged defects. This can be shown by the scanning tunneling spectroscopy measurements [27], which showed that the above atomic scale defects do not shift the energy of the spectral features in their vicinity. Therefore, in order to understand the role of the defects in EPS, it is necessary to establish the detailed structural relationship between the electronic domain boundaries and the defects. As already mentioned, most of the PFDs are located at the domain boundaries between 4×1 and 8×2 phases or within the metallic 4×1 domains. In Fig. 2(a), we show a close-up of a single metallic 4×1 domain to reveal the detailed local structures of domain boundaries. One can confirm that most of the defects involved are the strong PFDs. In particular, we mark the CDW orientations as pinned by the defects with red and blue bars in Fig. 2(a) and the red and blue stripes

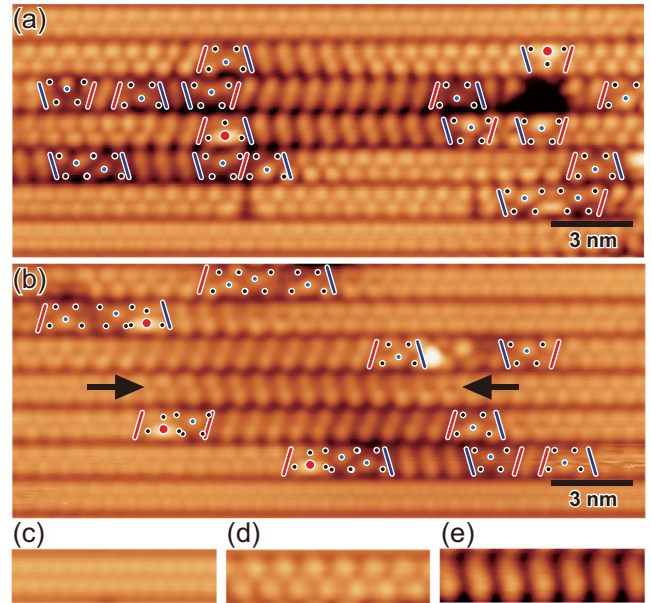


FIG. 3. (a),(b) STM topographic images ($V_s = -0.5$ V) of two small domains in the 8×2 CDW state surrounded by typical PFDs (dots) and metallic domains at 95 K. Red and blue bars denote the CDW orientation imposed by defects. (c),(d),(e) Close-up STM images of two metallic structures of 4×1 and 4×2 and the 8×2 CDW structures at 95 K, respectively.

in Fig. 2(b). The length scale of the constraint by a PFD along the wire is assumed from the decay lengths of the local lattice distortion induced by the absorbers [33,34] and the coherence length of the CDW [29], $\sim 10a_0$ (a_0 is the lattice constant of the substrate surface, 0.384 nm). The color plot of Fig. 2(b) clearly differentiates the characteristics of the insulating and metallic areas. The insulating phase persists away from the PFDs (top left of Figs. 2) and where the constraints by the PFDs obey properly the alternating CDW orientations between neighboring wires (top right and bottom middle of Figs. 2). There exist few exceptional wires in this rule. For these wires, the chiral solitons are trapped to fix the wrong orientation of CDW imposed by particular defects [29] [the yellow box in Fig. 2(a)]. The alternating CDW orientations between neighboring wires is the prerequisite for the insulating 8×2 2D CDW ordering. In sharp contrast, the intra- and interwire CDW orders were obviously broken in the metallic domain. The broken intrawire order is shown by the overlapped color and the interwire disorder is represented by the same colors repeating in neighboring wires. These areas with the frustrated CDW order show either the 4×1 structure [Fig. 3(c)] or a characteristic 4×2 structure [Fig. 3(d)]. The latter is a different $\times 2$ structural distortion from the insulating wires, which was observed in the previous STM studies around defects [35]. This structure is metallic and its STM images are consistent with the previous DFT calculations for the trimer distortion with metallic bands [36,37]. The previous DFT calculation [38] further indicated that the 4×2 trimer structure is energetically competing with the metallic 4×1 structure but is less stable than the 8×2 CDW structure [Fig. 3(e)]. In turn, it suggests that the constraints

on the CDW local order by the defects prevent the $\times 2$ order along the wire or its interwire $\times 8$ order to destabilize the 8×2 structure in favor of the 4×1 or the 4×2 structures, respectively.

The above phenomenological rule of the domain boundary can further be confirmed at different temperatures. The STM images of domain boundaries formed at a higher temperature of 95 K are shown in Fig. 3 and in Fig. S5 of the Supplemental Material [28]. In contrast to 78 K, most part of the surface is held in the metallic phase, while only small insulating 8×2 domains are observed. Note that the strong PFDs are tightly bound to the domain boundaries for those small insulating domains. We can confirm that the PFDs are arranged to keep the alternating CDW orientation of the surviving 8×2 domains in Fig. 3(a). This may be compared with what was observed for oxygen impurities for the early CDW condensation above the transition temperature, where the distance between neighboring defects within a wire (being commensurate to the CDW periodicity or not) was the crucial parameter [39]. The commensurate distance between neighboring defects is also naturally required in the present case. The distorted metallic 4×2 structures are also observed near the defects. In another small insulating domain shown in Fig. 3(b), one can reconfirm the same rule of the coherent overlap of the CDW constraint between the pinning defects for the insulating domain. In addition, we observe a defect free wire with a CDW segment between the wires with the strong PFDs [arrowed in Fig. 3(b)]. We believe that this CDW segment is caused by the substantial interwire coupling of the CDW, which is natural for the 8×2 structure.

The domain boundary formation mechanism revealed above manifests itself in a dynamical but consistent way at a even higher temperature of 120 K, close to the pristine transition temperature. At 120 K, it is hard to find an insulating 8×2 domain, but only very short segments mostly with the 4×2 structure are found to be pinned to clusters of defects [Figs. 4(a), 4(b), and 4(c)]. Short CDW segments occur when two neighboring defects within a single wire have proper CDW constraints and there is at least another defect in the neighboring wire to guarantee locally the 8×2 structure [Fig. 4(d)]. The strong PFDs become mobile at this temperature as already reported ([40], and [28] Fig. S4). For the particular defect clusters in Fig. 4(d), one strong PFD [arrowed in Fig. 4(d)] is detrapped to disappear from the image in the next moment and the local CDW distortion vanishes for that wire and also for the neighboring wire [Fig. 4(e)]. This event and a few similar ones observed corroborate the two important factors to maintain local CDW patches or domains, namely, the coherent overlap of the CDW phase constraints from neighboring defects and the interwire coupling, as discussed above.

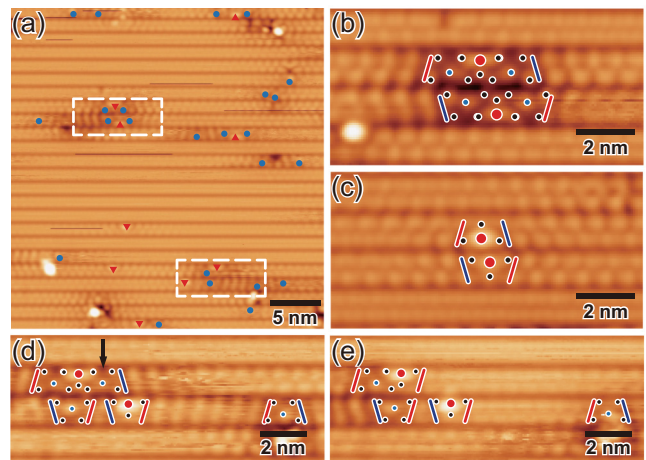


FIG. 4. (a) STM topographic images ($V_s = -0.5$ V) of the electronic phase separation at 120 K slightly below the pristine CDW transition temperature of 130 K. White boxes indicate some local 8×2 patches formed near defect clusters. Blue dots and red triangles indicate typical PFDs in Figs. 1(b) and 1(d). (b),(c) Close-up STM images showing very short 8×2 or 4×2 structures decaying rapidly from defect clusters. (d),(e) Two consecutive STM images of a short 8×2 structure formed in between two neighboring PFDs.

In summary, we have investigated the atomic scale domain boundaries formed on the electronically phase separated In/Si(111) surface at various temperatures. Atomically resolved STM images reveal characteristic atomic scale defects, which induce the CDW phase flip next to the defects. The major type of defects originates from the extra In adatoms, and the domain boundaries are largely decided by this type of defects. The detailed analysis of STM images reveals further that the constraints of the CDW phase imposed by the defects break the coherent CDW ordering to induce local metallic domains. At a temperature close to the transition temperature, the motion of the defects is activated and induces the dynamical EPS in a very short length scale but its mechanism is consistent with that at a lower temperature. This result phenomenologically unveils the atomistic origin of the EPS in a CDW system as induced by atomic scale defects collectively, emphasizing the importance of the atomic scale approach to understand inhomogeneous electronic phases. Defect engineering would be a promising way to manipulate phase-separated electronic systems.

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