# Annealing reduces Si<sub>3</sub>N<sub>4</sub> microwave-frequency dielectric loss in superconducting resonators

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(Received 20 December 2023; revised 4 April 2024; accepted 25 April 2024; published 22 May 2024)

The dielectric loss of silicon nitride  $(Si_3N_4)$  limits the performance of microwave-frequency devices that rely on this material for sensing, signal processing, and quantum communication. Using superconducting resonant circuits, we measure the cryogenic loss tangent of either as-deposited or high-temperature annealed stoichiometric  $Si_3N_4$  as a function of drive strength and temperature. The internal loss behavior of the electrical resonators is largely consistent with the standard tunneling model of two-level systems (TLSs), including damping caused by resonant energy exchange with TLSs and by the relaxation of non-resonant TLSs. We further supplement the TLS model with a self-heating effect to explain an increase in the loss observed in as-deposited films at large drive powers. Critically, we demonstrate that annealing remedies this anomalous power-induced loss, reduces the relaxation-type damping by more than 2 orders of magnitude, and reduces the resonant-type damping by a factor of 3. Employing infrared absorption spectroscopy, we find that annealing reduces the concentration of hydrogen in Si<sub>3</sub>N<sub>4</sub>, suggesting that hydrogen impurities cause substantial dissipation.

DOI: 10.1103/PhysRevApplied.21.054044

## I. INTRODUCTION

Silicon nitride Si<sub>3</sub>N<sub>4</sub> is a crucial material for a range of applications at microwave frequencies. Radiofrequency microelectromechanical systems leverage the unique mechanical properties of the dielectric to realize sensors, actuators, and switches [1,2]. Suspended Si<sub>3</sub>N<sub>4</sub> membranes help achieve excellent thermal isolation of transition-edge sensors in bolometer and calorimeter applications [3,4]. These membranes are also used to create extraordinarily high-quality-factor mechanical resonators [5-8] and are used in experiments that couple electrical and mechanical resonators with the goal of achieving quantum control of motion [9]. These electromechanical experiments have realized ground-state cooling of macroscopic resonators [10,11], demonstrated efficient microwave frequency conversion [12], and shown promise for quantumenhanced sensing [13]. Finally, there are efforts to use these mechanical resonators for quantum microwaveto-optical frequency conversion [14–16], leveraging the ability of  $Si_3N_4$  mechanical resonators to interact with both electrical and optical resonators.

The electromechanical results in particular share a common feature: a strong microwave drive tone is required to maximize the parametric coupling of the microwavefrequency resonant circuit and mechanical resonator. The drive tone populates the circuit with photons and enhances the electromechanical interaction rate, but at the same time may induce microwave loss, a resonance frequency shift, or other nonlinear effects. In fact, recent work using Si<sub>3</sub>N<sub>4</sub> membranes for microwave-to-optical frequency conversion was limited by excess power-induced loss in the electrical circuit [17,18]. Understanding and eliminating the contribution to that microwave loss from the Si<sub>3</sub>N<sub>4</sub> dielectric is important for future advancements with membrane electromechanical systems.

Hydrogen impurities are a natural suspect for this loss, as they are known to be present in  $Si_3N_4$  films [19,20] and contribute to low-power microwave dielectric absorption [21]. Meanwhile, nanophotonics applications commonly perform a high-temperature, postdeposition thermal anneal to remove residual hydrogen impurities from the precursor gases used in the deposition of nominally stoichiometric  $Si_3N_4$  films [22,23]. Together, these observations imply

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In this work, we investigate the effect of annealing on the power- and temperature-dependent microwave dielectric loss of Si<sub>3</sub>N<sub>4</sub>. Specifically, we fabricate and measure lumped-element superconducting resonant circuits that contain either as-deposited or annealed Si<sub>3</sub>N<sub>4</sub>. The loss attributed to Si<sub>3</sub>N<sub>4</sub> is well characterized by the standard tunneling model of two-level systems (TLSs) supplemented with a self-heating model, which is particularly relevant for the as-deposited Si<sub>3</sub>N<sub>4</sub> devices at large drive strengths. The TLS model includes damping caused both by resonant energy exchange with TLSs and by the relaxation of nonresonant TLSs [24,25]. Annealing remedies the power-induced loss at large drive strengths, reduces the relaxation-type damping by more than a 100-fold, and reduces the resonant-type damping by a factor of 3. Fourier-transform infrared (FTIR) spectroscopy reveals that annealing reduces the atomic hydrogen percentage by at least an order of magnitude, implying that atomic hydrogen impurities form TLSs that induce microwavefrequency dielectric loss.

## **II. THEORY OF DIELECTRIC LOSS**

Amorphous materials can host defect states wherein an electron, atom, or group of atoms within the solid can tunnel between two nearly degenerate configurations, forming a two-level system [26]. The standard tunneling model describes how the electric and elastic dipole moments of these TLSs couple to external electric or strain fields [24,25,27]. In particular, the coupling between the electric dipole moments of a bath of the TLS and the electric field of an electrical resonator adds dielectric loss to the circuit.

The TLS model predicts damping via two different coupling mechanisms: transverse and longitudinal. Transverse coupling, known as resonant damping, samples TLSs whose detuning from the electrical resonance frequency  $\omega_0$  is small compared to the TLS decoherence rate. These TLSs directly exchange energy with the resonator, and the loss inherited from this interaction can be reduced by increasing the temperature *T* or drive strength (parameterized by the intracavity photon number  $\overline{n}$ ). The resonant contribution to the loss is

$$Q_{\rm res}^{-1}(\overline{n},T) = \frac{F_{\rm SiN} \tan \delta_{0,\rm res}}{\sqrt{1 + \overline{n}/n_c}} \tanh\left(\frac{\hbar\omega_0}{2k_B T}\right),\tag{1}$$

where  $n_c$  is a critical photon number describing TLS saturation, tan  $\delta_{0,\text{res}}$  is the loss tangent of the dielectric from the resonant interaction at zero temperature, and  $F_{\text{SiN}}$  is the fraction of electric field energy of the electrical resonator mode that is present in the Si<sub>3</sub>N<sub>4</sub> volume.

Longitudinal coupling, known as relaxation damping, does not involve direct energy exchange with the electrical resonator. Instead, the oscillator amplitude shifts the TLSs' energy levels such that TLSs with energies of the order of the thermal energy are brought out of equilibrium. The TLSs then dissipate energy to phonons as they relax back to equilibrium. The loss inherited by the electrical resonator depends on the characteristic TLS relaxation rate, which increases monotonically as a function of temperature. As a result, this loss mechanism is independent of the drive strength, but predicts an increase in loss with increased temperature. When the TLS relaxation rate is small compared to the electrical resonator frequency (generally true for  $T \leq 1$  K at microwave frequencies [25]), we may approximate the relaxation damping contribution to the loss as a power law

$$Q_{\rm rel}^{-1}(T) = F_{\rm SiN} \tan \delta_{0,\rm rel} \left(\frac{T}{T_0}\right)^d, \qquad (2)$$

where  $\tan \delta_{0,\text{rel}}$  is the loss tangent of the dielectric from relaxation damping relative to a reference temperature  $T_0$ , and *d* represents the dimensionality of the phonon bath into which the TLSs decay (see Appendix C for the full expression) [28].

Including the TLS damping mechanisms, we describe the total loss with four contributions

$$Q_{\rm i}^{-1}(\bar{n},T) = Q_{\rm bg}^{-1} + Q_{\rm res}^{-1}(\bar{n},T) + Q_{\rm rel}^{-1}(T) + Q_{\rm qp}^{-1}(T), \quad (3)$$

where  $Q_{\rm bg}^{-1}$  is a constant value of background loss and  $Q_{\rm qp}^{-1}(T)$  is a parameter-free calculation of the loss from thermal quasiparticles in the superconductor [29].

We can extend this model by accounting for any potential self-heating of the device. In the presence of a microwave drive, energy is dissipated by the internal loss of the electrical circuit, which can elevate the effective temperature  $T_{\text{eff}}$  of the TLS bath by an amount  $T_{\text{heat}}$  above the base plate temperature of the dilution refrigerator  $T_{\text{bp}}$ ,

$$T_{\rm eff}(\overline{n}) = T_{\rm bp} + T_{\rm heat}(\overline{n}). \tag{4}$$

In Sec. IV below, we find that  $T_{heat}$  likely describes the temperature of the TLS bath in Si<sub>3</sub>N<sub>4</sub>. This effect can manifest as excess loss as a function of drive power in electrical resonators with large self-heating.

#### **III. EXPERIMENTAL DESIGN**

The resonator geometries we use to study the dielectric properties of  $Si_3N_4$  are represented schematically in Fig. 1(a). The lumped-element circuits are composed of a loop inductor and interdigitated capacitor. To investigate the dielectric properties of  $Si_3N_4$ , we design five resonators labeled A to E, with resonance frequencies of about 6 GHz (see Table I in Appendix A). The key difference in their design is the fraction of capacitor fingers that lie above  $Si_3N_4$ , with resonator A having the most and resonator E



FIG. 1. Circuit schematics and  $Si_3N_4$  participation. (a) Schematic representations of the different electrical resonator geometries measured in this work. A Nb-Ti-N (blue) readout line couples to a superconducting microwave circuit that partially lies above a  $Si_3N_4$  film (pink shaded area), all on a silicon chip (gray). The  $Si_3N_4$  thin film is depicted as a shaded rectangle for clarity in the schematic, but in actual devices,  $Si_3N_4$  is only underneath the superconductor, as clarified by the expanded view. (b) Example optical microscope image of resonator geometry D. The upper set of capacitor fingers has  $Si_3N_4$  underneath, while the lower set does not. (c) Electric field energy participation in  $Si_3N_4$  for different resonator designs expressed as a percentage.

having the least. Figure 1(c) shows the electric field participation ratio  $F_{SiN}$  for different devices, which is calculated from finite element simulations in Ansys HFSS [30,31].

To fabricate the planar circuits, we start by depositing a thin film of  $Si_3N_4$  on a high-resistivity silicon wafer with a Tystar TYTAN low-pressure chemical vapor deposition furnace. Using the precursor gasses ammonia (NH<sub>3</sub>) and dichlorosilane (SiCl<sub>2</sub>H<sub>2</sub>) in a 3:1 ratio, we deposit a 100-nm film of Si<sub>3</sub>N<sub>4</sub> at 770 °C. Postdeposition, we measure a film stress of around 1 GPa and a refractive index at 623 nm of 2.1. These measurements are consistent with a 3:4 stoichiometric ratio of Si and N [21]. We leave some films as deposited, while, for others, we perform a 4-h anneal of the Si<sub>3</sub>N<sub>4</sub> at 1100 °C in a pure O<sub>2</sub> atmosphere. The atmosphere of O<sub>2</sub> is recommended by the furnace manufacturer to drive out the hydrogen impurities better than N<sub>2</sub> or vacuum.

We lithographically pattern and etch the  $Si_3N_4$  film to control the  $Si_3N_4$  participation of each resonator. We then sputter a 140-nm film of Nb-Ti-N and lithographically pattern the lumped-element circuit. The Nb-Ti-N etch also removes the remaining  $Si_3N_4$  that is not masked by the superconductor [expanded view in Fig. 1(a)]. The relatively thick superconducting layer ensures step coverage of  $Si_3N_4$  and minimizes the otherwise strong kinetic inductance nonlinearity associated with thin-film Nb-Ti-N [32]. Our Nb-Ti-N has a superconducting critical temperature  $T_c$  of 15 K, so the loss contribution from thermal quasiparticles is subdominant to the dielectric loss from the TLS at the temperatures studied in this work.

To study the power- and temperature-dependent loss, we excite the resonant circuits with a variable-strength microwave drive tone detuned by 1.5 MHz, while measuring the electrical resonator response using a network analyzer emitting a probe tone with at most 1% of the power of the drive. Each design is inductively coupled to a common on-chip transmission line, and the device is mounted to the base plate of a dilution refrigerator and cooled to a base temperature of  $T_{bp} = 10$  mK. The drive and probe tones access the device through an array of attenuators and cables in the fridge whose microwave transmission response is measured prior to cooling down so that we may calibrate the intracavity photon number  $\overline{n}$ . To measure the temperature-dependent loss, we hold the drive power fixed and vary the base plate temperature. We fit the frequency-dependent transmission response to extract the resonator's internal loss  $Q_i^{-1}$ , external coupling  $Q_{\rm e}^{-1}$ , and center frequency  $\omega_0$  [33,34] (Appendix A).

## **IV. RESULTS AND DISCUSSION**

Measurements of the power- and temperature-dependent internal loss of the electrical resonators are shown in Fig. 2, along with fits to model (3) with (dotted lines) and without (dashed lines) self-heating. The resonators' internal loss as a function of both power and temperature is directly proportional to electric field participation ratio  $F_{SiN}$  (see Fig. 5 below). This trend indicates that the power- and temperature-dependent loss originates from the Si<sub>3</sub>N<sub>4</sub> dielectric. Similar circuits with no Si<sub>3</sub>N<sub>4</sub> or as-deposited Si<sub>3</sub>N<sub>4</sub> only under the inductive part of the circuit do not demonstrate this behavior (Appendix B). Qualitatively, we also see that the electrical resonators fabricated on annealed Si<sub>3</sub>N<sub>4</sub> have significantly less loss and exhibit starkly different behavior than that of the resonators with as-deposited Si<sub>3</sub>N<sub>4</sub>. In the following analysis, we begin by applying the TLS-only model, before considering self-heating as a way to understand the full data set.

In Figs. 2(a) and 2(b), we plot the loss as a function of power at base temperature  $T_{bp} = 10$  mK. For the resonators with as-deposited Si<sub>3</sub>N<sub>4</sub>, we observe an initial saturation of the loss with increasing power for  $\overline{n} \leq 10^4$ , followed by a sharp increase at higher powers. This is the same behavior that limited the performance of electromechanical resonators with as-deposited Si<sub>3</sub>N<sub>4</sub>



FIG. 2. Power- and temperature-dependent loss of Si<sub>3</sub>N<sub>4</sub>. Loss data for as-deposited Si<sub>3</sub>N<sub>4</sub> (purple circles) and annealed Si<sub>3</sub>N<sub>4</sub> (green triangles) with  $\pm 1\sigma$  error bars. Different shades represent the different designs A–E. (a),(b) Loss as a function of drive power at  $T_{bp} = 10$  mK. The TLS-only model (dashed lines) predicts the decrease of loss as increasing power saturates the TLS absorbers. The combined TLS and self-heating model (dotted lines) accounts for the increase in loss for the electrical resonators with as-deposited Si<sub>3</sub>N<sub>4</sub> at higher powers. (c),(d) Loss as a function of base plate temperature at  $\bar{n} \sim 1$ . For both films, the TLS model captures the behavior without the need to invoke self-heating, due to the low drive power. The increase in loss from relaxation damping observed at higher temperatures with the as-deposited film is absent in the annealed film. (e) Inferred temperature of the as-deposited Si<sub>3</sub>N<sub>4</sub> as a function of drive power is calculated by correlating the power-dependent and temperature-dependent loss data. The fit (black dotted line) yields the prediction of loss from self-heating in (a).

(Appendix B). Notably, this increase is absent for the resonators with annealed Si<sub>3</sub>N<sub>4</sub>. We can extract the resonant damping contribution  $Q_{\rm res}^{-1}$  from this low-power data since it is the only power-dependent term in Eq. (3), in the absence of self-heating. From this fit to the TLS-only model, we extract the resonant damping loss tangents  $\tan \delta_{0,\rm res} = (1.4 \pm 0.1) \times 10^{-3}$  for the as-deposited film and  $\tan \delta_{0,\rm res} = (4.8 \pm 0.4) \times 10^{-4}$  for the annealed film. Annealing yields a factor of 3 improvement in the low-power, low-temperature dielectric loss.

The temperature-dependent loss at fixed  $\overline{n} = 1$  in Figs. 2(c) and 2(d) provides information about the relaxation-type loss. For the as-deposited film, we observe an initial saturation of the resonant-type loss with temperature ( $T_{bp} \le 0.3$  K), after which the relaxation-type contribution becomes the dominant term ( $T_{bp} \ge 0.5$  K). Similar to the power-dependent data, the temperature-dependent increase in loss from relaxation damping is absent for the resonators with annealed Si<sub>3</sub>N<sub>4</sub>. No self-heating contribution is required to capture the temperature-dependent data due to the small drive power. Empirically, we find the power-law relationship  $Q_i^{-1} \propto T^2$  in the as-deposited

Si<sub>3</sub>N<sub>4</sub> resonators between 0.3 and 1 K. Accordingly, we fix d = 2 in the full expression of  $Q_{\rm rel}^{-1}(T)$  [Eq. (C2)] for the remaining analysis. The contrast between the increasing loss with temperature for as-deposited Si<sub>3</sub>N<sub>4</sub> and the lack thereof for annealed Si<sub>3</sub>N<sub>4</sub> is reflected in the extracted loss tangents. We find that  $\tan \delta_{0,\rm rel} = (3.4 \pm 0.1) \times 10^{-3}$  for the as-deposited film and  $\tan \delta_{0,\rm rel} = (8 \pm 8) \times 10^{-6}$  for the annealed film with reference temperature  $T_0 = 500$  mK, representing a dramatic decrease of this loss mechanism.

Interestingly, annealing realizes only a modest improvement to the resonant-type loss, but vastly reduces the relaxation-type loss. As noted in Sec. II and further explored in Appendix C, the resonant interaction couples the electrical resonator to the TLS with energy close to  $\omega_0$ , while the relaxation interaction mainly couples to off-resonant TLSs. The relaxation contribution starts to dominate over the resonant contribution above  $T_{\rm bp} \sim 500$  mK, suggesting that the impurities removed by annealing were host to TLSs with energies predominantly above 500 mK  $\times 2k_B/h \simeq 20$  GHz. This observation also suggests that the annealing changes the frequency-dependent structure of the TLS density of states.

Although the drastic decrease in the relaxation-type loss may seem inconsequential for experiments at  $T \ll T_0$ , this loss mechanism can become relevant in situations where a high-power drive tone induces local heating. We propose that the anomalous increase in loss at high drive strengths observed in the resonators with as-deposited Si<sub>3</sub>N<sub>4</sub> seen in Fig. 2(a) could be a symptom of self-heating, which activates the temperature dependence of the relaxationtype loss. The self-heating mechanism is suggested by the striking qualitative similarity between the power- and temperature-dependent loss of as-deposited Si<sub>3</sub>N<sub>4</sub>. A similar qualitative agreement is observed between the powerand temperature-dependent electrical resonator frequency shifts (see Appendix D). We can parameterize the powerdependent loss within the TLS model by assigning an effective temperature  $T_{\rm eff}$  to the electrical resonator at each drive power  $\overline{n}$ . We use the fits to the TLS-only model of the temperature-dependent loss to determine the temperature that is consistent with the measured dissipation  $Q^{-1}(\overline{n}, T_{\text{eff}}).$ 

This mapping for the electrical resonators with asdeposited  $Si_3N_4$  is shown in Fig. 2(e). Though the mapping for each device A-E is made separately, the inferred temperature curves obey the same functional dependence with  $\overline{n}$  independent of  $F_{SiN}$ . Resonators with more Si<sub>3</sub>N<sub>4</sub> dissipate more power in proportion to the relative amount of Si<sub>3</sub>N<sub>4</sub>, but the dielectric also has more thermalization pathways due to the larger area. As a result, the inferred temperature is about the same for a given  $\overline{n}$ , despite the differences in geometry. We can perform a self-consistency check of the TLS and self-heating model by plotting the power-dependent loss predicted by Eq. (3), now including a power-dependent temperature. We parameterize the effective temperature by jointly fitting the data from all the resonators in Fig. 2(e) to a simple power law:  $T_{\text{eff}}(\overline{n}) = A\overline{n}^{\beta} + T_{\text{bp}}$ . Empirically, we find that  $\beta \approx 0.5$ , and subsequently fix this exponent in the fitting. The consequence of this analysis is shown in Fig. 2(a) with dotted lines, demonstrating that self-heating could explain the increase in loss at high powers in the as-deposited films.

This analysis suggests significant heating of the relevant degrees of freedom, up to more than 2 K with  $\overline{n} = 10^7$ . In principle,  $T_{\text{eff}}(\overline{n})$  could reflect the temperature of either the whole Si chip, the Si<sub>3</sub>N<sub>4</sub> film, or the bath of the TLS. However, we observe no power-induced loss when driving one circuit and measuring the loss of another circuit on the same chip, indicating the Si chip remains cold. Likewise, calculations and observations of the thermal boundary resistance at the Si<sub>3</sub>N<sub>4</sub>-Si interface [35,36] are inconsistent with the temperature difference we appear to measure. This leaves the possibility that the TLS bath itself is at an elevated temperature, which can occur if the TLS-TLS

interactions are stronger than the TLS-phonon interaction. In the as-deposited films, the TLS-TLS interaction may be significantly enhanced relative to the annealed films due to the large density of impurities [37], as reported in the next section.

#### **V. MICROSCOPIC ORIGIN OF THE TLS**

To better understand the impact of annealing on the dielectric loss of  $Si_3N_4$ , we use FTIR spectroscopy to attempt to identify the microscopic source of the TLS. For our  $Si_3N_4$  film, we expect to primarily find molecular vibrational modes of the Si—N bond and Si—H or N—H impurities [21,38]. As-deposited and annealed  $Si_3N_4$  films of various thicknesses are prepared (details in Appendix E), and results of the FTIR measurements are shown in Fig. 3(a).

The spectra reveal that the as-deposited  $Si_3N_4$  is host to hydrogen impurities, whose atomic concentration is substantially reduced after annealing. In addition to the Si—N stretching mode at 890 cm<sup>-1</sup> (not shown), we observe peaks in the absorption spectrum at 3330 and 2210 cm<sup>-1</sup>



FIG. 3. Infrared spectroscopy reveals hydrogen impurities. (a) The N—H and Si—H bond stretching vibrational modes are in the IR spectrum between 60 and 100 THz at wave numbers of 3330 and 2210 cm<sup>-1</sup>, respectively. We measure Si<sub>3</sub>N<sub>4</sub> films of different thicknesses with Fourier-transform infrared (FTIR) spectroscopy before and after annealing, noting a significant reduction in the absorbance at these wave numbers. (b) The atomic hydrogen percentage in the film is calculated from the spectrum measurements. The annealed film has more than an order of magnitude less hydrogen content than the as-deposited film. Error bars represent  $\pm 1\sigma$ .

for the as-deposited films corresponding to the stretching modes of the N—H and Si—H bonds, respectively [21]. The height of these peaks decreases with decreasing film thickness. Notably, the peaks of the spectrum for the annealed films are nearly indistinguishable from the background. By comparing the atomic hydrogen percentage in the films [39], Fig. 3(b) shows that annealing reduces the hydrogen content of Si<sub>3</sub>N<sub>4</sub> by at least an order of magnitude. Additionally, the hydrogen content for both films is independent of thickness, implying that the impurities are uniformly distributed throughout the bulk.

The near-IR modes identified by FTIR spectroscopy are likely too high in frequency to be the source of loss in Si<sub>3</sub>N<sub>4</sub>, but they do serve as markers of hydrogen impurities that can host other TLSs. Hydrogen impurities in alumina (Al<sub>2</sub>O<sub>3</sub>) are strong candidates for gigahertzfrequency TLSs [26,40], where an interstitial hydrogen atom tunnels between adjacent oxygen atoms [41]. Analogous defects in the 20-50-GHz range have been found in niobium films, where interstitial hydrogen atoms tunnel between adjacent nitrogen impurities [42-44]. Similar defects could exist in our Si<sub>3</sub>N<sub>4</sub> films. While the data presented in this work do not conclusively identify a specific physical TLS realization, it does suggest that the source of the TLS is intimately related to the presence of hydrogen impurities, which we have shown are removed by thermal annealing.

## **VI. CONCLUSION**

We have investigated the power- and temperaturedependent dielectric properties of Si<sub>3</sub>N<sub>4</sub> with lumpedelement superconducting resonators. We observe not only the familiar saturable loss associated with the resonant interaction between the TLS and electrical resonator, but also a significant temperature-dependent loss that is attributed to relaxation-type damping. The as-deposited Si<sub>3</sub>N<sub>4</sub> films also exhibit increasing power-induced loss at large powers, which we understand via a combined TLS and self-heating model. We show that high-temperature annealing reduces the relaxation damping component significantly, while also modestly improving the resonant damping contribution. Moreover, the increasing powerinduced loss at large powers observed in the as-deposited films is absent in the annealed films. There have been similar anomalous power-dependence observations in other materials, such as amorphous silicon [45], crystalline silicon [46], and sapphire [47], which could also originate from TLS relaxation damping activated by local selfheating. In Si<sub>3</sub>N<sub>4</sub>, we attribute the source of dissipation to hydrogen impurities in the dielectric, which are reduced by an order of magnitude in the annealing process.

Our combined TLS and self-heating model also highlights the importance of proper thermalization. Systems with suspended  $Si_3N_4$  are particularly vulnerable to self-heating, as the pathways for thermalization are already restricted [48,49]. In fact, the power-induced loss observed in electro-optomechanical transducers was larger than that measured in these planar circuits despite having smaller  $F_{SiN}$ , suggesting considerable heating of the dielectric (see Appendix B) [17,18]. In the absence of power-induced internal loss from relaxation damping, the electromechanical interaction, which is mediated by a strong drive tone, can be maximized without negatively impacting the resonant circuit.

Another limiting factor of the electro-optomechanical transducer's performance was microwave noise induced by the strong microwave drive tone. From the fluctuationdissipation theorem, a portion of this noise must have resulted from the loss and self-heating identified in this work. Further investigation could interrogate the degree to which annealing also improves the power-induced microwave noise.

The data collected for this study and the code used to generate the figures for this paper are available on Zenodo [50].

### ACKNOWLEDGMENTS

We thank James Beall and Michael Vissers for helping with device fabrication, and Jordan Wheeler for helping with the  $T_c$  measurements of our Nb-Ti-N films. Benjamin Brubaker, Jonathan Kindem, Peter Burns, and Andrew Higginbotham launched this study with early investigations of the loss and noise in Si<sub>3</sub>N<sub>4</sub> electromechanical devices. Additionally, we thank Shuo Sun and Jacob Davidson for helpful discussions. This work was supported by funding from ARO Grant W911NF2310376, NSF Grant No. PHY-2317149, NSF QLCI Award OMA—2016244, the Office of the Secretary of Defense via the Vannevar Bush Faculty Fellowship, Award No. N00014-20-1-2833, and the Baur-SPIE Endowed Chair at JILA.

## APPENDIX A: DETERMINING RESONATOR PARAMETERS

Electrical resonators A–E are inductively coupled to a single transmission line that traverses the chip, and we measure the transmission response in order to extract the resonators' parameters. Each resonator is designed to have a unique frequency both from varying  $Si_3N_4$  participation and from small variation in the designed inductance. We first preprocess the frequency-dependent data by normalizing the off-resonance values to unity transmission. We then fit the response to

$$S_{21}(\omega) = 1 - \frac{Q|Q_e^{-1}|e^{i\phi}}{1 + 2iQ(\omega - \omega_0)/\omega_0}$$
(A1)

TABLE I. Device parameters. The center frequency, internal quality factor, and external quality factor for the studied devices measured at  $T_{\rm bp} = 10$  mK and  $\bar{n} \sim 1$ . For all resonators, the external quality factor is roughly within an order of magnitude of the internal loss, which allows for accurate determination of the relative coupling rates from the numerical fits.

	Device	$\omega_0/2\pi$ (GHz)	$Q_i^{-1} \times 10^5$	$ Q_e^{-1}  \times 10^5$
As-deposited	А	5.968	18.4	9.3
resonator	В	6.133	16.2	10.3
	С	6.289	12.5	22.4
	D	6.384	7.5	9.8
	Е	6.480	1.9	15.3
Annealed	А	5.959	2.4	9.8
resonator	В	6.103	2.2	12.4
	С	6.271	1.9	23.1
	D	6.362	1.3	8.7
	Е	6.443	0.8	16.6

to extract the quality factor of resonator Q, the external quality factor  $Q_e$ , and the center frequency  $\omega_0$ . We use a complex external quality factor  $Q_e^{-1} = |Q_e^{-1}|e^{i\phi}$  to account for unintended reflections that lead to asymmetry in the frequency response [33,34]. We define the internal quality factor as  $Q_i^{-1} = Q^{-1} - \operatorname{Re}\{Q_e^{-1}\}$ .

Using the fitted resonator parameters and known attenuation of the fridge input line, we calculate the intracavity photon number  $\overline{n}$  using

$$\overline{n} = \frac{P_{\rm inc}}{\hbar\omega_0^2} \frac{|Q_e^{-1}|/2}{(Q^{-1}/2)^2 + (\Delta_p/\omega_0)^2},\tag{A2}$$

where  $P_{\text{inc}}$  is the incident power on the resonator and  $\Delta_p = \omega_p - \omega_0$  is the detuning of the drive tone from the resonator center frequency ( $\Delta_p/2\pi = 1.5$  MHz in this work). Values for the fitted parameters at  $T_{\text{bp}} = 10$  mK and  $\overline{n} \sim 1$  are shown in Table I for both the as-deposited and annealed resonators.

# APPENDIX B: SCALING OF LOSS WITH Si<sub>3</sub>N<sub>4</sub> IN RELATED ELECTRICAL RESONATORS

In order to test whether the power- and temperaturedependent loss could be caused by something other than dielectric loss, we fabricated devices either with no Si<sub>3</sub>N<sub>4</sub> or with Si<sub>3</sub>N<sub>4</sub> only under the inductor rather than the capacitor in the circuit. Schematic depictions and measurements of these devices are shown in Fig. 4. The electric field participation ratio  $F_{SiN}$  is 0 for the resonator with no Si<sub>3</sub>N<sub>4</sub>, and  $F_{SiN}$  is 0.002% for the resonator with Si<sub>3</sub>N<sub>4</sub> under the inductive part of the circuit—orders of magnitude lower than resonators A–E. We observe almost no change in the loss with power in these resonators, as expected if the loss in the resonators A–E is from the Si<sub>3</sub>N<sub>4</sub> dielectric. The behavior of these devices shows that



FIG. 4. Power-dependent loss in electrical resonators with different geometries. Resonators with no Si<sub>3</sub>N<sub>4</sub> (red diamonds) or Si<sub>3</sub>N<sub>4</sub> under the inductor (blue crosses) have loss that saturates to low values at moderate drive strengths  $\bar{n} \sim 10^4$ . The loss of a device with electromechanically compliant Si<sub>3</sub>N<sub>4</sub> (yellow stars) saturates to a higher value and, furthermore, rapidly increases with larger drive strengths, likely due to poor thermalization. Error bars indicate  $\pm 1\sigma$  uncertainty for the numerical fits.

other possible sources of loss, such as excess quasiparticles or mobile magnetic vortices, are inconsistent with the observed power-induced loss [30].

The power-dependent loss measured in an electromechanical device is also plotted in Fig. 4 [18]. Because of variability in the geometry from misalignment of the electromechanical capacitor,  $F_{SiN}$  can vary, but for reasonable assumptions of the alignment, we simulate  $F_{SiN} \sim 0.1\%$ . Although this value is less than the planar circuits discussed in the main text, this electromechanical resonator exhibits a very strong increase in the power-induced loss



FIG. 5. Loss versus  $F_{SiN}$ . The internal loss of resonators A–E with as-deposited Si<sub>3</sub>N<sub>4</sub> scales linearly with the electric field participation ratio  $F_{SiN}$ . This relationship holds at different drive strengths and temperatures. Error bars indicate  $\pm 1\sigma$  uncertainty.

above  $\overline{n} \sim 10^7$ . The power dependence suggests restricted thermalization pathways for the phonons in the suspended Si<sub>3</sub>N<sub>4</sub> membrane, such that the TLSs interact with a phonon bath at a temperature elevated from that of the base plate of the dilution refrigerator.

As shown in Fig. 5, we verify that the loss is determined by the dielectric loss of  $Si_3N_4$  by plotting the internal loss  $Q_i^{-1}$  as a function of the electric field participation ratio  $F_{SiN}$  for as-deposited devices. The loss is linearly proportional to  $F_{SiN}$  for a range of drive strengths and temperatures.

## APPENDIX C: TLS MODEL PREDICTIONS OF LOSS

Following the derivation from Phillips [25], the loss contribution from the resonant interaction is

$$Q_{\rm res}^{-1}(\overline{n},T) = \frac{P|\bar{\mathbf{p}}_0|^2 \pi}{3\epsilon} \frac{\tanh\left(\hbar\omega_0/2k_BT\right)}{\sqrt{1+\overline{n}/n_c}},\qquad({\rm C1})$$

where  $|\bar{\mathbf{p}}_0|$  is the spatially averaged TLS dipole moment,  $\epsilon$  is the permittivity of the dielectric, and *P* is the TLS density of states. In practice, we absorb these numerical quantities into  $F_{\text{SiN}} \tan \delta_{0,\text{res}}$  in Eq. (1). The contribution to loss from the relaxation interaction is

$$Q_{\rm rel}^{-1}(T) = \frac{P|\bar{\mathbf{p}}_0|^2 \omega_0}{6\epsilon k_B T} \int_0^\infty dE \int_{\tau_{\rm min}(E)}^\infty d\tau \, {\rm sech}^2\left(\frac{E}{2k_B T}\right) \\ \times \sqrt{1 - \frac{\tau_{\rm min}(E)}{\tau}} \frac{1}{1 + \omega_0^2 \tau^2}.$$
 (C2)

The double integral is over the TLS energies E and the TLS relaxation time  $\tau$ . The TLS decay is dominated by phonon interactions, and the relaxation rate due to one-phonon processes in d dimensions is given by Eq. (7) of Ref. [51],

$$\tau^{-1}(E) = \frac{\bar{\gamma}^2}{\bar{\nu}^{d+2}} \frac{\pi S_{d-1}}{(2\pi)^d} \frac{E^{d-2} \Delta_0^2}{\hbar^{d+1} \rho_d} \coth\left(\frac{E}{2k_B T}\right), \quad (C3)$$

where  $\bar{\gamma}$  is the TLS elastic dipole averaged over position and orientation,  $\bar{v}$  is the polarization-averaged acoustic velocity,  $S_d$  is the *d*-dimensional unit-hypersurface area,  $\Delta_0$  is the TLS tunneling strength, and  $\rho_d$  is the *d*-dimensional density. The relaxation time is minimized when the TLS energy *E* matches the tunneling strength  $\Delta_0$  [25], resulting in  $\tau_{\min}^{-1}(E) \propto E^d \operatorname{coth}(E/2k_BT)$ . The dimensionality of the phonon bath experienced by the TLS is determined by the temperature and geometry of the dielectric material [51,52].

In the high-temperature regime, the TLS lifetime is reduced by higher-order phonon processes such that  $\omega_0 \tau_{\min} \ll 1$ , so we can approximate  $\tau_{\min} \rightarrow 0$ . In this

limit, the loss saturates to a value independent of temperature,

$$\lim_{\nu_0 \tau_{\min} \ll 1} \mathcal{Q}_{\text{rel}}^{-1}(T) = \frac{P|\bar{\mathbf{p}}_0|^2 \pi}{6\epsilon}.$$
 (C4)

In the low-temperature regime, when  $\omega_0 \tau_{\min} \gg 1$  and the TLS are long lived, Eq. (C2) reduces to a power law with exponent *d* described earlier in Eq. (2). To see this, we can approximate  $1/(1 + \omega_0^2 \tau^2) \approx 1/(\omega_0^2 \tau^2)$ . Then, the integral over  $\tau$  can be evaluated using the identity  $\int_a^{\infty} \sqrt{(1 - a/x)}/x^2 = 2/(3a)$ . Putting it all together, we have

$$\lim_{\omega_0 \tau_{\min} \gg 1} Q_{\text{rel}}^{-1}(T) = \frac{P|\bar{\mathbf{p}}_0|^2 \omega_0}{9\epsilon} \frac{\bar{\gamma}^2}{\bar{v}^{d+2}} \frac{S_{d-1}}{(2\pi)^{d-1}} \frac{1}{\hbar^{d+1} \rho_d} (2k_B T)^d \times \int_0^\infty \xi^d \operatorname{sech}^2(\xi) \operatorname{coth}(\xi) \, \mathrm{d}\xi, \quad (C5)$$

where we have performed the change of variables  $\xi = E/(2k_BT)$ . This expression includes both the TLS's electric and elastic dipole properties because the TLS absorbs electrical energy and subsequently decays to phonons. In Eq. (2), we absorb all of the numerical quantities into  $F_{\text{SiN}} \tan \delta_{0,\text{rel}}/T_0^d$ , but the fits to the data employ the full expression from Eq. (C2).

A key assumption of the standard tunneling model is a TLS density of states P that is constant with respect to energy [25]. Consequently, the model predicts similar scales for the loss inherited from the resonant- and relaxation-type interactions, but we observe very different scales for the two contributions in practice. For example, annealing improves the relaxation-type loss



FIG. 6. Sampling of the TLS energy distribution. The resonant contribution (dotted line) samples a narrow distribution of energies, centered on the frequency of the microwave circuit (about 6 GHz). The relaxation contribution (solid lines) samples different regions of TLS energy at different temperatures. For  $T \ge 0.5$  K, where we start to observe appreciable relaxation damping in the as-deposited films, a wide distribution of TLSs with energies  $\gtrsim 10$  GHz are relevant.

much more than the resonant-type loss. Additionally, the high-temperature asymptote of the relaxation contribution [Eq. (C4)] is significantly larger than the zero-temperature, zero-power limit of the resonant contribution [Eq. (C1)] for the devices with as-deposited  $Si_3N_4$ . The need for independent prefactors in our fits is further evidence of a nonuniform energy distribution of the TLS, as the resonant and relaxation contributions sample different regions of the TLS density of states.

We plot the regions of the TLS distribution that are being sampled by the two damping terms in Fig. 6. The resonant contribution is sensitive to TLSs with energies close to the resonance frequency of the microwave circuit, with the width of the distribution set by the characteristic TLS decoherence rate. To see the TLS energies sampled by the relaxation contribution, we plot the value of the integrand in Eq. (C5) as a function of the TLS energy at various temperatures for d = 2. This distribution is centered on  $E \sim 2k_{\rm B}T$ , with a characteristic width of  $\delta E \sim 1.7 \times 2k_{\rm B}T$ . Thus, the relaxation damping samples a distribution of TLS energies whose center and width scale proportionally with the temperature. From Fig. 6, it is clear that, for temperatures  $T \gtrsim 0.5$  K, the TLS with energy much greater than that of the microwave circuit causes the relaxation damping loss.

## APPENDIX D: TLS MODEL PREDICTIONS OF THE FREQUENCY SHIFT AND DATA

In addition to a change in the loss rate, we measure the frequency of the resonators as a function of power and temperature. The TLS model predicts frequency shifts from both resonant- and relaxation-type interactions. The contribution from the resonant-type interaction is

$$\left(\frac{\Delta f}{f}\right)_{\rm res} = \frac{P|\bar{\mathbf{p}}_0|^2}{3\epsilon} \left[ \operatorname{Re}\left\{ \Psi\left(\frac{1}{2} + \frac{\hbar\omega_0}{2\pi i k_B T}\right) \right\} - \ln\left(\frac{\hbar\omega_0}{2\pi k_B T}\right) \right],$$
 (D1)

where  $\Psi$  is the digamma function [27,28]. The first term results in an initial red shift of the resonance frequency as temperature increases, while the second term constitutes a blue shift.



FIG. 7. Power- and temperature-dependent resonator fractional frequency shift. Frequency shift data for as-deposited Si<sub>3</sub>N<sub>4</sub> (purple circles) and annealed Si<sub>3</sub>N<sub>4</sub> (green triangles) with  $\pm 1\sigma$  error bars. Different shades represent the different designs A–E. Each resonator's frequency has been normalized with respect to its frequency at the base plate temperature  $T_{bp} = 10$  mK and intracavity photon number  $\bar{n} \sim 1$ . (a),(b) Power-dependent frequency shift at 10 mK. The TLS model does not predict a frequency shift with drive power, as evidenced by the devices with annealed Si<sub>3</sub>N<sub>4</sub>. Using the self-heating model shown in Fig. 2 translates power dependence into temperature dependence, which captures the qualitative behavior of the frequency shift observed for the as-deposited Si<sub>3</sub>N<sub>4</sub>. (c),(d) Temperature-dependent frequency shift at  $\bar{n}$ . The resonators with as-deposited Si<sub>3</sub>N<sub>4</sub> exhibit both resonant and relaxation contributions, while the resonators with annealed Si<sub>3</sub>N<sub>4</sub> only exhibit the resonant contribution, supporting the large reduction in the strength of the relaxation interaction from annealing.

The relaxation term involves integrating over all the relaxation timescales of the many off-resonant TLSs [25],

$$\left(\frac{\Delta f}{f}\right)_{\rm rel} = \frac{-P|\bar{\mathbf{p}}_0|^2}{12\epsilon k_B T} \int_0^\infty dE \int_{\tau_{\rm min}(E)}^\infty d\tau \, {\rm sech}^2 \left(\frac{E}{2k_B T}\right) \frac{1}{\tau} \\ \times \sqrt{1 - \frac{\tau_{\rm min}(E)}{\tau}} \frac{1}{1 + \omega_0^2 \tau^2}.$$
 (D2)

This term predicts a red shift of the resonator frequency with increasing temperature. Notably, both the resonantand relaxation-type couplings induce frequency shifts as a function of temperature, but are independent of power. While these expressions suggest that the scale of the TLSinduced frequency shift can be determined by the scale of the TLS-induced loss, this is only valid under the assumption of a uniform density of states. For the same reasons outline in Appendix C, we determine these parameters independently when fitting to the model.

In Figs. 7(a) and 7(b), we plot the power-dependent frequency shift at  $T_{bp} = 10$  mK. The annealed Si<sub>3</sub>N<sub>4</sub> devices shown in Fig. 7(b) agree with the TLS model prediction that frequency is independent of drive strength. Curiously, we observe a strong power-induced frequency shift in devices with as-deposited Si<sub>3</sub>N<sub>4</sub>. To understand this, we can look at the temperature-dependent frequency shifts at  $\overline{n} \sim 1$  in Figs. 7(c) and 7(d). The temperature-dependent fractional frequency shift observed in the resonators with as-deposited or annealed Si<sub>3</sub>N<sub>4</sub> is well described by fits that include the TLS couplings and a parameter-free contribution from thermal quasiparticles [29]. Similar to the loss, we see that the resonators with annealed Si<sub>3</sub>N<sub>4</sub> have negligible impact from the relaxation-type interaction compared to those with as-deposited Si<sub>3</sub>N<sub>4</sub>. Noting the similarity in shape to the temperature-dependent frequency shift, we reuse the effective temperature  $T_{\rm eff}$  used to describe the loss in Fig. 2. With this self-heating assumption, we find qualitative similarity between the data and the combined TLS and self-heating model. However, this combined model does not precisely predict the behavior of all of the resonators. This discrepancy likely stems from the previously mentioned fact that the loss and frequency shift expressions find support on different regions of the TLS density of states.

Another consequence of this frequency shift is that we must dynamically tune the frequency of the drive tone to maintain a 1.5-MHz detuning relative to the *LC* frequency. It is difficult to set the detuning appropriately for a given drive strength, because the drive detuning sets the intracavity photon number that may in turn shift the *LC* frequency. We converge on the correct detuning by iteratively setting the drive detuning relative to an assumed *LC* resonance frequency and then fitting the measured *LC* response. We repeat this loop until the predicted detuning differs from the measured detuning by less than 0.01%.

## APPENDIX E: SPECTROSCOPY SAMPLE PREPARATION AND ANALYSIS

To prepare samples for FTIR spectroscopy, we deposit a 400-nm-thick layer of  $Si_3N_4$  onto both sides of a Si wafer. This film is much thicker than that used for the resonators because a thicker film will increase the absorption signal. We dice the wafer into four samples, and then etch the film on one side of each of the samples to obtain different thicknesses. This results in a set of samples with  $Si_3N_4$  thickness ranging from 800 nm (no  $Si_3N_4$  etched) to 500 nm (300 nm of  $Si_3N_4$  etched on one side of the wafer). A similar procedure is used to prepare the annealed  $Si_3N_4$  samples, except the anneal is performed directly after the deposition.

Our FTIR spectrometer model is Thermo Scientific Nicolet iS50, operated in transmission mode [53]. The transmission mode of operation is less sensitive than attenuated total reflection techniques, but nonetheless reveals the film impurities given proper sample preparation and data analysis. After purging the sample chamber with nitrogen gas for 30 min, we take a background spectrum with a bare Si wafer. For the spectra of each Si<sub>3</sub>N<sub>4</sub> film, we subtract the background spectra and then perform an atmospheric correction to remove absorption features from residual water and  $CO_2$  in the sample chamber. A polynomial fit is used to remove the baseline structure [54]. The areas under the absorption peaks are extracted from Gaussian fits to the spectra.

Using well-established calibrations of the infrared absorption cross sections of these bonds, we can calculate the atomic hydrogen percentage in the film using the Lanford and Rand method [39], described by

atomic %H = 
$$\frac{[Si-H] + [N-H]}{[Si] + [N] + [Si-H] + [N-H]}$$
, (E1)

where [Si], [N], etc. denote the densities in atoms per unit volume. We have made the assumption that all of the -H in the film are bonded to either N or Si. [Si-H] can be determined by [Si-H] =  $A_{\text{Si-H}}/(\sigma_{\text{Si-H}} \times t)$ , where  $A_{\text{Si-H}}$  is the area under the Si-H absorption peak,  $\sigma_{\text{Si-H}}$  is the IR absorption cross section, and *t* is the thickness of the film ([N-H] is found in an analogous manner). The sum [Si] + [N] is the atomic density of Si<sub>3</sub>N<sub>4</sub> in units of atoms per unit volume, which can be determined from the density of the film.

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