

# High-Gain Low-Noise Phototransistors Based on Solution-Processed $\text{Bi}_2\text{S}_3$ Nanocrystals

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Chalcogenide-based semiconductors have recently emerged as promising optoelectronic materials. In particular, bismuth-based sulfides have demonstrated great potential for device applications, such as in photovoltaics and photodetection. However, most of the devices reported have been based on silver-bismuth-sulfide-based solar cells. Work on bismuth sulfide based on precursor methods has been barely reported, and studies of its optoelectronic properties are lagging far behind. In this paper, we first report solution processing of  $\text{Bi}_2\text{S}_3$  thin films, describe the systematic optimization of the precursors, and investigate the influence of composition and annealing on the properties of the films, including their morphology, crystallinity, charge-carrier dynamics, charge transport, and photoconductive gain. We also demonstrate excellent field-effect transistors based on  $\text{Bi}_2\text{S}_3$  thin films, and introduce the use of these  $\text{Bi}_2\text{S}_3$  phototransistors for weak-light detection. The optimized devices exhibit extremely high responsivity and low dark current, paving the way for high-performance photodetection.

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## I. INTRODUCTION

Chalcogenide-based optoelectronic materials, e.g., binary  $\text{CdTe}$  [1,2],  $\text{Sb}_2\text{S}_3$  [3–5],  $\text{Bi}_2\text{S}_3$  [6,7], ternary  $\text{CuSbS}_2$  [8],  $\text{AgSbS}_2$  [9,10],  $\text{NaBiS}_2$  [11], and  $\text{AgBiS}_2$  [12,13], have recently demonstrated great potential for optoelectronic applications, benefiting mainly from their low cost, superior stability, tunable optoelectronic properties, and facile processability. Antimony- and bismuth-based chalcogenide thin films have exhibited fairly good device performance on multiple platforms, including photovoltaics [6,14,15], photodetectors [5,16,17], field-effect transistors (FETs) [4,5] and photocatalysts [18,19]. Furthermore, these chalcogenide thin films can be easily processed by various approaches, such as thermal evaporation [20], chemical-bath deposition [21], hydrothermal processing [22–24], spin-coating of quantum dots [25], and the use of precursors [26–28]. Among these methods, vacuum-based techniques have higher equipment and processing costs. Chemical-bath deposition and hydrothermal methods require large amount of solutions and complicated fabrication procedures. Spin-coating is a facile approach, and has excellent repeatability in tuning the composition

and film thickness. In particular, solar cells based on silver bismuth sulfide ( $\text{AgBiS}_2$ ) have achieved compelling device performance, and the power conversion efficiency has reached more than 9% (certified) [12]. However, such high efficiency was achieved based on the quantum dot strategy, which requires sophisticated synthesis and is still suffering from instability of the quantum dot solution, and the charge transport can be highly affected by ligands.

Devices based on precursors possess relatively poor device performance. In addition, the reported chalcogenide-based phototransistors have either been fabricated based on other FET channels [10] or been mixed with additional transport materials [29]. Most of the chalcogenides do not exhibit proper transfer curves when they are deposited as channel layers. Only a few papers have reported field-effect transistors based on  $\text{Bi}_2\text{S}_3$  [30,31] and  $\text{Bi}_2\text{Se}_3$  [32] single-crystalline nanowires. Furthermore, the solution processing of  $\text{Bi}_2\text{S}_3$  thin films is still challenging, due to the highly anisotropic crystallinity and the stoichiometric imbalance induced by sulfur or bismuth vacancies (i.e., defects). It has been reported that highly crystalline  $\text{Bi}_2\text{Se}_3$  nanoribbons can be prepared via a solvothermal method [33]. However, nanostructured  $\text{Bi}_2\text{Se}_3$  samples are not favorable for device fabrication. More importantly, the understanding of the fundamental properties of  $\text{Bi}_2\text{S}_3$  is still limited, and new insights into charge-carrier dynamics and charge transport

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are urgently needed to further improve the performance of devices based on it.

To address these issues, we introduce a sol-gel method to form  $\text{Bi}_2\text{S}_3$  nanocrystals, and systematically investigate the composition of the precursors and the annealing conditions to carefully tune the morphology, crystallinity, and optoelectronic properties. Then, we study the charge-transport properties using time-resolved microwave conductivity (TRMC) [34] and temperature-dependent photoconductivity. We also fabricate field-effect transistors based on the spin-coating of ultrathin  $\text{Bi}_2\text{S}_3$  layers used as the charge-transport channel, and investigate the influence of the thiourea-bismuth (S-Bi) ratio and thermal annealing on the device performance of  $\text{Bi}_2\text{S}_3$ -based FETs. Furthermore, we introduce the use of these  $\text{Bi}_2\text{S}_3$  transistors for photodetection, and try to further improve the responsivity and reduce the dark current and noise.

## II. EXPERIMENTAL PROCEDURES

### A. Materials

Bismuth nitrate pentahydrate [ $\text{Bi}(\text{NO}_3)_3 \cdot 5\text{H}_2\text{O}$ , 98.0%] is purchased from Sigma Aldrich. Thiourea ( $\text{CH}_4\text{N}_2\text{S}$ , 99%) is purchased from Sinopharm. 2-Methoxyethanol ether (99%) is purchased from Aladdin. The Si substrate [ $\text{SiO}_2(300 \text{ nm})/\text{Si}(725 \mu\text{m})$ ] is purchased from Kaihua Silicon Wafer Corp.

### B. Device fabrication

All devices are fabricated based on precleaned  $\text{SiO}_2/\text{Si}$  substrates, and thin-film transistors (TFTs) are fabricated based on a bottom-gate-top-contact configuration. The substrates are cleaned with deionized water and ethanol with successive sonication, and dried with dry air. Then the substrates are treated with UV radiation and ozone for 15 min. To deposit the channel layers, 0.2 mmol of  $\text{Bi}(\text{NO}_3)_3 \cdot 5\text{H}_2\text{O}$  is dissolved in 1 ml of 2-methoxyethanol. Then 0.3 mmol of thiourea (TU) ( $\text{CH}_4\text{N}_2\text{S}$ ) is added to the solution. By adding different amounts of  $\text{CH}_4\text{N}_2\text{S}$ , we can obtain precursors with different Bi-S ratios. Subsequently,  $\text{Bi}_2\text{S}_3$  solutions with different S contents are spin-coated via spin-coating at 5000 rpm for 40 s on commercial Si substrates. The films obtained are annealed at 100 °C for 10 min in a glove box and further annealed at 280 °C for 10 min in air. Finally, 25 nm of  $\text{C}_{60}$  and 60 nm of Al are thermally deposited through shadow masks as electrodes. The channel length ( $L$ ) and width ( $W$ ) of the TFTs are 100  $\mu\text{m}$  and 2 mm, respectively.

### C. Material characterization

The transmittance and reflectance are acquired based on measured transmittance and reflectance spectra (Lambda 1050 spectrometer, Perkin Elmer). The surface morphology of the  $\text{Bi}_2\text{S}_3$  films is studied with a field-emission

scanning electron microscope (Zeiss GeminiSEM 500). The crystal phases of the  $\text{Bi}_2\text{S}_3$  for various TU contents are indexed by use of x-ray diffraction (XRD, D8 Advance x-ray diffractometer). The thickness and roughness of the  $\text{Bi}_2\text{S}_3$  films obtained are measured with a profilometer (Alpha-Step D-500). The photoconductivity of the  $\text{Bi}_2\text{S}_3$  films is tested with a semiconductor analyzer (Keysight, B1500A) under white LED illumination (Thorlabs). The temperature is modulated with a cryostat (Instec, LN2-P4C, C100W) under vacuum.

### D. Device characterization

The electrical performance of the TFTs, such as the transfer curves, output curves, noise current, external quantum efficiency (EQE), and response times, is collected with a semiconductor analyzer (Keysight, B1500A) at -20 °C. The temperature-dependent transfer curves are measured with a semiconductor analyzer (Keysight, B1500A), and the temperature is controlled with a cryostat (Instec, mK2000B, LN2-P4C,C100W) under vacuum. The optoelectronic performance of the devices, including light-intensity-dependent transfer curves and output curves, is tested via a semiconductor analyzer (Keysight, B1500A) under LED illumination (Thorlabs).

## III. RESULTS AND DISCUSSION

In the work presented here, we develop a new precursor route to depositing uniform high-quality  $\text{Bi}_2\text{S}_3$  thin films. The  $\text{Bi}_2\text{S}_3$  precursors are prepared by dissolving bismuth nitrate [ $\text{Bi}(\text{NO}_3)_3 \cdot 5\text{H}_2\text{O}$ ] in 2-methoxyethanol. Then, various amounts of TU are added to the bismuth nitrate solution to form a  $\text{Bi}_2\text{S}_3$  sol [35] (see Supplementary Note 1 [36] for more details); we can clearly observe the transparent solution change to yellow, indicating the formation of  $\text{Bi}_2\text{S}_3$  nanocrystal seeds (see Supplementary Video 1 [36]). It is also worth noting that with an increase in the thiourea-bismuth (S-Bi) ratio, the color of the  $\text{Bi}_2\text{S}_3$  precursor changes from light yellow to brown, as shown in Fig. S1(a). We then spin-coat the prepared precursor and thermally anneal the precursor film using a two-step approach; that is, we preanneal the film at a low temperature (around 100 °C) for a short time (10 min) and then fully anneal the preannealed film at an elevated temperature (greater than 200 °C) for a certain time (3–15 min). Figure 1(a) depicts these processes schematically, and Fig. S1(b) displays optical images of  $\text{Bi}_2\text{S}_3$  deposited with precursors containing various S-Bi ratios.

Interestingly, the color of the  $\text{Bi}_2\text{S}_3$  thin films obtained also becomes darker with an increase in the amount of TU in the precursor. Figure S2 shows the surface roughness of these films recorded with a profilometer, and all of these films show similar values of approximately 3 nm. However, the films prepared with an S-Bi ratio of 1.5 exhibit a relatively small variation, and the films prepared with a

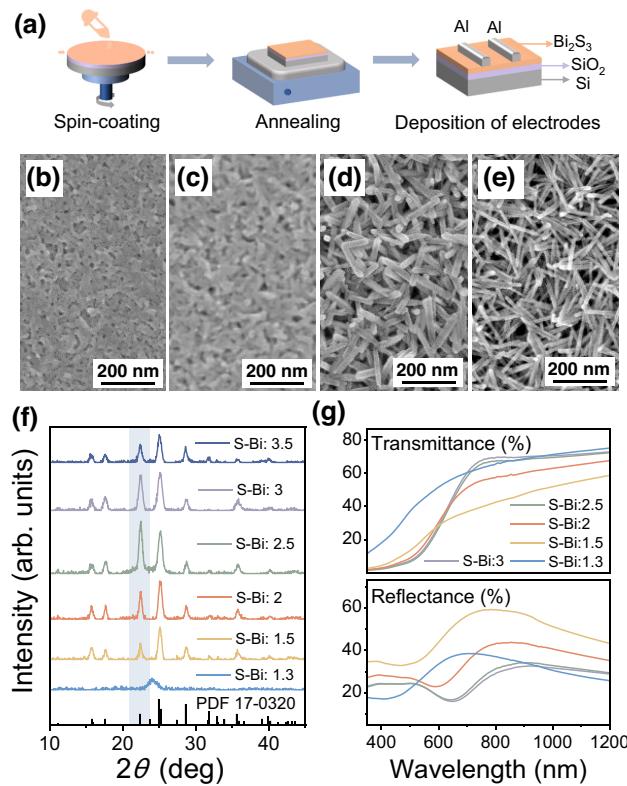


FIG. 1. (a) Schematic illustration of the processing of Bi<sub>2</sub>S<sub>3</sub> thin films and FET devices. (b)–(e) Comparison of scanning-electron-microscope images of Bi<sub>2</sub>S<sub>3</sub> thin films prepared with various S-Bi ratios: (b) 1.5, (c) 1.8, (d) 2, (e) 3. (f) Recorded XRD patterns. (g) Transmittance and reflectance spectra of these thin films with gradually increasing S-Bi ratio.

large S-Bi ratio (2.5 or 3) show a large fluctuation. Figure S3 displays spin-coated large-area Bi<sub>2</sub>S<sub>3</sub> films on flexible substrates, indicating fairly good uniformity and superior processability on various substrates.

To further identify the morphology differences, we perform scanning-electron-microscopy (SEM) measurements, as shown in Figs. 1(b)–1(d). Intriguingly, we find that the films obtained possess increased numbers of pinholes with an increased S-Bi ratio. The Bi<sub>2</sub>S<sub>3</sub> films spin-coated with large S-Bi ratios show a typical nanorod structure, and the films become porous. In addition, the diameter decreases but the length of the nanorods increases when the S-Bi ratio is increased from 1.8 to 3. In contrast, the Bi<sub>2</sub>S<sub>3</sub> films prepared with an S-Bi ratio of 1.5 are dense with minimal pinholes, despite the small grain size. In addition, we also compare the crystallinity by recording x-ray diffraction patterns, as shown in Fig. 1(f). All of these films exhibit a typical Bi<sub>2</sub>S<sub>3</sub> crystal structure, and the characteristic peaks match well with the standard Joint Committee on Powder Diffraction Standards card for the Bi<sub>2</sub>S<sub>3</sub> XRD pattern (PDF 17-0320). We also find that the XRD intensity increases gradually when the S-Bi ratio is increased

from 1.3 to 2.5. However, the XRD intensity decreases slightly when the S-Bi ratio is increased further from 2.5 to 3, which is consistent with the SEM results. In addition, we also determine the crystallinity of these films based on the ratio between the area of the crystalline peaks ( $A_c$ ) and the sum of the areas of the amorphous ( $A_a$ ) and crystalline peaks, according to

$$\chi_c = \frac{A_c}{A_c + A_a}. \quad (1)$$

Interestingly, we also find a decreased crystallinity when the S-Bi ratio is larger than 2.5, as shown in Fig. S4. Furthermore, Fig. 1(g) presents transmittance and reflectance spectra of Bi<sub>2</sub>S<sub>3</sub> films prepared with various S-Bi ratios. The absorption edge is redshifted for the high-S-Bi-ratio films, which is in line with the recorded optical images. More importantly, the samples prepared with an S-Bi ratio of 1.5 exhibit the largest reflectance, reflecting a high refractive index (corresponding to, e.g., high crystallinity and high density) and low scattering losses.

As aforementioned, understanding the charge-carrier dynamics and charge-transport properties is critical to elucidating the working mechanism and providing key messages for designing optoelectronic devices. Here, we introduce the use of TRMC to evaluate the fundamental properties of the charge carriers, as shown in Fig. 2. TRMC measurement is a contactless technique and can probe the charge-carrier dynamics of various semiconductors [37–39]. The thin-film samples are excited with a fast laser pulse (approximately 5 ns), which generates a certain amount of carriers within the semiconductor thin film and leads to absorption of microwaves. Then, the transmittance or reflectance of the microwaves can be monitored as a function of time after photoexcitation, resulting in measurement of the decay. To enhance the microwave sensitivity, we introduce a high-*Q* microwave cavity, which has a resonance frequency of approximately 4.3 GHz. Note that the resonance frequency is mainly determined by the microwave cavity, and is tuned with a tuning screw. We also measure the TRMC decay at approximately 8.6 GHz (as shown in Fig. S5), which is consistent with the results obtained from the measurements at approximately 4.3 GHz. The source power of the microwaves is set to 1 mW, and the excitation laser wavelength is 532 nm, with a tunable pulse fluence of 0.01–100  $\mu\text{J cm}^{-2}$ . The changes in the microwave conductivity of the samples can be correlated with the changes in the detected microwave power by use of a sensitivity factor  $K$  [38] (see Supplementary Note 2 [36] for more details) as follows:

$$\frac{\Delta P(t)}{P} = -K \Delta G(t), \quad (2)$$

where  $\Delta P(t)/P$  is the change in the detected microwave power and  $\Delta G(t)$  is the change in the conductance. Based

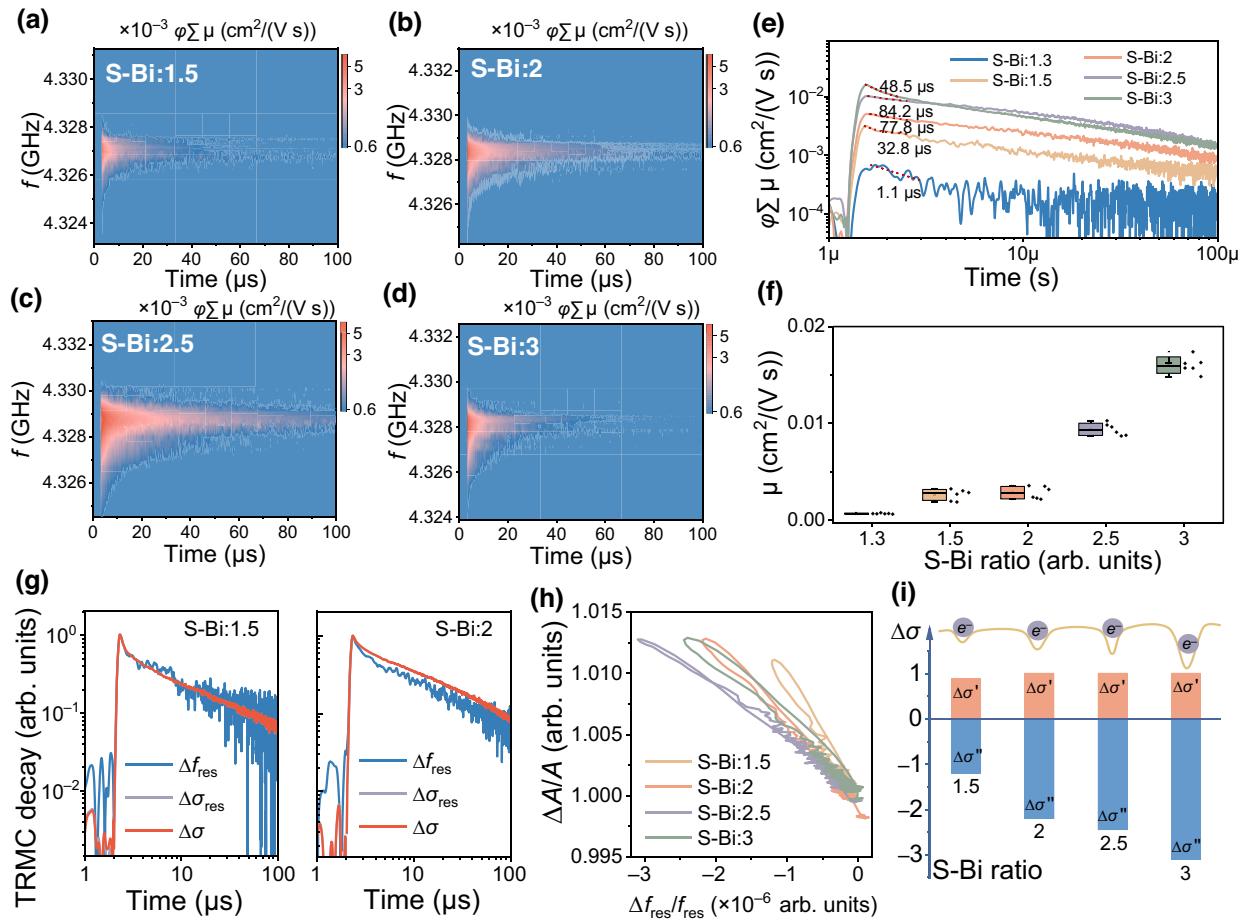


FIG. 2. (a)–(d) Comparison of two-dimensional (2D) TRMC spectra of  $\text{Bi}_2\text{S}_3$  thin films prepared with various S-Bi ratios (at a fluence of approximately  $5 \mu\text{J}/\text{cm}^2$ ): (a) 1.5, (b) 2, (c) 2.5, (d) 3. Comparison of (e) TRMC decay (at a fluence of approximately  $5 \mu\text{J}/\text{cm}^2$ ) at the resonance frequency and (f)  $\varphi \sum \mu$  for  $\text{Bi}_2\text{S}_3$  thin films prepared with gradually increasing S-Bi ratios (1.3–3). (g) Comparison of the hysteresis loops of the complex photoconductivity of  $\text{Bi}_2\text{S}_3$  thin films prepared with varying S-Bi ratios. (h) Real and imaginary parts of the photoconductive decay of samples based on S-Bi ratios of 1.5 and 2. (i) Schematic illustration of the energetic depth of the trap states.

on the maximum change in the conductance and the incident light intensity ( $I_0$ ), we can determine the mixed mobility ( $\sum \mu$ ) as follows [40]:

$$\varphi \sum \mu = \frac{\Delta G_{\max}}{I_0 \beta_{\text{TRMC}} e F_A}, \quad (3)$$

where  $\varphi$  is the charge-carrier generation yield,  $\beta_{\text{TRMC}}$  is the ratio between the broad and narrow inner dimensions of the waveguide,  $e$  is the elementary charge, and  $F_A$  is the optical attenuation.

Specifically, we record the 2D TRMC spectra of  $\text{Bi}_2\text{S}_3$  thin films prepared with various S-Bi ratios, as shown in Figs. 2(a)–2(d), and Fig. 2(g) shows typical time-resolved TRMC decays extracted at the resonance frequency ( $f_{\text{res}}$ ). Similarly to the trend observed by SEM and XRD, the TRMC lifetime increases first until the S-Bi ratio reaches 2.5, and then decreases slightly when the S-Bi ratio is 3. In contrast, the TRMC amplitude (proportional to the

mobility) increases gradually up to an S-Bi ratio of 3, as shown in Figs. 2(e) and 2(f). We also fit the fitted initial TRMC decays, shown in Fig. 2(e), and extract the photoconductivity charge-carrier lifetimes by using a single exponential function. The increased product of the generation yield and mobility ( $\varphi \sum \mu$ ) and the increased lifetime are attributed mainly to the enhanced crystallinity and increased grain size. The reduced charge-carrier lifetime of the  $\text{Bi}_2\text{S}_3$  samples prepared with a large S-Bi ratio can be attributed to the increased number of grain boundaries and increased surface area. To further reveal the complex photoconductivity of the solution-processed  $\text{Bi}_2\text{S}_3$  thin films, we also decouple the photoconductive effect and photodielectric effect by resolving the frequency shift ( $\Delta f_0$ ) and the amplitude change ( $\Delta A$ ) as a function of time. These values are normalized to the initial resonance frequency ( $f_0$ ) and amplitude ( $A$ ). Then, we plot the hysteresis loop and also compare the real and imaginary parts of the photoconductivity, as shown in Fig. 2(h). The

complex photoconductivity is a sign of the energetic depth of the traps [41,42]. Interestingly, we find that the imaginary part of the photoconductivity increases gradually with increased S-Bi ratio. However, the real part of the photoconductivity does not change much. The TRMC decay also implies that the samples with an excessive amount of TU show a faster imaginary photoconductive decay. Based on these observations, we can qualitatively compare the shallow and deep traps based on the relative values of the normalized amplitude change and frequency shift for these  $\text{Bi}_2\text{S}_3$  thin films, as shown in Fig. 2(i). This suggests that the  $\text{Bi}_2\text{S}_3$  thin films prepared with large S-Bi ratios may have an increased amount of relatively deeper trap states, assuming that the crystal grain size and shape do not affect the charge transport [43]. However, it is also worth noting that the morphology of the crystal grains may also influence the imaginary part of the photoconductivity to some extent. Additionally, we study the influence of the annealing time (in the second stage, at 280 °C) on the charge-carrier dynamics, as shown in Fig. S6. The charge-carrier lifetime increases with an increase in the annealing time, which could also be related to the improved crystallinity. Moreover, the recorded photoconductive gain of  $\text{Bi}_2\text{S}_3$  photoconductors annealed for 10 min showed a higher *on/off* ratio and a reduced dark conductivity (Fig. S7), suggesting full conversion of the amorphous state. Based on the above analysis of the TRMC, we can retrieve basic information about both  $\varphi \sum \mu$  and the lifetime ( $\tau$ ) within the  $\text{Bi}_2\text{S}_3$  crystal grains; these are the quantities that mainly determine the photoconductive gain in operational devices, given by

$$\text{gain} = \frac{\tau}{t_{\text{tr}}} = \frac{\mu \tau V}{L^2}, \quad (4)$$

where  $\tau$  is the carrier lifetime,  $t_{\text{tr}}$  is the carrier transit time,  $\mu$  is the carrier mobility,  $L$  is the channel length, and  $V$  is the bias voltage. Ideally, a large mobility and long lifetime are desirable for high-performance photodetection. However, the real charge transport within a device is also significantly affected by the morphology. The porous nature of  $\text{Bi}_2\text{S}_3$  films composed of nanoribbons significantly reduces the charge-transport channel in an FET. Hence, there is a trade-off between charge-carrier dynamics and morphology in real cases.

Having knowledge of the optoelectronic properties, we now turn to the consideration of devices. A set of thin-film transistors are fabricated based on silicon substrates with thin  $\text{SiO}_2$  layers on top. Note that a thin layer of  $\text{C}_{60}$  (25 nm) is deposited to isolate the Al electrodes from direct contact with the  $\text{Bi}_2\text{S}_3$  layer, as the hot Al vapor may react during evaporation with the excess S in the  $\text{Bi}_2\text{S}_3$  thin film. We also measure the typical transfer curves of FETs fabricated without a  $\text{C}_{60}$  interlayer, as shown in Fig. S8. These devices show a much higher *off*-state current, indicating

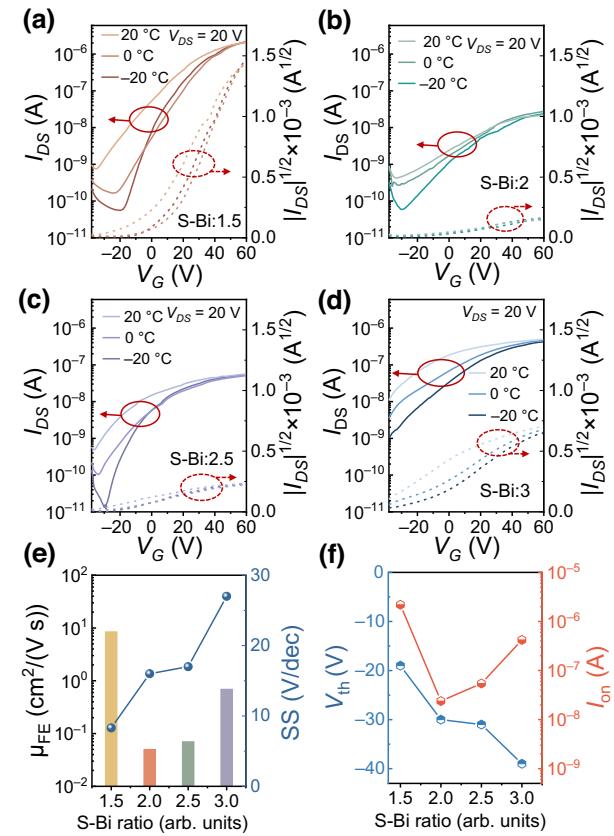


FIG. 3. (a)–(d) Temperature-dependent transfer curves of FETs fabricated based on  $\text{Bi}_2\text{S}_3$  thin films with various S-Bi ratios: (a) 1.5, (b) 2, (c) 2.5, and (d) 3. (e) FET mobility and subthreshold swing. (f)  $V_{th}$  and  $I_{on}$  of  $\text{Bi}_2\text{S}_3$  FETs fabricated with various S-Bi ratios.

that the FETs cannot be fully switched off. We first compare the impact of the S-Bi ratio on the device performance (Fig. 3), and find that the transfer curves deteriorate with an increase in the TU content of the precursor, and we do not observe any transfer curves for devices based on an S-Bi ratio of 1.3. In addition, the *off*-state current of these high-S-Bi-ratio FETs cannot be fully switched off, and the turned-on current is reduced, due mainly to the porous nature of the channels. We also compare the inferred FET mobility [44], subthreshold swing (SS), turn-on voltage ( $V_{th}$ ), and turned-on current ( $I_{on}$ ) for devices prepared with various S-Bi ratios, as shown in Figs. 3(e) and 3(f). It is found the devices based on an S-Bi ratio of 1.5 exhibit the smallest SS and the highest FET mobility and  $I_{on}$ . The turn-on voltage shifts to larger reverse bias voltage with an increase in the S-Bi ratio. Not surprisingly, the devices based on  $\text{Bi}_2\text{S}_3$  prepared with an S-Bi ratio of 1.5 show fairly good transfer curves, benefiting mainly from the dense morphology, relatively high crystallinity, fairly good charge transport, and shallow nature of the traps. In addition, we compare the S-Bi-ratio-dependent device performance, e.g., the responsivity ( $R$ ) and the specific

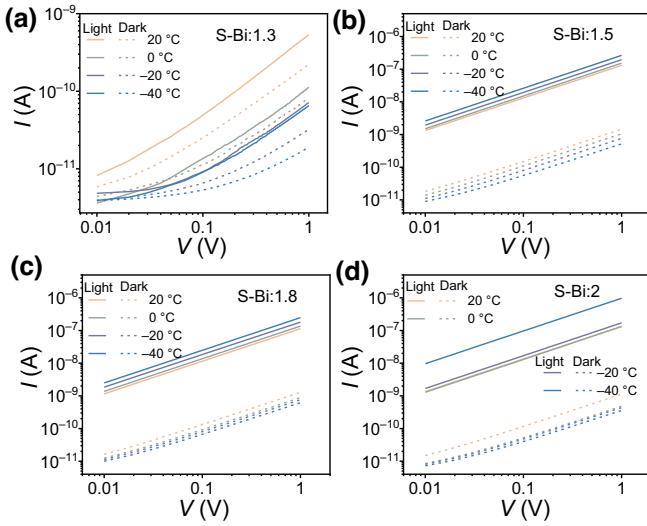


FIG. 4. Temperature-dependent dark conductivity and photoconductivity under white LED illumination of  $\text{Bi}_2\text{S}_3$  thin films prepared with various S-Bi ratios: (a) 1.3, (b) 1.5, (c) 1.8, (d) 2.

detectivity ( $D^*$ ), given by

$$D^* = \frac{R\sqrt{A}}{i_{\text{noise}}}, \quad (5)$$

where  $A$  is the device area. It is found that the responsivity decreases slightly when the S-Bi ratio is increased to 2, and increases slightly with a further increase in the S-Bi ratio (Fig. S9). However, the noise density increases significantly with an increase in the S-Bi ratio. The observed trend can be attributed to the changes in mobility, carrier lifetime, and also morphology, which suggests that the device with an S-Bi ratio of 1.5 shows the highest specific detectivity. Moreover, all of these devices show a slight temperature dependence, but this is not so prominent as in the case of single-crystal devices [31]. To further elucidate the underlying mechanism, we perform measurements of the conductivity of these  $\text{Bi}_2\text{S}_3$  thin films, as shown in Fig. S10.

Compared with the dark current, the devices based on  $\text{Bi}_2\text{S}_3$  with an S-Bi ratio of 1.3 do not show noticeable photoconductivity, mainly due to insufficient crystallization and the deficit of S. The photoconductors based on  $\text{Bi}_2\text{S}_3$  with an S-Bi ratio of 1.5 exhibit the largest *on*:*off* ratio, and with a further increase in the S-Bi ratio, the photocurrent does not show a significant increase, but the dark current increases with an increase in the TU content of the precursor, suggesting that the S source is excessive and results in S-rich phases. We also conduct temperature-dependent photoconductive measurements, as shown in Fig. 4, which confirm a similar trend in the dependence on the S-Bi ratio, and the calculated activation energy in the dark for these  $\text{Bi}_2\text{S}_3$  photoconductors also decreases with an increase in the S-Bi ratio, as shown in Fig. 5. The

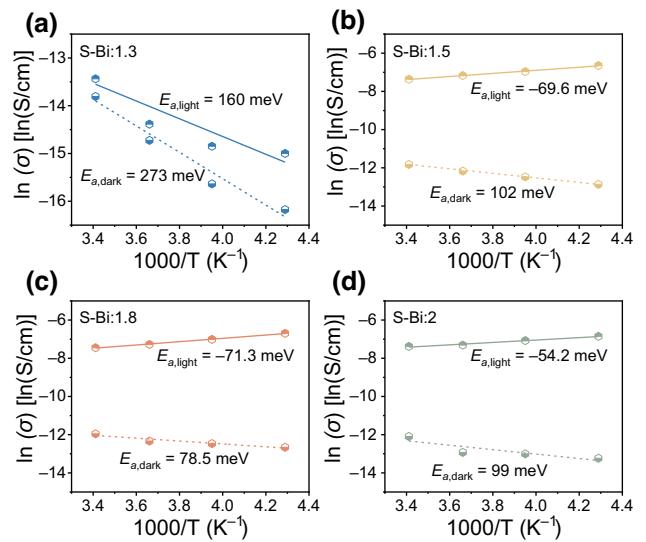


FIG. 5. Fitting of the temperature-dependent dark conductivity and photoconductivity of  $\text{Bi}_2\text{S}_3$  thin films prepared with various S-Bi ratios: (a) 1.3, (b) 1.5, (c) 1.8, (d) 2.

positive activation energy is mainly due to the reduced thermalized carrier density at low temperature. More interestingly, the activation energy of the photoconductivity changes to negative when the S-Bi ratio is greater than 1.5, indicating that the charge-transport mode can be switched from a localized-state-induced “hopping” mechanism to a more “bandlike” transport [45,46]. The photoconductivity is independent of the carrier density, as the number of dark carriers ( $n_{\text{dark}}$ ) is much less than the photoexcited carrier density ( $n_{\text{ph}}$ ), which is the same for all samples at various temperatures. Hence, the change in the conductivity  $\sigma$  should be attributed mainly to a change in the mobility  $\mu$ , given by

$$\sigma_{\text{ph}} = (n_{\text{dark}} + n_{\text{ph}})q\mu \approx n_{\text{ph}}q\mu. \quad (6)$$

The S vacancies caused by a S deficit could introduce a large number of deep-level trap states [47] and result in “hopping” transport [48,49]. According to the literature on chalcogenides, localized defects can be effectively eliminated by sulfurization [50]. Hence, an S-Bi ratio greater than 1.5 is required to facilitate charge transport. Furthermore, the sample with a S-Bi ratio of 2, with excess S, also shows a slightly increased activation energy, indicating deteriorated charge transport at a high S-Bi ratio, which is also consistent with the FET performance.

Considering that excellent charge transport, the highest photoconductive gain, and the best FET performance are obtained with  $\text{Bi}_2\text{S}_3$  with a stoichiometric S-Bi ratio of 1.5, we systematically characterize the performance metrics of phototransistors fabricated with this precursor composition at a thermoelectric-cooling temperature of  $-20^\circ\text{C}$ . Figure 6(a) displays a contour plot of the photosensitivity

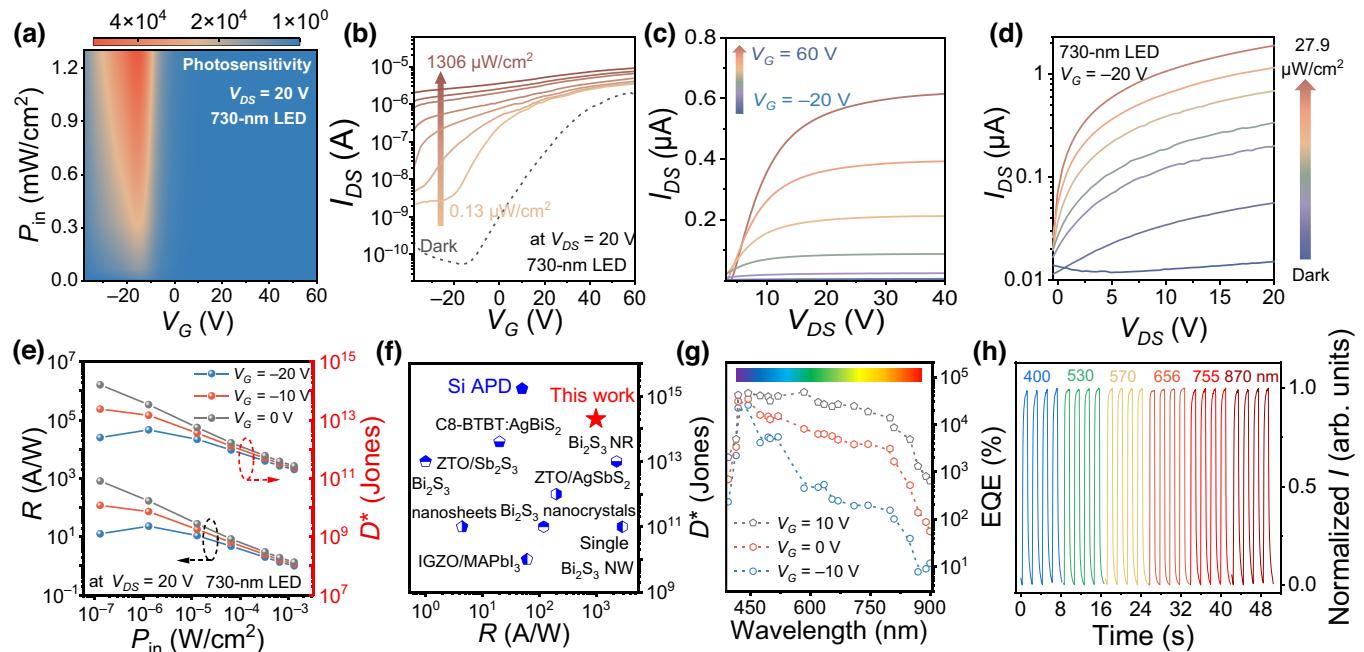


FIG. 6. (a) Contour plot of photosensitivity, (b) light-intensity-dependent transfer curves, (c) gate-voltage-dependent output curves, (d) light-intensity-dependent output curves, (e) light-intensity-dependent responsivity and specific detectivity, (f) comparison of the specific detectivity [Avalanche Photodiode (APD), 2,7-diethyl benzothiophene [3,2-*b*]benzothiophene (C8-BTBT), Nanorods (NR), Zinc-Tin Oxide (ZTO), Indium Gallium Zinc Oxide (IGZO), Nanowire (NW)], (g) EQE spectra, and (h) temporal photoresponses under illumination at various wavelengths of optimized  $\text{Bi}_2\text{S}_3$  phototransistors fabricated based on an S-Bi ratio of 1.5.

as a function of the incident light intensity ( $P_{\text{in}}$ ) and gate voltage ( $V_G$ ) under illumination with a 730-nm LED.

Figures 6(b)–6(d) present the light-intensity-dependent transfer curves and gate-voltage-modulated and light-intensity-dependent output curves, which suggest that the optimized  $\text{Bi}_2\text{S}_3$  FETs can be very sensitive to incident photons and that the devices can also be well tuned by the gate voltage. Similarly, we also present the transfer curves and photosensitivity under blue (400 nm) and green (530 nm) light in Fig. S11. Figure 6(e) presents the light-intensity-dependent responsivity, which shows the typical  $P_{\text{in}}$ -dependent responsivity of phototransistors, that is, the responsivity decreases with an increase in light intensity. This nonlinearity has been well recognized in the field of photoconductors and phototransistors, and can be attributed to increased recombination losses at high irradiance [51]. The highest responsivity can reach 1000 A/W under extremely weak illumination. We also record the noise spectra of the optimized devices, as shown in Fig. S12, based on which we can further determine the specific detectivity. The calculated detectivity is shown in Fig. 6(e), and the highest  $D^*$  reaches  $10^{14}$  Jones, which is the highest value found, compared with literature reports [Fig. 6(f)] [4,10,29,52–56]. Figure 6(g) displays the EQE spectra of the  $\text{Bi}_2\text{S}_3$  phototransistors, and the temporal photoresponses of the devices under illumination at various wavelengths are shown in Fig. 6(h); these

results show a relatively high EQE and fast photoresponse covering the whole visible and near-infrared regions. Figure S13 displays the transient photocurrent of the FETs under illumination with a 100-Hz-modulated 650-nm LED. The rise times ( $t_r$ ) and fall times ( $t_f$ ) of the devices are determined to be 152 and 270  $\mu\text{s}$ , respectively. The response time is relatively slow compared with that of photodiodes. However, the response time could be enhanced by reducing the transport-channel width and increasing the bias voltage. Considering the extremely high responsivity and detectivity, we also evaluate the capability of these phototransistors for weak-light detection. The transfer curves of the  $\text{Bi}_2\text{S}_3$  phototransistors show a clear increase even under weak illumination of 500  $\text{pW}/\text{cm}^2$ , as shown in Fig. S14, indicating ultrahigh photosensitivity and great potential for weak-light detection, in fields such as security monitoring, light communication, and medical diagnosis.

#### IV. CONCLUSION

In conclusion, we develop a facile solution-processing technique for depositing high-quality  $\text{Bi}_2\text{S}_3$  thin films, and the nanocrystal morphology, crystallinity, and optoelectronic properties can be easily tuned by optimizing the S-Bi ratio in the precursors and the thermal-annealing conditions. Achieving the stoichiometric S-Bi ratio is found to be critical to addressing the stoichiometric-imbalance

issue and has a profound impact on high-performance  $\text{Bi}_2\text{S}_3$ -based photoconductors and field-effect transistors, since both S-rich and Bi-rich compositions can be detrimental and can easily result in the formation of trap states. We also systematically investigate the charge-carrier dynamics of the  $\text{Bi}_2\text{S}_3$  thin films, and features of the carrier lifetime, mobility, and traps are revealed. Furthermore, we fabricate ultrasensitive phototransistors based on optimized  $\text{Bi}_2\text{S}_3$  thin films, and achieve a high detectivity, which shows great potential for weak-light detection.

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