

Effect of Strain on Room-Temperature Spin Transport in $\text{Si}_{0.1}\text{Ge}_{0.9}$

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We report a strain effect on spin transport in semiconductors that exhibit Ge-like conduction bands at room temperature. Using four-terminal nonlocal spin-transport measurements in lateral spin-valve devices, we experimentally estimate the spin diffusion length (λ) of Ge and strained $\text{Si}_{0.1}\text{Ge}_{0.9}$ with two different carrier concentrations. Despite the Ge-like electronic band structure, the obtained λ of a strained $\text{Si}_{0.1}\text{Ge}_{0.9}$ is comparable to that of a Si channel. We discuss a possible mechanism of the strain-induced enhancement of λ at room temperature. As a consequence, we demonstrate the electrical detection of 5- μm lateral spin transport in the strained $\text{Si}_{0.1}\text{Ge}_{0.9}$ by applying an electric field at room temperature.

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I. INTRODUCTION

Lattice strain in semiconductors enables modification of their electronic band structure, including their band gap, electronic charge density, and phonon frequency [1–6]. In particular, strained III-V and group-IV semiconductors have been widely investigated theoretically and experimentally because lattice strain can be applied in these materials through heterointerfaces formed via epitaxial growth techniques, leading to advances in the field of condensed-matter physics and to various applications [7–14]. Recent studies of two-dimensional semiconductors have revealed an alternative strain effect on the electronic band structures and band gaps even in graphene [15,16], transition-metal dichalcogenides [17,18], and monolayer honeycomb elements [19–21].

Thus far, electron and hole mobility have been enhanced by in-plane biaxial or longitudinal uniaxial strain in group-IV channel layers [7–11]. For example in Ge(111), the in-plane biaxial and tensile strain causes energy splitting between a onefold and threefold L valley in the conduction band, leading to electrons occupying a onefold L valley with greater electron mobility [22–24]. In addition, the lattice strain in Ge induced by Si dramatically changes the physical properties because of the energy proximity

(approximately 140 meV) between the conduction-band minima at L points and the Γ point. Tensile strain is well known to decrease the energy difference between the L and Γ gaps, enhancing the optically accessible nature of Ge [25,26]. Even for spins, Bottegoni *et al.* and Pezzoli *et al.* have reported on the high spin polarization and long spin lifetime in strained Ge layers or Ge quantum wells grown on SiGe [27–30], respectively, lifting the heavy-hole–light-hole degeneracy in the valence band of Ge. Recently, Cesari *et al.* [31] used strained Ge-rich $\text{Ge}_{1-x}\text{Sn}_x$ alloys to effectively explore spin-related optical properties.

Theoretical and experimental studies on the effect of strain on the spin-relaxation mechanism in Si and/or Ge have also been reported. In particular, because the dominant spin relaxation pathway in Si and Ge is spin-flip scattering between the conduction-band valleys (i.e., intervalley spin-flip scattering) induced by electron-phonon [32–38] and electron-impurity interactions [39–41] through the spin-orbit coupling in host materials and impurities, respectively. Tang *et al.*, Li *et al.*, and Chalaev *et al.* have theoretically predicted suppression of intervalley spin-flip scattering through the strain, leading to lifting of the degenerate valleys at six Δ points for Si or at four L points for Ge [33,35,42]. Even in the case of pure spin-current transport, the impurity-induced intervalley spin-flip scattering was partly suppressed at low temperatures

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when a strained heavily doped n -Si_{0.1}Ge_{0.9}(111) layer was used [43], where the Ge-rich Si_{1-x}Ge_x exhibited a Ge-like electronic band structure with conduction-band minima at L points. However, because of the marked influences of electron-phonon interactions and experimental difficulties, it has been difficult to clarify the effect of the lattice strain on the spin transport in group-IV semiconductors at room temperature.

Here we report a strain-induced enhancement in one of the spin-related physical properties, the spin-diffusion length (λ), in a semiconductor at room temperature. Note that the difference in the band structure between Si_{0.1}Ge_{0.9} and pure Ge is quite small [44,45] and the position of the Fermi level (E_F) can easily be tuned by varying the doping concentration in the Si_{0.1}Ge_{0.9} and pure Ge channel. Using four-terminal nonlocal spin-transport measurements in lateral spin-valve (LSV) devices, we clearly observe room-temperature spin transport in the strained Si_{0.1}Ge_{0.9} and experimentally estimate the λ of the strained Si_{0.1}Ge_{0.9}. As a consequence, for an electron carrier concentration (n) of $1 \times 10^{18} \text{ cm}^{-3}$, the λ of the strained Si_{0.1}Ge_{0.9} is much larger than that of the pure Ge. Despite the Ge-like electronic band structure, the obtained λ of the strained Si_{0.1}Ge_{0.9} is comparable to that of a Si channel at room temperature in Ref. [40]. We discuss a possible origin of the strain-induced enhancement of λ and demonstrate the electrical detection of 5- μm lateral spin transport in the strained Si_{0.1}Ge_{0.9} by applying an electric field at room temperature.

II. RESULTS AND DISCUSSION

A. Growth and electrical properties of strained Si_{0.1}Ge_{0.9}

We prepare coherently grown Ge-rich Si_{1-x}Ge_x (Si_{0.1}Ge_{0.9}) and Ge spin-transport layers on a Ge buffer layer on Si(111) substrate. Figure 1(a) shows a schematic and the nominal thicknesses of the n -Si_{0.1}Ge_{0.9} spin-transport layer grown on a Ge buffer layer/Si(111). Here, the heterostructure is grown by molecular beam epitaxy (MBE) as follows. First, a Ge(111) buffer layer on an undoped Si(111) substrate ($\rho \sim 1000 \text{ } \Omega \text{ cm}$) is formed via a two-step growth method [46], where the first undoped Ge layer (approximately 30 nm) is grown at 350 °C (LT-Ge), followed by an undoped Ge layer (approximately 500 nm) grown at 700 °C (HT-Ge). The Ge buffer layer is relaxed, and misfit dislocations are confined near the Ge/Si(111) interface, as shown in Ref. [43]. On top of the HT-Ge layer, a 70-nm-thick phosphorus (P)-doped n -Si_{0.1}Ge_{0.9}(111) layer is grown by MBE at 350 °C [47]. Finally, a 7-nm-thick P δ -doped Ge layer with a 0.3-nm-thick Si layer is grown on top of the spin-transport layer for the Schottky tunnel conduction of electrons in spin-transport measurements [48]. As a reference, 140-nm-thick P-doped n -Ge(111) layers on a HT-Ge layer

(approximately 70 nm) [49] are also prepared to verify the strain effect. Figure 1(b) displays a two-dimensional x-ray diffraction (XRD) reciprocal space map for the grown Si_{0.1}Ge_{0.9}/Ge/Si(111) heterostructure, where Q_x and Q_z are the reciprocal space lattice parameters for in-plane and out-of-plane, respectively. Because of the difference in the lattice constant between Si and Ge, the (Q_x , Q_z) positions between the Si substrate and the grown Ge buffer layer differ dramatically. However, we observe matching of the Q_x parameter between the Ge buffer layer and the grown Si_{0.1}Ge_{0.9} layer. This matching means that the grown Si_{0.1}Ge_{0.9} layer on the Ge buffer layer is fully strained toward the layer plane, indicating biaxial tensile strain (ϵ_x) in (111). A comparison of the lattice constant of the strained Si_{0.1}Ge_{0.9} and that reported for unstrained Si_{0.1}Ge_{0.9} [50] indicates that the value of ϵ_x should be 0.64%–0.66%. Here we should also consider the presence of the tensile strain ($\epsilon_x \sim 0.2\%$) in the Ge buffer layer from the Si substrate due to the mismatch in thermal expansion coefficients between Ge and Si. However, there was no impact on the electron Hall mobility and spin-diffusion length at room temperature for the Ge spin-transport layers studied in the present study. Thus, in the strained Si_{0.1}Ge_{0.9}, we can simply predict that the four degenerate L valleys are lifted into a onefold low-energy valley and threefold higher-energy valleys [22–24,33,35], as depicted in the inset of Fig. 1(c).

To measure the electrical properties of the strained Si_{0.1}Ge_{0.9} layer, we process the heterostructure into Hall bar devices. From the longitudinal resistance (V_{xx}/I) and Hall voltage (V_{xy}) measurements, we determine the electrical resistivity (ρ) and electron carrier concentration (n) and estimated the electron mobility (μ) [Fig. 1(c)] at room temperature for the strained Si_{0.1}Ge_{0.9} and Ge layers. By tuning the doping concentration of P in the channel layers, we prepared Ge and strained Si_{0.1}Ge_{0.9} layers with two different n values: approximately $5 \times 10^{18} \text{ cm}^{-3}$ and approximately $1 \times 10^{18} \text{ cm}^{-3}$ [bottom of Fig. 1(c)]. Thus, in the present study, we compare these four channels to clarify the strain effect on room-temperature spin transport in semiconductors. Here the HT-Ge layer is undoped but exhibits p -type conduction due to the defect-induced hole generation [46], leading to the formation of a p - n junction at the n -SiGe/HT-Ge heterointerface [Fig. 1(d)]. As a result, the electron spin diffusion toward the Ge-on-Si substrate can be suppressed. Of note, the strained Si_{0.1}Ge_{0.9} layer with $n \sim 1 \times 10^{18} \text{ cm}^{-3}$ clearly shows an almost twofold enhancement in μ at room temperature. This enhancement is consistent with the increase in the population of conduction electrons in the lowest L valley, which has a relatively small electron effective mass compared with the other three valleys [22–24]. On the basis of the literature [51,52], the energy splitting of the conduction L valleys for the strained Si_{0.1}Ge_{0.9} is theoretically expected to be 55–90 meV.

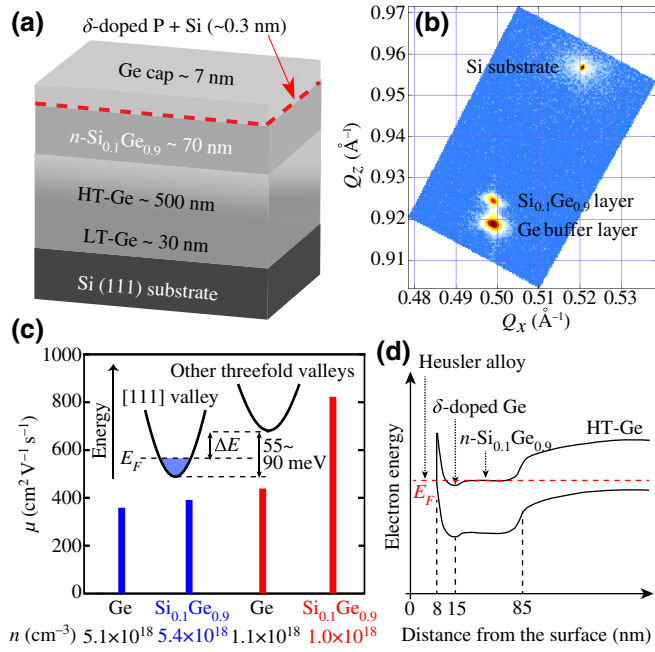


FIG. 1. (a) Schematic of the grown n -Si_{0.1}Ge_{0.9}/Ge/Si(111) heterostructure for spin-transport measurements of the strained n -Si_{0.1}Ge_{0.9}. (b) Asymmetric (153) reciprocal space map of the Si_{0.1}Ge_{0.9}/Ge/Si(111). The Si_{0.1}Ge_{0.9} layer is fully strained on the Ge buffer layer. (c) Electron Hall mobility of the strained n -Si_{0.1}Ge_{0.9} and n -Ge with relatively high (approximately 5×10^{18} cm⁻³) and low (approximately 1×10^{18} cm⁻³) carrier concentrations at 300 K. The inset shows a schematic of the conduction-band valleys in the strained n -Si_{0.1}Ge_{0.9}. (d) Schematic of a band lineup of a part of the Co-based Heusler/P δ -doped Ge/ n -Si_{0.1}Ge_{0.9}/HT-Ge (p -Ge).

B. Spin transport in strained Si_{0.1}Ge_{0.9}

To examine the spin transport in semiconductors, we focus on four-terminal nonlocal voltage measurements in LSV devices [53,54]. The top view of a fabricated LSV device with the strained n -Si_{0.1}Ge_{0.9} channel layer is shown in Fig. 2(a), where d is the edge-to-edge distance between the spin injector and the detector. The size of the spin injector (detector) contact is $0.4 \times 5.0 \mu\text{m}^2$ ($1.0 \times 5.0 \mu\text{m}^2$). Details of the fabrication processes and top views of similar LSV devices have been reported elsewhere [48,55]. As the spin injector and detector materials, we use Co-based Heusler alloys [56,57] such as Co₂FeAl_{0.5}Si_{0.5} and Co₂MnSi, which are grown by MBE on top of the Schottky tunnel barriers [48,58]. Here, a 0.7-nm-thick Fe layer is inserted between the Co-based Heusler alloy and the Ge cap layer to obtain large spin signals at room temperature [59,60]. All the measurements are carried out at room temperature (approximately 298 K) by applying a negative direct current ($I < 0$), for which the spin-polarized electrons are injected into the

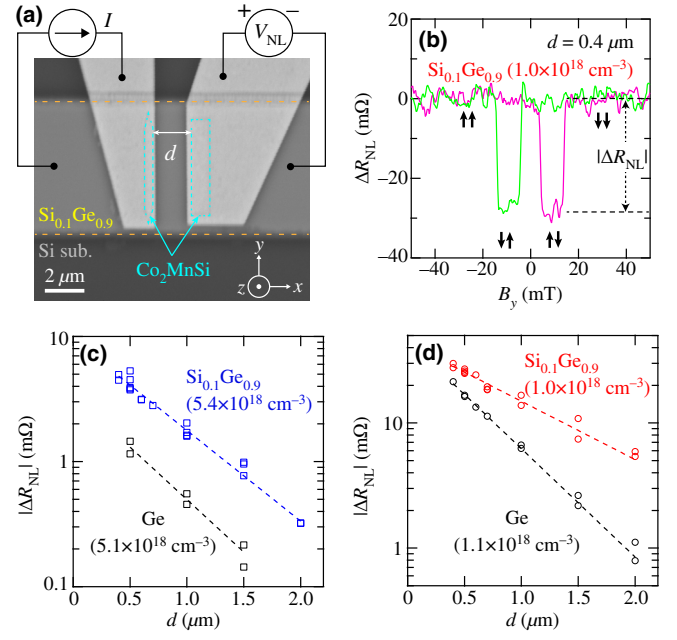


FIG. 2. (a) SEM image of a lateral spin-valve (LSV) device fabricated using the strained n -Si_{0.1}Ge_{0.9}. (b) A nonlocal spin signal for the strained Si_{0.1}Ge_{0.9} with $n \sim 1.0 \times 10^{18}$ cm⁻³ at $d = 0.4 \mu\text{m}$ and at room temperature. (c),(d) The d -dependence of $|\Delta R_{\text{NL}}|$ at room temperature for Si_{0.1}Ge_{0.9} and Ge with two different n values of approximately 5×10^{18} cm⁻³ and approximately 1×10^{18} cm⁻³, respectively. The linear fits to the data using Eq. (1) are represented as dashed lines.

semiconductor channel used here, under applied in-plane (B_y) or out-of-plane (B_z) magnetic fields.

Figure 2(b) shows a representative nonlocal spin signal [$\Delta R_{\text{NL}} = \Delta V_{\text{NL}}/I = (V_{\text{NL}}^{\downarrow} - V_{\text{NL}}^{\uparrow})/I$] as a function of B_y for a LSV device ($d = 0.4 \mu\text{m}$) with the strained n -Si_{0.1}Ge_{0.9} with $n \sim 1.0 \times 10^{18}$ cm⁻³. Hysteretic behavior depending on the magnetization switching between the parallel and antiparallel states is clearly observed at room temperature. Here, the magnitude of ΔR_{NL} , $|\Delta R_{\text{NL}}|$, can be determined as described in Fig. 2(b). To estimate λ , we plot $|\Delta R_{\text{NL}}|$ as a function of d for the strained Si_{0.1}Ge_{0.9} and Ge LSV devices with $n \sim 5 \times 10^{18}$ cm⁻³ and $n \sim 1 \times 10^{18}$ cm⁻³ in Figs. 2(c) and 2(d), respectively. For all the four-channel LSV devices, $|\Delta R_{\text{NL}}|$ is found to decay exponentially with increasing d .

In general, the reduction in $|\Delta R_{\text{NL}}|$ with increasing d can be represented by the following equation [48,53,54]:

$$|\Delta R_{\text{NL}}| = P^2 \frac{\rho \lambda}{S} \exp(-d/\lambda), \quad (1)$$

where P is the average of the spin injection and detection efficiency and S is the cross-section area of the semiconductor channels (approximately $0.49 \mu\text{m}^2$ for n -Si_{0.1}Ge_{0.9} and approximately $0.98 \mu\text{m}^2$ for n -Ge). Here, we use the estimated ρ values of 7.5 and

TABLE I. Spin-diffusion length (λ), diffusion constant (D), and spin lifetime (τ) at room temperature for various semiconductor channels.

Channel	n (cm $^{-3}$)	λ (μ m)	D (cm 2 /s)	τ (ns)
Si $_{0.1}$ Ge $_{0.9}$	1.0×10^{18}	0.93	32	0.27
Ge	1.1×10^{18}	0.50	17	0.15
Si $_{0.1}$ Ge $_{0.9}$	5.4×10^{18}	0.60	23	0.16
Ge	5.1×10^{18}	0.52	21	0.13
Si [40]	1.6×10^{19}	0.95	4.4	2.1

3.0 m Ω cm for the strained n -Si $_{0.1}$ Ge $_{0.9}$ with $n \sim 1.0 \times 10^{18}$ cm $^{-3}$ and $\sim 5.4 \times 10^{18}$ cm $^{-3}$, respectively; by contrast, for the Ge with $n \sim 1.1 \times 10^{18}$ cm $^{-3}$ and $\sim 5.1 \times 10^{18}$ cm $^{-3}$, we use the ρ values of 13.3 and 3.4 m Ω cm, respectively. As shown by the dashed curves in Figs. 2(c) and 2(d), all the decay behaviors can be fitted using Eq. (1), leading to the estimate of λ . Notably, the slope of the dashed curves for the strained n -Si $_{0.1}$ Ge $_{0.9}$ is smaller than that for the Ge in both Figs. 2(c) and 2(d).

Table I shows a summary of the estimated λ values at room temperature for all four channels, together with the value for heavily doped Si [40]. The largest λ of 0.93 μ m is obtained for the strained Si $_{0.1}$ Ge $_{0.9}$ with $n \sim 1 \times 10^{18}$ cm $^{-3}$. Notably, the λ of 0.93 μ m is large compared with that for the Ge ($n \sim 1 \times 10^{18}$ cm $^{-3}$). In addition, even for the SiGe channel with a Ge-like electronic band structure, the value of 0.93 μ m for the strained Si $_{0.1}$ Ge $_{0.9}$ is comparable to that (approximately 0.95 μ m [40]) for a Si channel with $n \sim 1.6 \times 10^{19}$ cm $^{-3}$. This means that the lattice strain enhances the electron spin-diffusion length in a semiconductor channel.

To elucidate the mechanism of the marked enhancement in λ in the strained n -Si $_{0.1}$ Ge $_{0.9}$ even at room temperature, we further estimate the spin lifetime (τ) using the relation $\lambda = \sqrt{D\tau}$, where D is the diffusion constant. In the present study, the value of D is determined from the modified Einstein's relation in Eq. (4) in Ref. [61] and the experimentally obtained n and μ in Fig. 1(c). The largest D of approximately 32 cm 2 /s is obtained for the strained n -Si $_{0.1}$ Ge $_{0.9}$ with $n \sim 1 \times 10^{18}$ cm $^{-3}$, which is already expected from its largest electron mobility in Fig. 1(c). In addition to the D , the calculated τ for all four channels are also shown in Table I. The longest τ of approximately 0.27 ns is also obtained for the strained n -Si $_{0.1}$ Ge $_{0.9}$ with $n \sim 1 \times 10^{18}$ cm $^{-3}$. This feature indicates that suppression of an electron spin relaxation becomes evident in the strained n -Si $_{0.1}$ Ge $_{0.9}$ with $n \sim 1 \times 10^{18}$ cm $^{-3}$. From the above, we conclude the enhancement in λ attributes not only to the enhancement in D but to the enhancement in τ . We also find almost no difference in τ between the Ge with $n \sim 1 \times 10^{18}$ cm $^{-3}$ and that with $n \sim 5 \times 10^{18}$ cm $^{-3}$. Thus, for Ge at room temperature, the phonon-induced

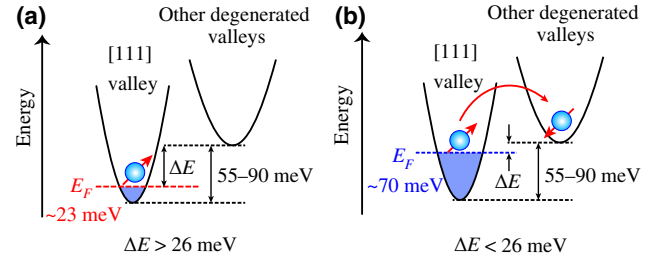


FIG. 3. Schematics of the conduction-band valleys in the strained n -Si $_{0.1}$ Ge $_{0.9}$ with (a) $n \sim 1 \times 10^{18}$ cm $^{-3}$ and (b) $n \sim 5 \times 10^{18}$ cm $^{-3}$ at room temperature.

spin relaxation is dominant rather than the donor-driven spin relaxation [43].

A simple picture of the suppression mechanism of spin relaxation is drawn in Fig. 3. Here the E_F above the conduction-band edge is roughly estimated as $\hbar^2(3\pi^2n)^{2/3}/(2m_e)$ and the valley energy splitting (55–90 meV) after applying a biaxial tensile strain in (111) to a Ge-like conduction band is also shown by referring to the literature [51,52]. Figures 3(a) and 3(b) show schematics of the correlation between the valley energy splitting and E_F for the strained n -Si $_{0.1}$ Ge $_{0.9}$ with $n \sim 1 \times 10^{18}$ cm $^{-3}$ and $n \sim 5 \times 10^{18}$ cm $^{-3}$, respectively. In both cases, we concentrate on the energy difference (ΔE) between the valley energy splitting and E_F . When the ΔE is greater than approximately 26 meV (300 K), the phonon-induced spin-flip scattering between valleys can be suppressed even at room temperature [Fig. 3(a)]. On the other hand, if the ΔE becomes less than approximately 26 meV, phonon-induced intervalley scattering can occur frequently [Fig. 3(b)]. Thus, in addition to the formation of the valley energy splitting, the position of the E_F should be considered when a semiconductor channel is designed as a room-temperature spin-transport layer.

C. Spin drift in strained Si $_{0.1}$ Ge $_{0.9}$

Until now, long-distance spin transport in semiconductors such as Si [62–65] and Ge [37] has been electrically detected by a method using a spin-drift effect. However, for Ge-like conduction-band channels, no study of the electrical detection of the long-distance spin transport at room temperature has been reported. Thus, we here explore the effect of strain on the long-distance spin transport in LSV devices at room temperature. For the strained n -Si $_{0.1}$ Ge $_{0.9}$ with $n \sim 1.0 \times 10^{18}$ cm $^{-3}$, we measure three-terminal nonlocal spin signals detected in the terminal configuration depicted in Fig. 4(a), where a negative direct current ($I_D < 0$) is applied between the spin injector and the detector contacts [64,66]. Figure 4(b) shows the room-temperature three-terminal nonlocal spin signal [$\Delta R_{3T} = \Delta V_{3T}/I_D = (V_{3T}^{\uparrow\downarrow} - V_{3T}^{\downarrow\uparrow})/I_D$] as a function of B_y at $d = 5$ μ m and at $I_D = -1$ mA. Despite the Ge-like conduction channel, a

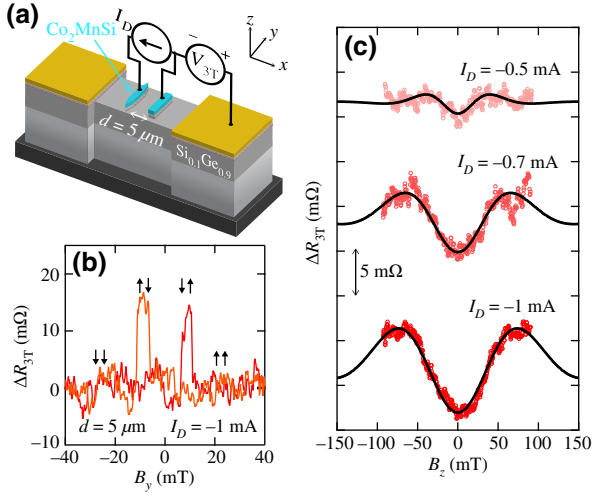


FIG. 4. (a) Schematic of a LSV device fabricated using the strained n - $\text{Si}_{0.1}\text{Ge}_{0.9}$ ($n \sim 1.0 \times 10^{18} \text{ cm}^{-3}$) for three-terminal nonlocal spin-transport measurements. (b) Three-terminal nonlocal spin signal at $d = 5 \mu\text{m}$, at $I_D = -1 \text{ mA}$, and at room temperature. (c) Hanle precession signals in a parallel magnetization state at $d = 5 \mu\text{m}$, at various I_D of -0.5 , -0.7 , and -1 mA , and at room temperature. The solid curves are curves simulated using Eq. (2).

lateral spin transport of $5 \mu\text{m}$ is electrically detected in a LSV device with the strained $\text{Si}_{0.1}\text{Ge}_{0.9}$ at room temperature unlike its optical detection [67,68]. By applying out-of-plane magnetic fields (B_z), we also observe Hanle precession signals in a parallel magnetization state at various I_D in Fig. 4(c). The solid curves are simulated curves based on the following equation [53,54]:

$$\Delta R(B_z) = \pm \frac{P^2 \rho D}{S} \int_0^\infty \frac{1}{\sqrt{4\pi Dt}} \exp\left[-\frac{(l - v_d t)^2}{4Dt}\right] \times \cos\left(\frac{g\mu_B B_z}{\hbar} t\right) \exp\left(-\frac{t}{\tau}\right) dt, \quad (2)$$

where $D = 32 \text{ cm}^2/\text{s}$ and $\tau = 0.27 \text{ ns}$ are fixed to the values in Table I, the center-to-center distance (l) between the spin injector and detector contacts is $5.45 \mu\text{m}$ (corresponding to the edge-to-edge distance d of $5 \mu\text{m}$), $\rho = 7.5 \text{ m}\Omega \text{ cm}$, and the drift velocity (v_d) is the product of the mobility $\mu = 823 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ in Fig. 1(c). The electron g factor (g) is set as a free parameter and is roughly estimated to be 1.13 – 1.47 , which falls in the range from 0.82 (electrons populate only in the lower energy L valley) to 1.56 (electrons populate equally in the four L valleys) [52]. When the values of P , which only affect the amplitude of the Hanle curves, are selected to be 1.61 , 2.38 , and 2.55% for $I_D = -0.5$, -0.7 , and -1 mA , respectively, the simulated curves show good agreement with the experimental results. Notably, the difference in the P values among various I_D originates mainly from an enhancement

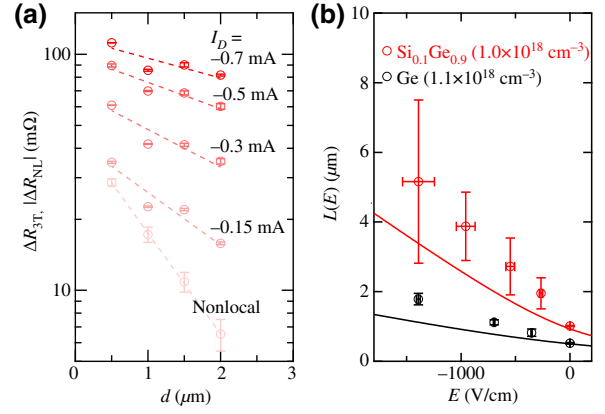


FIG. 5. (a) d dependence of ΔR_{3T} at room temperature for the strained $\text{Si}_{0.1}\text{Ge}_{0.9}$ with $n \sim 1 \times 10^{18} \text{ cm}^{-3}$, together with $|\Delta R_{NL}|$. The linear fits to the data using $\Delta R_{3T} \propto \exp(-d/L)$ are represented as dashed lines. (b) Room-temperature spin-transport length $L(E)$ as a function of E for $\text{Si}_{0.1}\text{Ge}_{0.9}$ (red) and Ge (black) with $n \sim 1 \times 10^{18} \text{ cm}^{-3}$. The solid curves are simulated data for $\text{Si}_{0.1}\text{Ge}_{0.9}$ (red) and Ge (black) with $\lambda = 0.93$ and $0.50 \mu\text{m}$, respectively.

in the spin-detection efficiency at a biased Schottky-tunnel contact [66]. These results indicate that the experimentally obtained λ , D , and τ are valid physical quantities for the strained $\text{Si}_{0.1}\text{Ge}_{0.9}$ with $n \sim 1.0 \times 10^{18} \text{ cm}^{-3}$.

To confirm the effect of electric fields (E) on the long-distance spin transport in the strained $\text{Si}_{0.1}\text{Ge}_{0.9}$, we finally examine the exponential decay behavior of ΔR_{3T} with varying d and I_D at room temperature, where the value of I_D is related to that of E in the three-terminal nonlocal measurements in Fig. 4(a). Figure 5(a) shows the d dependence of ΔR_{3T} with varying I_D in a LSV device with the strained $\text{Si}_{0.1}\text{Ge}_{0.9}$ with $n \sim 1 \times 10^{18} \text{ cm}^{-3}$ at room temperature, together with the d dependence of $|\Delta R_{NL}|$ in the same LSV device. We clearly observe that the exponential decay behavior of ΔR_{3T} becomes evidently moderate when I_D is increased. The dashed lines are linear fits to the data using $\Delta R_{3T} \propto \exp(-d/L)$ at a certain I_D , where L is the spin-transport length. Thus, we can estimate L from the fitting for various E . Figure 5(b) displays plots of L versus E for the strained $\text{Si}_{0.1}\text{Ge}_{0.9}$ (red) and the pure Ge (black) at room temperature. The estimated L for the strained $\text{Si}_{0.1}\text{Ge}_{0.9}$ is substantially enhanced with increasing $|E|$ compared to that for the pure Ge.

If E is applied to the spin-transport semiconductor channels, the value of L can become a function of E using the following equation [69–71]:

$$L(E) = \left(\frac{eE}{2\varepsilon_d} + \sqrt{\left(\frac{eE}{2\varepsilon_d}\right)^2 + \left(\frac{1}{\lambda}\right)^2} \right)^{-1}, \quad (3)$$

where e is the elementary charge and $\varepsilon_d (= eD/\mu)$ is an energy scale that controls the strength of the spin drift. Thus, we can simulate L as a function of E using Eq. (3). In the same panel of Fig. 5(b), the calculated $L(E)$ as a function of E using Eq. (3) is depicted, where $\varepsilon_d = 39$ meV and $\lambda = 0.93$ μm for the strained $\text{Si}_{0.1}\text{Ge}_{0.9}$ and $\varepsilon_d = 39$ meV and $\lambda = 0.50$ μm for the Ge are used. Although slight differences between the experimental data and the calculated ones are seen, the strain-induced enhancement in L is roughly reproduced using the enhancement in λ in Eq. (3). Thus, the long-distance spin transport in the strained n - $\text{Si}_{0.1}\text{Ge}_{0.9}$ ($n \sim 1.0 \times 10^{18}$ cm^{-3}) in Fig. 4 is attributed to the strain-induced enhancement in $L(E)$ at room temperature even in Ge-like conduction-band channels. Therefore, understanding the effect of strain on the spin transport in semiconductors is useful to the development of semiconductor spintronic applications at room temperature.

III. CONCLUSION

The effect of lattice strain on the spin transport in semiconductors is studied using four-terminal nonlocal spin-transport measurements in LSV devices with various semiconductor channels with Ge-like conduction bands. For $n = 1 \times 10^{18}$ cm^{-3} , the λ of the strained $\text{Si}_{0.1}\text{Ge}_{0.9}$ is much larger than that of the pure Ge at room temperature. A possible mechanism of the strain-induced enhancement of λ is discussed as a consequence of a sufficient energy difference between the conduction valley splitting and E_F at room temperature. Because of the enhancement in λ and L by the strain, 5- μm lateral spin transport at room temperature is electrically detected by applying an electric field to the channel in an LSV device.

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