


# Nongalvanic Calibration and Operation of a Quantum Dot Thermometer

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A cryogenic quantum dot thermometer is calibrated and operated using only a single nongalvanic gate connection. The thermometer is probed with radio-frequency reflectometry and calibrated by fitting a physical model to the phase of the reflected radio-frequency signal taken at temperatures across a small range. Thermometry of the source and drain reservoirs of the dot is then performed by fitting the calibrated physical model to new phase data. The thermometer can operate at the transition between thermally broadened and lifetime-broadened regimes and outside the temperatures used in calibration. Electron thermometry is performed at temperatures between 3.0 K and 1.0 K, in both a 1-K cryostat and a dilution refrigerator. In principle, the experimental setup enables fast electron-temperature readout with a sensitivity of  $4.0 \pm 0.3 \text{ mK}/\sqrt{\text{Hz}}$ , at kelvin temperatures. The nongalvanic calibration process gives a readout of physical parameters, such as the quantum dot lever arm. The demodulator used for reflectometry readout is readily available at relatively low cost.

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## I. INTRODUCTION

The electron temperature is a fundamental parameter that can limit the performance of low-temperature experiments and applications. Electron thermometry is an essential tool in understating the behavior of low-temperature circuitry [1], for example, the processors used in quantum computers or devices used to study exotic electronic phases and materials. Accurate and fast electron-temperature readout is also a valuable tool for thermodynamic experiments. Increasingly sensitive quantum circuits require delicate and noninvasive electronic thermometry. Quantum dot (QD) and single-electron transistor (SET) conduction thermometry are well established as powerful approaches to the monitoring of electron temperatures [1–11]. However, these thermometers require the measurement of current through the QD, which can complicate or interfere with other electronic measurements in the experiment. Furthermore, the extra galvanic connections for dot thermometry can be a source of additional noise and parasitic heating. Local charge sensing can be used to probe the occupation of a QD without having to pass a direct current through it [5,12–15]. This approach still requires galvanic connections to read the

charge sensor. Similarly, radio-frequency (rf) reflectometry techniques have recently allowed QD thermometry to be performed without measuring the current through the QD, which is effective outside the lifetime-broadened regime [16,17]. However, for both charge sensing and reflectometry, connections to the source and drain of the dot are still needed to measure the dot lever arm for calibration.

Here, we demonstrate how a completely nongalvanic QD thermometer can be calibrated and used with a single capacitive gate connection, including a calibration of the lever arm with no dc source-drain bias. The QD thermometer has the flexibility to be used on any conducting reservoir and operates in an intermediate regime where the maximum temperature is much lower than the QD charging energy  $k_B T \ll E_c$  and the minimum temperature is similar to the tunnel coupling  $k_B T \sim \hbar \Gamma$ .

## II. EXPERIMENTAL DETAILS

The QD device used in our experiment is a silicon-on-insulator trigate accumulation-mode field-effect transistor (FET) with a channel length, width, and height of 80 nm, 30 nm, and 10 nm, respectively, shown in Figs. 1(a) and 1(b). For more details of the device, see Ref. [18]. At cryogenic temperatures, when a positive subthreshold dc voltage  $V_{ig}$  is applied to the top gate, a localization potential appears in the Si channel underneath the gate. This allows bound electron states to accumulate and creates a

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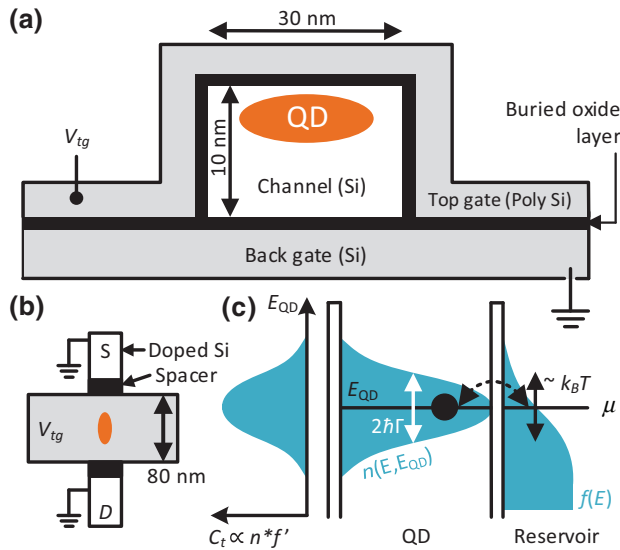


FIG. 1. Details of the silicon field-effect transistor QD. (a) A cross-section schematic of the device. The transistor consists of an undoped one-dimensional (1D) Si channel, 10 nm high by 30 nm wide, with  $n$ -doped Si source-drain connections. A polycrystalline silicon top gate, 80 nm wide, bridges over the channel, separated by a layer of SiO<sub>2</sub>. A grounded Si back gate is located beneath the channel, separated from the channel by 145-nm-thick buried SiO<sub>2</sub>. (b) The top view of the device. The source (S) and drain (D) channel connection points are  $n$  doped and behave as a single grounded reservoir during thermometry operation. Two spacers are used to prevent doping of the Si channel under the top gate. (c) The energy diagram of the system. The reservoir has an occupation of electron states given by the Fermi-Dirac distribution  $f$ . The QD energy level  $E_{\text{QD}}$  is broadened from tunnel coupling to the reservoir, with a density of states  $n$  described by Eq. (1). The tunneling capacitance  $C_t$  as a function of  $E_{\text{QD}}$  has a shape proportional to  $f' * n$ .

single QD. Corner dots [19,20] do not appear in the structure due to the narrow width of the channel. The FET top gate also acts as a plunger gate for the QD via capacitance  $C_{tg}$ , allowing tuning of the next available energy level in the QD, which is labeled  $E_{\text{QD}}$ , by adjusting  $V_{tg}$ . The source and drain electrodes are both grounded and act as a single reservoir of electrons with a Fermi level  $\mu$  [Fig. 1(c)]. Throughout the experiment, the back gate is grounded.

The QD energy level  $E_{\text{QD}}$  is broadened by tunnel coupling to the reservoir [21,22]. This is described with a broadened density of states in the form of a normalized Lorentzian, given by

$$n(E_{\text{QD}}, E) = \frac{1}{\pi} \frac{\hbar\Gamma}{(E_{\text{QD}} - E)^2 + (\hbar\Gamma)^2}, \quad (1)$$

where  $\hbar$  is the reduced Planck's constant [21]. The tunnel rate  $\Gamma$  between the QD and the reservoir is given by  $\Gamma = \Gamma_s\Gamma_d/(\Gamma_s + \Gamma_d)$ , where  $\Gamma_{s(d)}$  is the tunnel rate through the source(drain) barrier. The probability  $P_{\text{QD}}$  of an excess

electron occupying the QD is given by the integral of the product of  $n(E_{\text{QD}}, E)$  and the Fermi-Dirac distribution of electrons in the reservoir  $f(E)$  [21]:

$$P_{\text{QD}}(E_{\text{QD}}) = \int_{-\infty}^{\infty} f(E)n(E_{\text{QD}}, E)dE. \quad (2)$$

This is the convolution of the two functions, so  $P_{\text{QD}}$  becomes

$$P_{\text{QD}}(E_{\text{QD}}) = (f * n). \quad (3)$$

Due to the spin degeneracy of the extra electron in the QD,  $P_{\text{QD}} = 1/2$  occurs when  $E_{\text{QD}} = \mu \pm k_B T \ln 2$  [17,23]. The QD has both a constant geometric capacitance and a variable “tunneling” capacitance  $C_t$ , given by [17,24–28]:

$$C_t(V_{tg}) = e\alpha \frac{\partial P_{\text{QD}}}{\partial V_{tg}}, \quad (4)$$

where  $\alpha = C_{tg}/C_{\Sigma}$  is the top-gate lever arm,  $C_{\Sigma}$  is the total QD capacitance,  $\partial V_{tg} = -\partial E_{\text{QD}}/e\alpha$ , and  $e$  is the elementary charge. Insertion of Eq. (3) into this definition gives us the tunneling capacitance of the QD-reservoir system:

$$C_t(V_{tg}) = e\alpha(f * n)' = e\alpha(f' * n). \quad (5)$$

The derivative of  $f$  with respect to  $V_{tg}$ , denoted  $f'$ , is

$$f'(V_{tg}) = \frac{1}{4k_B T_e} \cosh^{-2} \left( \frac{-\alpha e(V_{tg} - V_0)}{2k_B T_e} \right), \quad (6)$$

where  $T_e$  is the electron temperature of the reservoirs,  $V_0$  is the value of  $V_{tg}$  when  $P_{\text{QD}} = 1/2$ , and  $k_B$  is the Boltzmann constant. The  $T_e$  dependence of  $C_t$  via Eq. (6) is the basis for the nongalvanic QD thermometer.

The capacitance  $C_t$  is measured by rf reflectometry using the setup shown in Fig. 2(a). A resonant circuit is connected to the QD top gate and consists of a Nb-Ti-N-on-quartz spiral inductor  $L = 96$  nH and a coupling capacitor  $C_c = 0.18$  pF. The inductor is placed parallel to the measured capacitance to help improve the loaded  $Q$  factor, which helps achieve a larger signal for a given change in capacitance [29]. The modeling of the circuit using the measured rf reflection  $|R|$  and phase  $\phi$  [shown in Fig. 2(b)] gives a resonance frequency of  $f_0 = 593.4$  MHz with a  $Q$  factor of 63, a parasitic capacitance of  $C_p = 0.57$  pF, and a parasitic resistance of  $R_p = 43.2$  k $\Omega$ . A modeled circuit loss to ground is represented by resistance  $R_p$ , which affects the resonance  $Q$  factor [29]. The parasitic capacitance  $C_p$  includes the geometric gate capacitance  $C_{tg}$ . The demodulation of  $|R|$  and  $\phi$  is performed using an “ADL5387” active-quadrature demodulator chip. To allow a dc bias  $V_{tg}$  to be applied to the top gate, a 100-pF capacitor is placed after the inductor to avoid dc shorting

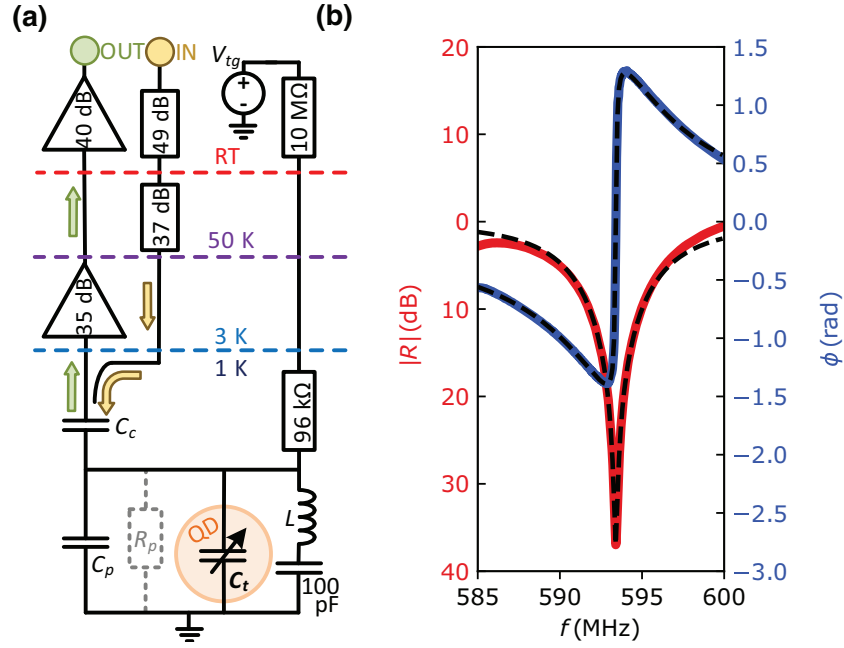


FIG. 2. Details of the rf circuitry. (a) A schematic of the circuit layout. The resonant circuit is comprised of the Nb-Ti-N-on-quartz spiral inductor  $L$ , the parasitic capacitance  $C_p$ , the coupling capacitance  $C_c$ , and the variable QD tunneling capacitance  $C_t$ , which is the physical parameter monitored for thermometry.  $C_p$  includes the geometric gate capacitance  $C_{ig}$  for modeling purposes.  $R_p$  is a modeled parasitic loss resistance to ground, which impacts the resonant circuit  $Q$  factor. The inductor line has a 100-pF capacitor to provide a dc break between the top gate and ground. The 96-k $\Omega$  resistor limits the top-gate rf signal loss to the dc line.  $V_{tg}$  is the controllable dc top-gate voltage. IN and OUT represent the rf reflectometry input and output signal, respectively. (b) The loaded measurement circuit resonance when coupled to the top gate, with the reflected-signal magnitude  $|R|$  and phase  $\phi$  shown in red and blue, respectively. The black dashed lines show the fitted circuit model, which measures a  $Q$  factor of 63. Thermometry is performed on resonance at  $f_0 = 593$  MHz, where the phase is most responsive.

of the device and to create a good rf ground at the frequency of operation. A 96-k $\Omega$  resistor is used to prevent rf signals from escaping via the dc-bias line. The resonant frequency depends on the total top-gate capacitance via  $f_0 = 1/2\pi\sqrt{L(C_c + C_p + C_t)}$ . At the circuit resonant frequency,  $\phi \propto C_t$ , when  $C_t \ll C_p + C_c$  [17,28,30–32]. This gives a change in reflected-signal phase that depends on  $T_e$  according to

$$\phi - \phi_0 = A\epsilon\alpha (f' * n), \quad (7)$$

where  $A$  is a fitting constant, which tells us the phase change due to a small change in capacitance, and  $\phi_0$  is the circuit phase offset at the resonant frequency when  $C_t \approx 0$ . If the constants  $A$ ,  $\alpha$ , and  $\Gamma$  are known, measurement of  $\phi - \phi_0$  as a function of  $V_{tg} - V_0$  and fitting of the model described by Eq. (7) gives a readout of the electron temperature  $T_e$ . A demonstration of this technique is shown in Fig. 3.

### III. RESULTS

The QD thermometer is installed in two fridge systems: a cryogen-free 1 K cryostat [33] and a cryogen-free dilution refrigerator [34], where it is calibrated and

operated. In both systems, the fridge temperature is monitored by a ruthenium oxide resistance fridge thermometer [35], mounted alongside the QD thermometer during data collection. The reading of the ruthenium oxide fridge thermometer is denoted as  $T_f$ .

In the 1-K cryostat, calibration is done by reading the reflected-signal phase across a number of sweeps of  $V_{tg}$  at a set of fridge temperatures  $T_f = 2.0, 2.5,$  and  $3.0$  K. A single least-squares fit using Eq. (7) is performed on the collective phase traces for this set of temperatures (Fig. 3). It is assumed that the electron temperature is well thermalized with the fridge temperature at 2 K and above and so during the calibration the condition  $T_e = T_f$  is applied to Eq. (7). For each phase trace,  $\phi_0$  and  $V_0$  are individually fitted. The calibration fit produces an estimate for the values  $\alpha = 0.74 \pm 0.02$ ,  $\Gamma = 270 \pm 20$  ns $^{-1}$ , and  $A = 5.13 \pm 0.06$  rad pF $^{-1}$ . This implies that  $\Gamma \gg 2\pi f_0$ ; therefore, dissipative components are neglected and the cyclic tunneling is considered adiabatic. With these three constants defined, the thermometer is calibrated and ready for operation.

To use the QD thermometer,  $\phi - \phi_0$  is measured as a function of  $V_{tg} - V_0$  and fitted with the calibrated Eq. (7) to give a readout of the electron temperature  $T_e$ . A series of

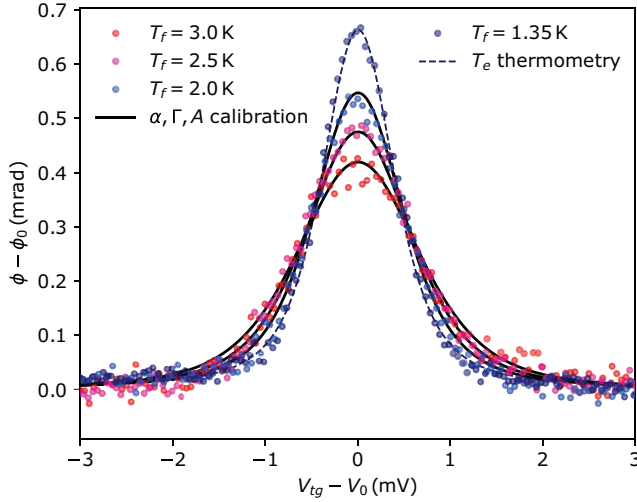


FIG. 3. The QD-thermometer calibration in 1 K cryostat, showing three experimental phase traces taken at fridge temperatures  $T_f = 2.0, 2.5,$  and  $3.0$  K. Each trace shows the change in reflected-signal phase  $\phi - \phi_0$  against the top-gate voltage  $V_{tg} - V_0$  around the Coulomb peak of the QD. The solid black lines show the least-squares fit of Eq. (7) onto the data, assuming that the electron temperature  $T_e$  equals the fridge thermometer readout  $T_f$ . This calibration procedure estimates  $\alpha = 0.74 \pm 0.02$ ,  $\Gamma = 270 \pm 20$  ns $^{-1}$ , and  $A = 5.13 \pm 0.06$  rad pF $^{-1}$ . These three constants are then included within Eq. (7), allowing electron thermometry to be performed, shown here with a  $T_e$  fit to data taken at  $T_f = 1.35$  K, yielding an electron temperature  $T_e = 1.4 \pm 0.1$  K.

thermometry readings are taken at varying fridge temperatures between 3.0 K and 1.3 K. The fridge temperature  $T_f$  is monitored for each QD thermometer reading of  $T_e$  (Fig. 4). The QD thermometer agrees with the fridge thermometer across the range of temperatures, even at temperatures below the calibration range. It is worth noting that this works in the intermediate regime where  $k_B T \sim \hbar \Gamma$  because the tunnel broadening is taken into account within the physical model. For a quicker electron-temperature readout, the QD can be tuned to where  $V_{tg} = V_0$ , so that  $\phi - \phi_0 = \phi_{MAX}$ , which is directly converted to a electron temperature via Eq. (7), using the previously calibrated values of  $\alpha, \Gamma,$  and  $A$ . This approach works effectively even at fridge temperatures below the calibration data.

Finally, to provide an independent confirmation of the calibration process, the QD source and drain connections are ungrounded to apply a source-drain voltage  $V_{SD}$  across the QD and measure a charge-stability diagram of the device. Using the relationship

$$\alpha = \frac{1}{m_d - m_s}, \quad (8)$$

where  $m_d = \Delta V_{ig}^d / \Delta V_{SD}^d$  is the gradient along the “drain resonance” side of a Coulomb diamond and  $m_s =$

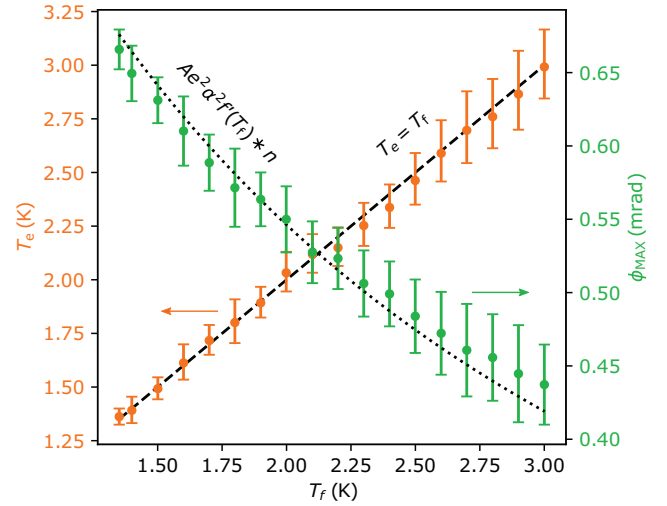


FIG. 4. The electron temperature  $T_e$  (orange) and the maximum phase  $\phi_{MAX}$  (green) readout from the QD thermometer in the 1-K cryostat. The thermometry readout is generated by fitting  $T_e$ , via the calibrated Eq. (7), to the phase curve observed by sweeping the  $V_{ig}$  over the QD Coulomb peak. The peak phase  $\phi_{MAX}$  is measured at  $V_{ig} = V_0$ , with no fitting process required. The fridge temperature  $T_f$  is read from a ruthenium oxide fridge thermometer thermally linked to the QD device. The dashed line highlights where  $T_e = T_f$ . The dotted line represents the model prediction from the calibrated Eq. (7), assuming  $T_e = T_f$ .

$\Delta V_{ig}^s / \Delta V_{SD}^s$  is the “source-resonance” side, we can see that the lever arm  $\alpha = 0.74 \pm 0.02$  matches well with the Coulomb diamond geometry in Fig. 5. This demonstrates that the lever arm of a QD can be obtained using one non-galvanic gate connection and measurements spanning a range of fridge temperatures. The order of magnitude of the source-drain current  $|I_{SD}|$  from the unblocked QD is found to be in the order of approximately 10 nA. This agrees with the calibration fit of the total tunnel-rate constant  $270 \pm 20$  ns $^{-1}$ , which equates to a source-drain current of  $e\Gamma = 6.9 \pm 0.5$  nA for a single-electron transport channel.

To study operation at lower temperatures, the QD thermometer is mounted into a dilution refrigerator. The calibration fit is performed as described above, with phase data taken at 1300 mK and 1600 mK. Within this system, the three calibration constants are found to be  $\alpha = 0.84 \pm 0.03$ ,  $\Gamma = 510 \pm 10$  ns $^{-1}$ , and  $A = 0.75 \pm 0.07$  rad pF $^{-1}$ . The change in  $A$  is attributed to the differences between the two fridge systems affecting the rf electronics, such as the parasitic capacitance  $C_p$  changing due to a different metallic geometry near the QD chip. Both  $\alpha$  and  $\Gamma$  are sensitive to the shape and position of the QD in the Si channel, which are likely to be different after a thermal cycle of the device.

The electron-temperature readout  $T_e$  from the QD thermometer in the dilution refrigerator agrees with the fridge

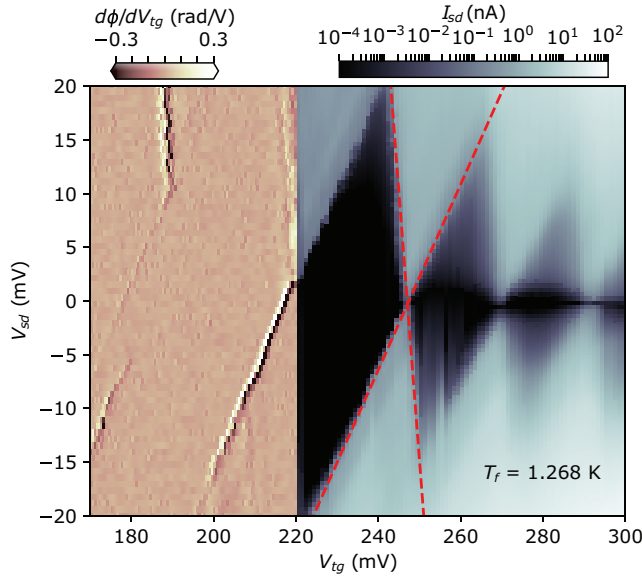


FIG. 5. The charge-stability diagram measured by reflectometry technique and dc transport in the 1-K cryostat. To the left of  $V_{tg} = 220$  mV, the derivative of the reflected-signal phase with respect to the top-gate voltage  $d\phi/dV_{tg}$  is plotted, demonstrating the nongalvanic reflectometry technique. For this measurement, the source and drain connections are grounded. To the right of  $V_{tg} = 220$  mV, the QD source-drain current  $|I_{SD}|$  is plotted on a log scale. The fridge temperature is  $T_f = 1.268 \pm 0.001$  K. The red lines highlight the source-drain gradients that match the calibration fit lever-arm prediction,  $\alpha = 0.74 \pm 0.02$ , via Eq. (8), crossing at the QD Coulomb peak where the thermometry takes place. Here, the on-resonance current has an order of magnitude  $I_{SD} \approx 1 - 10$  nA, which is similar to that of the single-electron current defined by the calibration fit tunnel rate  $e\Gamma \sim 6.9 \pm 0.5$  nA.

temperature readout  $T_f$  above 1 K, despite the fact that  $k_B T_f < \hbar\Gamma$  [Fig. 6(a)]. Below 1 K there is a deviation of  $T_e$  away from  $T_f$ , although the point  $T_e = T_f$  remains within one standard deviation of the experimental uncertainty (for details on how the error is determined, see Sec. II in the Supplemental Material [36]). The deviation of the  $T_e$  readout and its increase in uncertainty occur because the QD energy level is strongly tunnel broadened and the response of the phase trace to the temperature becomes weaker (for details on the influence of tunnel broadening, see Sec. III in the Supplemental Material [36]). With a reduced  $\Gamma$ , the QD thermometer would work at lower temperatures. This can be achieved by adjusting the device design geometry. Checking the charge-stability diagram, the predicted lever arm  $\alpha = 0.84 \pm 0.03$  matches the Coulomb diamond geometry well [Fig. 6(b)]. The dilution refrigerator experiment has an average white-noise phase sensitivity of  $1.1 \pm 0.1 \mu\text{rad}/\sqrt{\text{Hz}}$ , in this case dominated by the measurement chain. With this equipment and the  $\phi_{\text{MAX}}$  measurements demonstrated in Fig. 4, the QD thermometer setup could achieve a potential sensitivity

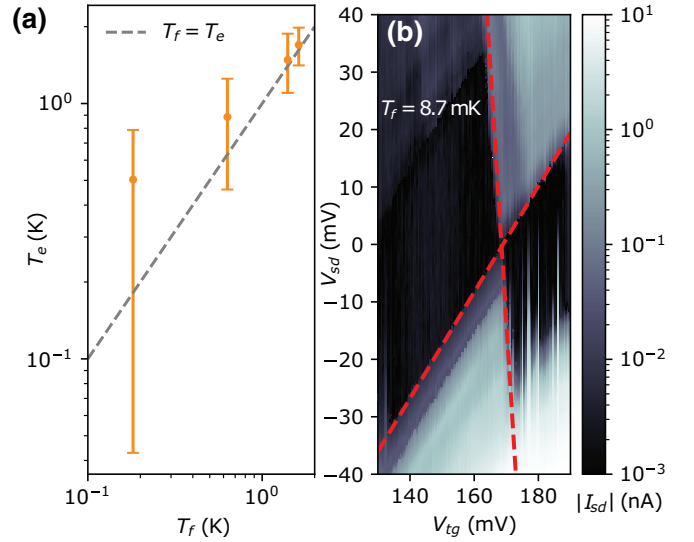


FIG. 6. QD The thermometry and stability diagram from the dilution-refrigerator measurements. (a) The recalibrated QD thermometer readout of the electron temperature  $T_e$  compared with the fridge-thermometer readout  $T_f$ , measured near the device within the dilution refrigerator. The QD and fridge thermometers start to disagree below 1 K but still agree to within one standard deviation of confidence [37] (for details on how the error is determined, see Sec. II in the Supplemental Material [36]). (b) The source-drain current-stability diagram of the QD mounted in the dilution refrigerator. The temperature of the fridge is held at  $T_f = 8.70 \pm 0.05$  mK. The red lines highlight the source-drain gradients that match the calibration fit lever-arm prediction,  $\alpha = 0.84 \pm 0.03$ , via Eq. (8), crossing at the QD Coulomb peak where the thermometry takes place.

of  $11 \pm 1 \text{ mK}/\sqrt{\text{Hz}}$  at 3.0 K, and  $4.0 \pm 0.3 \text{ mK}/\sqrt{\text{Hz}}$  at 1.3 K (for details on the thermometer sensitivity, see Sec. I in the Supplemental Material [36]).

#### IV. CONCLUSIONS

We describe the successful calibration and operation of a QD-thermometer readout via a single capacitive connection in two separate cryostats, which provides a simple and versatile way to measure the electron temperature. The calibration uses limited data to generate a physical model of the QD-reservoir system, which correctly estimates physical parameters such as the QD lever arm. The electron thermometry is successfully performed with the calibrated QD thermometer in a temperature range of 1.0–3.0 K. The QD thermometer is also used for faster readout by monitoring the phase when the QD has an occupation probability of 1/2. In this mode of operation, the noise floor of the measurement should allow for a sensitivity of  $4.0 \pm 0.3 \text{ mK}/\sqrt{\text{Hz}}$  and  $11 \pm 1 \text{ mK}/\sqrt{\text{Hz}}$ , at 1.3 K and 3.0 K, respectively. This process works with the same QD chip in both cryostats. The QD thermometer can operate even

in the case in which  $k_B T_e < \hbar\Gamma$ ; however, with the system and techniques used here, the thermometry uncertainty starts to increase below 1 K, due to strong tunnel coupling overriding the temperature dependence. Careful analysis of the thermometry uncertainty reveals the coldest limit of the QD thermometer, when the electron-temperature readout confidence boundary increases beyond a usable size. A redesign of the QD device to reduce the tunnel rate would be needed to decrease the uncertainty at lower temperatures. The ability to fully calibrate and operate a nongalvanic electron thermometer with a single rf line simplifies the application of the device substantially. This device provides a versatile, sensitive, and effective tool for monitoring the electron temperature in nanoelectronic devices at cryogenic temperatures.

The data that support the findings of this study are available online [38], including descriptions of the data sets.

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