Impedance-matched Marx generators

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We have conceived a new class of prime-power sources for pulsed-power accelerators: impedancematched Marx generators (IMGs). The fundamental building block of an IMG is a brick, which consists of two capacitors connected electrically in series with a single switch. An IMG comprises a single stage or several stages distributed axially and connected in series. Each stage is powered by a single brick or several bricks distributed azimuthally within the stage and connected in parallel. The stages of a multistage IMG drive an impedance-matched coaxial transmission line with a conical center conductor. When the stages are triggered sequentially to launch a coherent traveling wave along the coaxial line, the IMG achieves electromagnetic-power amplification by triggered emission of radiation. Hence a multistage IMG is a pulsed-power analogue of a laser. To illustrate the IMG approach to prime power, we have developed conceptual designs of two ten-stage IMGs with LC time constants on the order of 100 ns. One design includes 20 bricks per stage, and delivers a peak electrical power of 1.05 TW to a matched-impedance 1.22- Ω load. The design generates 113 kV per stage and has a maximum energy efficiency of 89%. The other design includes a single brick per stage, delivers 68 GW to a matched-impedance 19- Ω load, generates 113 kV per stage, and has a maximum energy efficiency of 90%. For a given electrical-poweroutput time history, an IMG is less expensive and slightly more efficient than a linear transformer driver, since an IMG does not use ferromagnetic cores.

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I. INTRODUCTION

A linear-transformer-driver (LTD) module is an induction voltage adder that is powered by circuits located within the cavities of the adder [1-20]. Since an LTD module can be designed to provide single-stage electrical-pulse compression and impedance matching, such modules are attractive candidates as prime-power sources for nextgeneration pulsed-power accelerators [1–21].

Figures 1 and 2 are idealized representations of a tencavity LTD module. Figure 3 is an idealized circuit model of such a module. As suggested by Figs. 1-3, an LTD module consists of a single LTD cavity or several cavities distributed axially and connected electrically in series. Each cavity is a right-circular annular enclosure that contains a single LTD brick, or several bricks distributed azimuthally within the cavity and connected electrically in parallel. Each brick comprises a single switch and two capacitors connected in series.

The circuit model presented by Fig. 3 makes the following simplifying assumptions: each brick within the LTD module is identical to every other brick, each LTD cavity includes the same number of bricks, all the bricks within a cavity are triggered simultaneously, and each brick can be modeled as an RLC circuit. Under these conditions the quantities R_c , L_c , and C_c of Fig. 3 are as follows:

$$R_c = \frac{R_b}{n_b},\tag{1}$$

$$L_c = \frac{L_b}{n_b},\tag{2}$$

$$C_c = n_b C_b. \tag{3}$$

The quantity R_c is the effective series resistance of a single cavity, R_b is the effective series resistance of a single brick, n_b is the number of bricks within each cavity, L_c is the effective series inductance of a cavity, L_b is the effective series inductance of a brick, C_c is the series capacitance of a cavity, and C_b is the series capacitance of a brick.

As indicated by Figs. 1-3, each LTD cavity includes ferromagnetic cores. These introduce an effective parallel resistance within a cavity [7,13,15-20]. For each cavity, Fig. 3 models the cores as a resistive circuit element with resistance R_{cores} located at the cavity output [7,13,15–20].

As indicated by Figs. 1 and 2, the walls of a cavitywhich are conductors-connect the downstream electrode

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FIG. 1. Cross-sectional view of a ten-cavity linear-transformer-driver (LTD) module. The module is powered by 200 bricks altogether; only 20 are illustrated here.

of the cavity output gap to the upstream electrode. After the bricks of a cavity are triggered, parasitic current flows along the inner surfaces of the cavity walls as illustrated by the red arrows of Fig. 2. The arrows represent a parasitic load that is in parallel with the cavity output gap.

The current that follows the path illustrated by the red arrows of Fig. 2 creates a magnetic field within the cavity that diffuses into the ferromagnetic cores and changes their magnetization state. Hence energy is lost to the cores through magnetic-energy diffusion and Ohmic heating. Since the cores absorb energy from the parasitic current, the cores introduce an effective resistance (i.e., R_{cores})

in the path of this current, which reduces the magnitude of the current.

In a well-designed cavity, the cores increase substantially the effective resistance of the path indicated by the red arrows of Fig. 2. For such a cavity, the dominant contribution to the resistance of this path is that due to the cores, R_{cores} . The cores of such a cavity reduce substantially the parasitic current, and cause most of the current generated by the bricks within the cavity to be delivered to the cavity output gap. Current delivered to the gap is used to drive the centrally located transmission line of the LTD module [1–20].



FIG. 2. Cross-sectional view of two LTD cavities. The red arrows indicate the path of parasitic current that flows along the inner surfaces of the walls of each cavity. This current creates a magnetic field within each cavity that diffuses into the ferromagnetic cores and changes their magnetization state. Hence energy is lost to the cores through magnetic-energy diffusion and Ohmic heating. Since the cores absorb energy from the parasitic current, the cores introduce an effective resistance in the path of this current, which reduces its magnitude.

However, the cores also increase the cost and weight of an LTD module. In addition, the magnetization state of the cores within a module must be reset before each module shot to maximize the available volt-second product of the cores at the beginning of the shot. Hence the cores require a core-reset pulse generator that presents an additional cost.

Furthermore, the cores and associated reset circuitry increase cavity-design and operational complexities. Moreover, even the most advanced cores do not create an effectively infinite resistance, and allow a small fraction of the current generated by the bricks within a cavity to be lost by flowing parasitically along the cavity's inner surfaces. An additional issue may be associated with building a *petawatt-class* LTD-powered accelerator: such a machine is likely to require at least ~12,000 cores [6,18–20], which may be challenging to fabricate in a timely manner.

Motivated by these observations, we have conceived a new class of prime-power sources that do not use ferromagnetic cores: impedance-matched Marx generators (IMGs) [18,20,22–27]. An IMG comprises a single stage or several stages distributed axially and connected electrically in series. Each stage is powered by a single brick or several bricks distributed azimuthally within the stage and connected electrically in parallel. The stages of a multistage IMG drive an impedance-matched coaxial transmission line with a conical center conductor.

The bricks that power an IMG can be designed to generate an electrical-power pulse with the temporal width required to drive experiments of interest. Hence an IMG can provide single-stage electrical-pulse compression, which eliminates the need for additional stages of pulse-compression hardware. Such hardware can increase design, simulation, engineering, fabrication, and operational complexities, and reduce the energy efficiency of an accelerator. In addition, an IMG is–by definition–impedance matched to a load. Impedance matching maximizes the peak electrical power that is delivered by an IMG to its load, and enables an IMG to achieve a maximum energy efficiency of ~90%. Single-stage pulse compression and impedance matching establish IMGs as attractive prime-power sources for pulsed-power accelerators.

An LTD module can also be designed to provide single-stage pulse compression and impedance matching [1-20]. However, for a given electrical-power-output time history, an IMG is less expensive and slightly more efficient than an LTD, since an IMG does not use ferromagnetic cores.

To illustrate the IMG approach to prime power, we have developed conceptual designs of two ten-stage IMGs with *LC* time constants on the order of 100 ns. One design assumes use of 100-kV 80-nF capacitors, and delivers a peak electrical power of 1.05 TW to a matched-impedance 1.22- Ω load. The design is outlined by Sec. II. An idealized circuit model of the design is presented by Sec. IV. The other design assumes 100-kV 160-nF capacitors, and delivers 68 GW to a matched-impedance 19- Ω load. This configuration is outlined by Sec. V; an associated circuit model and circuit-simulation results are presented by Secs. VI and VII, respectively.

We caution that Secs. II-VII describe only conceptual IMG designs. The present article does not describe engineering designs. Describing such designs would require developing engineering designs for the capacitor-charge circuits, capacitor-charge cables, capacitor-charge resistors, switch-trigger pulse generators, switch-trigger cables, switch-trigger circuits, postpulse energy diverters, plastic insulators that provide mechanical support for the IMG's components, plastic insulators that contain the IMG's liquid insulators, oil-handling-and-processing systems, waterhanding-and-processing systems, various diagnostics, etc. Engineering designs have been developed for previous Marx generators, such as the 20-MJ 6-MV Marx-generator system that powers the 85-TW 26-MA Z accelerator at Sandia National Laboratories. However, developing such designs for the two IMG concepts outlined herein would be outside the scope of the present article.



FIG. 3. (a) Circuit model of a ten-cavity LTD module. The module drives a ten-segment impedance-matched transmission line that in turn drives an impedance-matched load. The quantity R_c is the effective series resistance of a single LTD cavity, L_c is the effective series cavity inductance, C_c is the series cavity capacitance, R_{cores} is the effective parallel resistance within a single cavity due to its ferromagnetic cores, Z is the impedance of the first transmission-line segment, Z_{load} is the load impedance, τ is the one-way electromagnetic transit time of a single segment, R_b is the effective series resistance of a single brick, n_b is the number of bricks per cavity, L_b is the effective series brick inductance, and C_b is the series brick capacitance. The quantity Z_c is the effective impedance of a single cavity is triggered at time τ after the cavity immediately upstream is triggered, the circuit of (a) can be simplified as that given by (b). The quantity R_{LTD} is the effective series resistance of the ten-cavity LTD module, the effective series for the module due to its cores. The quantity Z_{LTD} is the effective impedance of the ten-cavity LTD module; the expression given by the figure for Z_{LTD} is valid when $R_{shunt} \gg [1.1(L_{LTD}/C_{LTD})^{1/2} + 0.8R_{LTD}]$ [7]. When circuit (b) is applicable and the values of Z and Z_{load} are as given by the figure, the LTD module is impedance matched to the load; i.e., the peak electrical power delivered by the module to the load is maximized.

As observed by Sec. VIII, the simplest form of an IMG is a single brick that drives an impedance-matched load. Such IMGs have already been developed and serve as the prime-power source of the new Thor accelerator [18,20, 22–25,27]. (The first Thor machine has been designed and is being assembled at Sandia.) As suggested by Sec. VIII, IMGs could also be developed to drive a wide variety of other pulsed-power accelerators, such as the Neptune machine [18,20,23,26,27] and possibly Z 300 and Z 800 [18–20].

The Appendix demonstrates analytically that all the reflected and backward voltages within a generalized multistage IMG cancel. Numbered equations in this article are in SI units throughout.

II. CONCEPTUAL DESIGN OF A 1.05-TW IMG

A conceptual design of a 1.05-TW impedance-matched Marx generator with an *LC* time constant of 80 ns is illustrated by Fig. 4. The IMG consists of ten stages that are distributed axially and connected electrically in series. Each stage is powered by 20 bricks distributed azimuthally within the stage and connected electrically in parallel. Hence the IMG is powered by 200 bricks altogether. (Only 20 of the 200 bricks are illustrated by Fig. 4.)

Each of the 200 bricks is identical to every other brick. Each brick includes two 100-kV 80-nF capacitors connected electrically in series with a single (normally open) 200-kV field-distortion gas switch [28]. Before an IMG shot, the capacitors are dc charged in a balanced manner, so that +100 kV appears across one of the brick's capacitors, -100 kV across the other, and 200 kV across the brick's switch [29].

The region within which the bricks are located is filled with oil. After the IMG's 400 capacitors are charged to high voltage (and before the switches are triggered), a potential difference of as much as 200 kV appears between any two exposed conductors within the oil-insulated region of the IMG.

To minimize the probability of dielectric failure within the IMG's oil section while the capacitors are charged to high voltage, we tentatively propose that the oil-section design satisfy everywhere the following relation:



FIG. 4. Cross-sectional view of a ten-stage impedance-matched Marx generator (IMG) with 20 bricks per stage. The IMG is powered by 200 bricks altogether; only 20 are illustrated here.

$$E_{\rm oil\,(dc)} \le 2 \times 10^6 \text{ V/m},\tag{4}$$

where

$$E_{\rm oil\,(dc)} \equiv \frac{V_{\rm oil\,(dc)}}{g_{\rm oil\,(dc)}}.$$
 (5)

The quantity $E_{\rm oil\,(dc)}$ is the average dc electric field between two exposed conductors within the oil section, $V_{\rm oil\,(dc)}$ is the dc potential difference between the conductors, and $g_{\rm oil\,(dc)}$ is the distance between the conductors. Equation (4), which was determined empirically, assumes that the minimum radius of the exposed electrodes within the IMG oil section is a few millimeters, and the insulating liquid within the section is Shell Diala transformer oil [30] or an equivalent insulator. Equation (4) also assumes the oil is dried, filtered, and degassed by a Parker Sentinel oil-purification system [31] or an equivalent configuration. When the maximum dc potential difference between two conductors is 200 kV, Eq. (4) requires that the gap between such conductors be at least 10 cm. As indicated above, we propose Eq. (4) be used for IMG design purposes only on a tentative basis, and suggest

additional work be conducted to develop definitive IMG-oil-section design criteria for dc voltages.

As indicated by Fig. 4, the ten IMG stages drive an impedance-matched coaxial transmission line located within the IMG. (Impedance matching is achieved as described by Sec. III.) The coaxial line includes a conical center conductor and is water insulated.

While the capacitors are dc charged and before the switches are triggered, no voltage appears across the IMG's coaxial transmission line. After the switches are triggered, a high-voltage pulse propagates along the coaxial line. To minimize the probability of dielectric failure within the coax, its design must satisfy everywhere the following relation [32,33]:

$$E_w \tau_w^{0.33} \le 1.13 \times 10^5,$$
 (6)

where

$$E_w \equiv \frac{V_w}{g_w}.$$
(7)

At a given location within the coaxial line the quantity E_w is the peak value in time of the average electric field between the line's two conductors, V_w is the voltage between the conductors, g_w is the gap between the conductors, and τ_w is the temporal width of the voltage pulse at 63% of its peak value [32,33]. (Like the other numbered equations in this article, Eq. (6) is in SI units. Different units are used by Refs. [32,33].)

After the IMG switches are triggered, a high-voltage pulse appears not only within the water-insulated coaxial transmission line of the IMG but also the IMG oil section. A potential difference on the order of the peak voltage of the IMG output pulse appears between conductors at the upstream end of the IMG and the IMG's outer enclosure, which is at ground potential.

According to Refs. [34–36], for pulse lengths of interest (i.e., ~100 ns to ~1 μ s), the dielectric strength of oil is a factor of ~2 greater than that of water. Hence to minimize the probability of dielectric failure within the IMG oil section after the switches are triggered, we tentatively propose that the design of this section satisfy everywhere the same relation used to design the water-insulated coax:

$$E_{\rm oil} \tau_{\rm oil}^{0.33} \le 1.13 \times 10^5,$$
 (8)

where

$$E_{\rm oil} \equiv \frac{V_{\rm oil}}{g_{\rm oil}}.$$
 (9)

At a given location within the oil section the quantity $E_{\rm oil}$ is the peak value in time of the average electric field between two exposed conductors, $V_{\rm oil}$ is the voltage between the conductors, $g_{\rm oil}$ is the gap between the conductors, and $\tau_{\rm oil}$ is the temporal width of the voltage pulse at 63% of its peak value [32,33].

Since Eq. (8) was developed for water, using it to design an oil-insulated region should be conservative [34–36]. As indicated above, we propose that Eq. (8) be used for the oil-insulated section of an IMG on a tentative basis, and suggest additional work be conducted to develop definitive IMG-oil-section design criteria for pulsed voltages.

III. CIRCUIT MODEL OF THE 1.05-TW IMG

We have developed an idealized circuit model of the 1.05-TW impedance-matched Marx generator outlined by the previous section. The model was developed using the SCREAMER circuit code [37–39] and is illustrated by Fig. 5. The circuit elements of the model are discussed in this section.

The circuit model makes the following simplifying assumptions: each of the 200 bricks that drive the IMG are identical to every other brick; each of the IMG's ten stages includes 20 bricks; all the bricks within a stage are triggered simultaneously; and each brick can be modeled as an *RLC* circuit. Under these conditions the quantities R_s , L_s , and C_s of Fig. 5 are as follows:

$$R_s = \frac{R_b}{n_b},\tag{10}$$

$$n_b = 20, \tag{11}$$

$$L_s = \frac{L_b}{n_b},\tag{12}$$

$$C_s = n_b C_b. \tag{13}$$

The quantity R_s is the effective series resistance of a single IMG stage, R_b is the effective series resistance of a single brick, n_b is the number of bricks within each stage, L_s is the effective series inductance of a stage, L_b is the effective series inductance of a brick, C_s is the series capacitance of a stage, and C_b is the series capacitance of a brick.

We make the following additional assumptions:

$$R_b = 0.3 \ \Omega, \tag{14}$$

$$L_b = 160 \text{ nH.}$$
 (15)

The brick resistance and inductance, R_b and L_b respectively, are functions of the spatially dependent electron, ion, and neutral-particle temperatures and densities of the current-carrying plasma channels within the brick's switch. Hence the resistance and inductance must be time dependent. Over the time interval of interest, the performance of a brick can be approximated with reasonable accuracy by using effective constant values for these quantities. Equations (14) and (15) give the assumed effective values.



FIG. 5. (a) Circuit model of a ten-stage IMG. The IMG drives a ten-segment impedance-matched transmission line that in turn drives an impedance-matched load. The quantity R_s is the effective series resistance of a single IMG stage, L_s is the effective series stage inductance, C_s is the series stage capacitance, Z is the impedance of the first transmission-line segment, Z_{load} is the load impedance, τ is the one-way electromagnetic transit time of a single segment, R_b is the effective series resistance of a single brick, n_b is the number of bricks per stage, L_b is the effective series brick inductance, C_b is the series brick capacitance, and Z_s is the effective impedance of a single stage. (b) When each stage is triggered at time τ after the stage immediately upstream is triggered, the circuit of (a) can be simplified as that given by (b). The quantity R_{IMG} is the effective series resistance of the IMG, L_{IMG} is the effective series IMG inductance, C_{IMG} is the series IMG capacitance, and Z_{IMG} is the effective impedance of the ten-stage IMG. When circuit (b) is applicable and the values of Z and Z_{load} are as given by the figure, the IMG is impedance matched to the load; i.e., the peak electrical power delivered by the IMG to the load is maximized.

We also assume each brick consists of a single switch that is connected electrically in series with two 80-nF capacitors:

$$C_b = 40 \text{ nF.}$$
 (16)

Figure 5(a) models the coaxial line that is driven by the IMG as ten transmission-line segments. This figure assumes that the impedance of the *n*th segment is nZ, where Z is the impedance of the first segment. The transmission line drives a load with impedance Z_{load} . We make the simplifying assumptions that the transmission-line segments are lossless, the impedance of each section is nonreactive and constant throughout its length, and the load impedance is purely resistive (i.e., nonreactive).

The quantity τ is the one-way electromagnetic transit time of a single transmission-line segment. As suggested by Figs. 4 and 5, the minimum value of τ is determined by the geometric distance that is required between bricks. Of course, it is desired to minimize τ to minimize the total length of the IMG (and hence, the size of the accelerator). Each IMG stage could be command triggered, as is done for an LTD module. (It may be possible to achieve requisite performance of an IMG by command triggering the first few stages of the IMG, then allowing the remaining stages to self-trigger, as is done for a conventional Marx generator. It is clear such an approach would be more likely to succeed when the number of bricks per stage is small.) When each stage is triggered at time τ after the stage immediately upstream is triggered, the circuit of Fig. 5(a) can be simplified as Fig. 5(b).

When the circuit model of Fig. 5(b) is applicable, the peak value of the electrical power delivered by the IMG to a load with impedance Z_{load} is maximized when Z and Z_{load} satisfy the following relations [6,7]:

$$Z = Z_s = 1.1 \sqrt{\frac{L_s}{C_s}} + 0.8R_s,$$
 (17)

$$Z_{\text{load}} = n_s Z_s = Z_{\text{IMG}} = 1.1 \sqrt{\frac{L_{\text{IMG}}}{C_{\text{IMG}}}} + 0.8 R_{\text{IMG}},$$
 (18)

$$n_s = 10, \tag{19}$$

$$L_{\rm IMG} = n_s L_s, \tag{20}$$

$$C_{\rm IMG} = \frac{C_s}{n_s},\tag{21}$$

$$R_{\rm IMG} = n_s R_s. \tag{22}$$

The quantity Z_s is the effective impedance of a single IMG stage, n_s is the number of IMG stages, Z_{IMG} is the effective impedance of the multistage IMG, L_{IMG} is the effective series inductance of the IMG, C_{IMG} is the series IMG capacitance, and R_{IMG} is the effective series IMG resistance.

When the circuit model of Fig. 5(b) is applicable and Eqs. (17)–(22) are satisfied, the IMG is impedance matched to its load; i.e., the peak electrical power delivered by the IMG to the load is maximized. (We do not address here the pulse-shaping technique of Ref. [7].) The expressions given for Z_s and Z_{IMG} by Eqs. (17) and (18) achieve a peak power at the load that is within 1% of its maximum possible value. It is clear from the above equations that

$$Z_{\text{load}} = Z_{\text{IMG}} = 1.22 \ \Omega. \tag{23}$$

The initial dc charge voltage across an IMG, V_{IMG} , is as follows:

$$V_{\rm IMG} = n_s V_s, \tag{24}$$

$$V_s = V_b, \tag{25}$$

$$V_b = 200 \text{ kV},$$
 (26)

where V_s is the initial dc voltage across a single IMG stage, and V_b is the initial dc voltage across a single brick.

The total energy initially stored by an IMG, E_{IMG} , is one measure of its size:

$$E_{\rm IMG} = \frac{1}{2} C_{\rm IMG} V_{\rm IMG}^2 = \frac{1}{2} n_{\rm IMG} C_b V_b^2, \qquad (27)$$

$$n_{\rm IMG} = n_b n_s, \tag{28}$$

where n_{IMG} is the total number of bricks that power the IMG. According to the above equations, the initial energy stored by the 1.05-TW impedance-matched Marx is 160 kJ.

We define the energy efficiency of a coupled IMG-load system to be the ratio of the energy delivered by the IMG to its load, to the total energy initially stored by the IMG capacitors. Since the energy delivered to a resistive load increases with time during an IMG power pulse, the energy efficiency also increases with time. When the circuit model given by Fig. 5(b) is applicable, the theoretical maximum energy efficiency of an IMG is readily calculated:

$$\eta_{\rm max} = \frac{Z_{\rm load}}{Z_{\rm load} + R_{\rm IMG}}.$$
 (29)

This is the efficiency that is achieved when the only parasitic resistance within an IMG is R_{IMG} [which is the case for the idealized circuit model of Fig. 5(b)], and the

energy delivered to the load has reached its maximum value. For the IMG outlined by this section and Sec. II,

$$\eta_{\rm max} = 89\%.$$
 (30)

IV. RESULTS OF CIRCUIT SIMULATIONS OF THE 1.05-TW IMG

Results of simulations conducted with the circuit model outlined by Sec. III are summarized in this section. As suggested by Fig. 5, the simulations assume the ten-stage IMG outlined by Secs. II and III drives a ten-segment impedance-matched transmission line; i.e., a transmission line with a spatial impedance profile that is matched to that of the IMG. The simulations assume also that the transmission line in turn drives a matched-impedance load. As discussed by the previous section, such impedance matching maximizes the peak electrical power delivered by the IMG to its load.

Assuming the circuit parameters given by Sec. III, the simulated time histories of the voltage and electrical power at the outputs of the ten transmission-line segments are as plotted by Figs. 6 and 7, respectively.

Before any of the switches of an IMG are triggered, all the bricks within the IMG are dc charged to high voltage. Since each brick is an *RLC* circuit, and the bricks within each IMG stage are connected electrically in parallel, we can consider each stage—after it is charged and before it is triggered—to be an oscillator in an excited state.

As indicated by Figs. 4 and 5, the stages of an IMG are connected electrically in series. Each stage is triggered by triggering simultaneously all the switches within the stage. When the stages are triggered sequentially as discussed by



FIG. 6. Simulated voltage time histories at the outputs of the ten transmission-line segments of the ten-stage IMG illustrated by Fig. 4, as given by the circuit model of Fig. 5(a). Since $10Z = Z_{load}$, the voltage at the output of the tenth stage is identical to that at the load.



FIG. 7. Simulated electrical-power time histories at the outputs of the ten transmission-line segments of the ten-stage IMG illustrated by Fig. 4, as given by the circuit model of Fig. 5(a). Since $10Z = Z_{load}$, the power at the output of the tenth stage is identical to that at the load.

Sec. III, a coherent traveling wave propagates downstream the impedance-matched transmission line of the IMG. The propagation is illustrated by Figs. 6 and 7. The voltage and electromagnetic power of the wave increase with distance along the length of the transmission line, as indicated by these figures.

As indicated by Figs. 6 and 7, the peak voltage and power at the output of an IMG's transmission line are n_sV_s and n_sP_s , respectively, where V_s and P_s are the peak voltage and power at the output of the IMG's first transmission-line segment. Hence an IMG achieves power amplification by triggered emission of radiation, and can be considered as a pulsed-power analogue of a laser, with a gain of n_s . (Since an LTD module operates in a similar manner, such a module can also be considered as a pulsedpower analogue of a laser.)

It is clear that transmitted, reflected, and backward voltages are launched within an IMG's transmission line (when the IMG includes more than a single stage). As demonstrated by the Appendix, the reflected and backward voltages cancel, which immensely simplifies analytic models of an IMG. The cancellation of reflected and backward voltages makes it possible to model an entire IMG as Fig. 5(b); i.e., a single simple *RLC* circuit. (As suggested by Fig. 3 and the Appendix, these observations also apply to an LTD module.)

Since the reflected and backward voltages cancel, the electrical-power time history delivered by an IMG to a load with impedance Z_{IMG} is simply a factor of n_s greater than the power that would be delivered by one of the IMG's stages to a load with impedance Z_s . (A single stage would be impedance matched to a load with impedance Z_s ; i.e., the peak electrical power delivered by a stage to a load would be maximized when the impedance of the load



FIG. 8. Simulated electrical-power time histories generated by one of the stages of the IMG illustrated by Fig. 4, and the tenstage IMG. The power generated by a single stage is that which would be delivered by the stage to a matched-impedance load; i.e., a load with impedance Z_s . The power generated by the tenstage IMG is that which would be delivered by the IMG to a matched-impedance load; i.e., a load with impedance Z_{IMG} .

is Z_s [6,7].) For example, as indicated by Fig. 8, the peak power delivered by the ten-stage IMG outlined by Secs. II and III to a matched-impedance $1.22-\Omega$ load is 1.05 TW. Hence the peak power that would be delivered by one of the IMG's stages to a matched-impedance $0.122-\Omega$ load is 105 GW.

Similarly, when Fig. 5(b) is applicable, the electricalpower time history delivered by an IMG to a load with impedance Z_{IMG} is simply a factor of n_{IMG} greater than the power that would be delivered by one of the IMG's bricks to a load with impedance Z_b , where

$$Z_b = 1.1 \sqrt{\frac{L_b}{C_b}} + 0.8R_b.$$
(31)

(A single brick would be impedance matched to a load with impedance Z_b ; i.e., the peak electrical power delivered by a single brick to a load would be maximized when the impedance of the load is Z_b [6,7].) For example, as indicated by Fig. 9, the peak power delivered by the 200-brick IMG outlined by Secs. II and III to a matched-impedance 1.22- Ω load is 1.05 TW. Hence the peak power that would be delivered by one of the IMG's bricks to a matched-impedance 2.44- Ω load is 5.25 GW.

More generally, when Fig. 5(b) is applicable, the time histories of the voltage, current, power, and energy that are delivered by an IMG to a load with impedance Z_{IMG} are factors of n_s , 1, n_s , and n_s , respectively, greater than the time histories that would be delivered by one of the IMG's stages to a load with impedance Z_s . Similarly, the time histories of the voltage, current, power, and energy



FIG. 9. Simulated electrical-power time histories generated by one of the bricks of the IMG illustrated by Fig. 4, and the 200brick IMG. The power generated by a single brick is that which would be delivered by the brick to a matched-impedance load; i.e., a load with impedance Z_b . The power generated by the 200brick IMG is that which would be delivered by the IMG to a matched-impedance load; i.e., a load with impedance Z_{IMG} .

delivered by an IMG to a load with impedance Z_{IMG} are factors of n_s , n_b , n_{IMG} , and n_{IMG} , respectively, greater than the time histories that would be delivered by one of the IMG's bricks to a load with impedance Z_b .

When $R_{\rm IMG} \ll \sqrt{L_{\rm IMG}/C_{\rm IMG}}$ and Fig. 5(b) is applicable, we obtain the following additional results:

$$V_p = 0.57 n_s V_b, \tag{32}$$

$$\tau_{\text{voltage}} = 2.2\sqrt{L_b C_b},\tag{33}$$

$$\tau_{63\%} = 1.8\sqrt{L_b C_b},\tag{34}$$

$$I_p = \frac{V_p}{Z_{\text{load}}},\tag{35}$$

$$\tau_{\rm current} = \tau_{\rm voltage},$$
 (36)

$$P_p = 0.33 \frac{(n_s V_b)^2}{Z_{\text{load}}},$$
(37)

$$\tau_{\rm power} = 1.6\sqrt{L_b C_b}.$$
(38)

The quantity V_p is the peak value of the voltage pulse that is delivered by an IMG to an impedance-matched load, $\tau_{voltage}$ is the FWHM of the voltage pulse, $\tau_{63\%}$ is the temporal width of the voltage pulse at 63% of its peak value, I_p is the peak current delivered to the load, $\tau_{current}$ is the FWHM of the current pulse, P_p is the peak power delivered to the load, and τ_{power} is the FWHM of the power pulse. Equations (32)–(38) are accurate to within 2%.



FIG. 10. Simulated electrical-power time histories generated by the ten-stage IMG illustrated by Fig. 4. The red trace assumes the IMG's impedance-matched transmission line has a stepped impedance profile, as suggested by Fig. 5(a). The blue trace assumes a piecewise-linear impedance profile, as suggested by the conical center conductor illustrated by Fig. 4.

The LC time constant of a single IMG brick can be designed to achieve a pulse with the temporal width required to drive physics experiments of interest. (It is clear that the LC time constant of a single brick is the same as that of a single IMG stage powered by such bricks, and that of an entire IMG powered by such bricks.) Hence an IMG can provide single-stage electrical pulse compression, which eliminates the need for additional stages of pulse-compression hardware.

As indicated by Fig. 6, the peak voltage delivered by the IMG outlined by Secs. II and III to a matched-impedance load is 1.13 MV. (The peak voltage generated by each of the ten stages is 113 kV.) The temporal width of the load voltage at 63% of its peak value is 149 ns. Hence according to Eqs. (6) and (8), the peak value of the average electric field between two conductors within the IMG's water-insulated coaxial transmission line and oil section must not exceed 202 kV/cm.

Figure 5 assumes that the impedance-matched transmission line of a multistage IMG is stepped and consists of constant-impedance segments. However, the center conductor of the transmission line illustrated by Fig. 4 is conical, which provides a smoother impedance profile. Circuit simulations indicate that assuming a piecewiselinear impedance profile instead of one that is stepped has less than a 1% effect on the output-power time history of an IMG, as demonstrated by Fig. 10.

V. CONCEPTUAL DESIGN OF A 68-GW IMG

A conceptual design of a 68-GW IMG with an *LC* time constant of 126 ns is illustrated by Fig. 11. The IMG



FIG. 11. Cross-sectional view of a ten-stage impedance-matched Marx generator (IMG) with a single brick per stage.

consists of ten stages that are distributed axially and connected electrically in series. Each stage is powered by a single brick; hence the IMG is powered by ten bricks altogether.

Each of the ten bricks is identical to each of the other bricks. We assume each brick consists of two 100-kV 160-nF capacitors connected electrically in series with a single (normally open) 200-kV field-distortion gas switch [28]. Before an IMG shot, the capacitors are dc charged in a balanced manner, so that +100 kV appears across one of the brick's capacitors, -100 kV across the other, and 200 kV across the brick's switch [29].

The region within which the bricks are located is filled with and insulated by oil. As indicated by Fig. 11, the ten stages drive an impedance-matched coaxial transmission line. (Impedance matching is achieved as discussed by Sec. III.) The coaxial line includes a conical center conductor. We assume the coaxial line of the 68-GW IMG is oil insulated. (We assume water insulation for the coaxial line of the 1.05-TW IMG described by Secs. II–IV since that coax has a substantially lower impedance than that of the 68-GW IMG.)

To minimize the probability of dielectric failure within the IMG's oil section and oil-insulated coaxial transmission line, we tentatively propose that the designs of these components satisfy Eqs. (4) and (8).

VI. CIRCUIT MODEL OF THE 68-GW IMG

We have developed an idealized circuit model of the 68-GW IMG outlined by Sec. V. The model was developed using the SCREAMER circuit code [37–39] and is represented by Fig. 5.

Figure 5 also represents the 1.05-TW IMG described by Secs. II–IV. Since the 1.05-TW and 68-GW IMG circuit models are similar, we include in this section only equations that differ from those given by Secs. II–IV. For the 68-GW IMG we assume the following:

$$n_b = 1, \tag{39}$$

$$R_b = 0.2 \ \Omega, \tag{40}$$

$$L_b = 200 \text{ nH},$$
 (41)

$$C_b = 80 \text{ nF.}$$
 (42)

According to Eq. (18) the effective impedance of the IMG is 19 Ω :

$$Z_{\rm IMG} = Z_{\rm load} = 19 \ \Omega. \tag{43}$$

According to Eq. (27) the initial energy stored by the IMG is 16 kJ. According to Eq. (29),



FIG. 12. Simulated voltage time histories at the outputs of the ten transmission-line segments of the ten-stage IMG illustrated by Fig. 11, as given by the circuit model of Fig. 5(a). Since $10Z = Z_{load}$, the voltage at the output of the tenth stage is identical to that at the load.

$$\eta_{\rm max} = 90\%.$$
 (44)

VII. RESULTS OF CIRCUIT SIMULATIONS OF THE 68-GW IMG

Results of simulations conducted with the circuit model outlined by Sec. VI are summarized in this section. Since the circuit models presented by Secs. III and VI are similar, we include in this section only results that differ from those presented in Sec. IV.

Assuming the circuit parameters given by Sec. VI, the simulated time histories of the voltage and power at the



FIG. 13. Simulated electrical-power time histories at the outputs of the ten transmission-line segments of the ten-stage IMG illustrated by Fig. 11, as given by the circuit model of Fig. 5(a). Since $10Z = Z_{load}$, the power at the output of the tenth stage is identical to that at the load.

outputs of the IMG's ten transmission-line segments are as plotted by Figs. 12 and 13, respectively.

As indicated by Fig. 12, the peak voltage delivered to a matched-impedance load is 1.13 MV. (The peak voltage generated by each of the ten stages is 113 kV.) The temporal width of the load voltage at 63% of its peak value is 235 ns. Hence according to Eq. (8), the peak value of the average electric field between two conductors within the IMG's oil section and oil-insulated coaxial transmission line must not exceed 174 kV/cm.

As indicated by Fig. 13, the peak electrical power delivered by the IMG to a matched-impedance load is 68 GW. Hence the peak power that would be delivered by each of the IMG's ten bricks to a matched-impedance load is 6.8 GW.

VIII. DISCUSSION AND SUGGESTIONS FOR FUTURE WORK

It is clear that the simplest form of an IMG is a single brick that drives an impedance-matched load. Such IMGs have already been developed, and serve as the prime-power source of the new Thor accelerator [18,20,22–25,27]. Presently, each Thor brick drives four polyethyleneinsulated 11- Ω coaxial transmission lines connected electrically in parallel. The impedance of the set of four parallel lines is approximately matched to that of a single brick. Sandia National Laboratories is presently building a prototype Thor machine that could eventually be driven by as many as 288 bricks [18,20,22–25,27].

IMGs could also be developed to serve as prime power sources for a wide variety of other pulsed-power accelerators, such as the Neptune machine [18,20,23,26,27] and possibly Z 300 and Z 800 [18–20].

The conceptual design of the Neptune accelerator outlined by Refs. [20,26,27] is powered by 600 IMGs. Each IMG consists of eight stages, each of which is driven by a single brick [20,26,27]. The ten-stage IMG described by Secs. V–VII stores twice as much energy as the IMG described by Refs. [20,26,27], and could also be used to power a future Neptune-class accelerator.

The conceptual design of the Z-300 machine outlined by Refs. [18–20] is powered by ninety 33-cavity LTD modules. Each cavity is in turn powered by 20 bricks, each of which has the parameters summarized by Eqs. (10)–(16). We presently assume each Z-300 cavity would contain four ferromagnetic cores; hence Z 300 would use 11,880 cores altogether. In principle, Z 300 could instead be powered by a large number of IMGs, each stage of which would be driven by one or more bricks. Since IMGs do not use ferromagnetic cores, the IMGs would be slightly more efficient than the LTD modules. In addition, eliminating the cores would eliminate potential core-supply-chain issues. However, using IMGs would likely require that the diameter of Z 300 be larger than that estimated by Refs. [18–20]. Similarly, the Z-800 accelerator [18–20] could be driven by IMGs instead of LTDs.

The two conceptual IMG designs outlined by this article are intended to serve as a starting point for the development of optimized final designs for next-generation accelerators.

We recommend that an IMG-development effort determine-for a given accelerator-the optimum brick design, number of bricks per stage, number of stages, and IMG design. In addition, we recommend such an effort include the following activities: (1) development of a detailed transmission-line-circuit model of the optimized IMG; (2) development of a 3D electromagnetic model of the IMG; (3) continued development of advanced capacitors, capacitor-charge cables, capacitor-charge resistors, switches, switch-trigger pulse generators, switch-trigger cables, switch-trigger circuits, impedance-matched coaxial transmission lines, liquid insulators, and postpulse energy diverters; (4) design, fabrication, assembly, and commissioning of a full-scale prototype IMG; and (5) demonstration that the prototype IMG meets all safety, electrical, mechanical, diagnostic, reliability, operational, and cost requirements.

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APPENDIX: DEMONSTRATION THAT THE REFLECTED AND BACKWARD VOLTAGES WITHIN AN IMG CANCEL

In this Appendix we demonstrate analytically that the reflected and backward voltages within the coaxial transmission line of an IMG cancel. To assist with this demonstration we consider Fig. 14(a), which models an



FIG. 14. (a) Circuit model of a generalized three-stage impedance-matched Marx generator (IMG). The quantity R_s is the effective series resistance of a single IMG stage; L_s is the effective series stage inductance; C_s is the series stage capacitance; Z is the impedance of each of the vertical transmission-line segments driven by the IMG, as well as that of the first horizontal segment; τ_s is the one-way electromagnetic transit time of a vertical segment; τ is the one-way electromagnetic transit time of a horizontal segment; and V is the time-dependent forward-going voltage that is launched by each stage. (b) When each stage is triggered at time τ after the stage immediately upstream is triggered, the circuit of (a) can be simplified as that given by (b). The quantity $R_{\rm IMG}$ is the effective series resistance of the IMG, $L_{\rm IMG}$ is the effective series IMG inductance, $C_{\rm IMG}$ is the series IMG capacitance, and Z_{load} is the load impedance. The figure makes clear that all the reflected and backward voltages within a generalized multistage IMG cancel.

IMG that is more general than that modeled by Fig. 5. Figure 14(a) outlines a three-stage generalized IMG; the results presented in this section are valid for a generalized IMG with an arbitrary number of stages.

The transmission-line segments of Fig. 14(a) that are illustrated horizontally represent the IMG's coaxial transmission line. We assume each horizontal transmission-line segment is lossless and has a constant nonreactive impedance. As suggested by Fig. 14(a), the impedance of the *n*th horizontal segment is nZ. The one-way transit time of each segment is τ .

Each transmission-line segment of Fig. 14(a) that is illustrated vertically connects an IMG stage to the coaxial transmission line. Figure 14(a) represents each IMG stage as a single *RLC* circuit. We assume each vertical transmission-line segment is lossless and has a constant nonreactive impedance. The impedance of each of these segments is *Z*; the one-way transit time of each of these segments is τ_s . We assume the *RLC* drive circuit of each stage is identical to that of every other stage, and each stage is triggered at time τ after the stage immediately upstream is triggered. The quantity *V* of Fig. 14 is the time-dependent forward-going voltage that is launched by each stage in its associated vertical transmission-line segment.

Under the conditions given above, the well-known analytic voltage transmission and reflection coefficients for an ideal lossless transmission line connected to an ideal nonreactive load can be used to calculate the transmitted, reflected, and backward voltages within the horizontal and vertical transmission-line segments of a generalized IMG.

We first consider the horizontal segments. It is straightforward to demonstrate that the transmitted voltage in the *n*th horizontal segment of a multistage IMG is the sum of the transmitted voltages generated by stages 1 through n, and that the sum of these voltages is nV:

transmitted voltage in the *n*th horizontal segment
$$= nV$$
.

(A1)

When the transmitted voltage in the *n*th horizontal segment interacts with the beginning of the (n + 1)th horizontal segment, a reflected voltage is generated in the *n*th horizontal segment:

reflected voltage in the *n*th horizontal segment

$$=\frac{2nV}{(2n+2)}.$$
(A2)

The forward-going voltage generated by the *RLC* circuit of stage n + 1 propagates downward in the (n + 1)th vertical transmission-line segment. When this voltage interacts with the beginning of the (n + 1)th horizontal segment, it generates a backward voltage in the *n*th horizontal segment: backward voltage in the *n*th horizontal segment

$$=\frac{-2nV}{(2n+2)}.$$
(A3)

As indicated by Eqs. (A2) and (A3), the reflected and backward voltages in the *n*th horizontal segment cancel. All that is left in the *n*th horizontal segment is the transmitted voltage nV.

Let us now consider the vertical transmission-line segments of Fig. 14(a). As mentioned above, the time-dependent forward-going voltage in each vertical segment is V. When the forward-going voltage generated by the *RLC* circuit of the (n + 1)th stage interacts with the beginning of the (n + 1)th horizontal segment, a reflected voltage is generated in the (n + 1)th vertical segment with the following amplitude:

reflected voltage in the (n + 1)th vertical segment

$$=\frac{2nV}{(2n+2)}.$$
(A4)

When the transmitted voltage nV in the *n*th horizontal segment interacts with the beginning of the (n + 1)th horizontal segment, a backward voltage is generated in the (n + 1)th vertical segment with the following amplitude:

backward voltage in the (n + 1)th vertical segment

$$=\frac{-2nV}{(2n+2)}.$$
(A5)

As indicated by Eqs. (A4) and (A5), the reflected and backward voltages in the (n + 1)th vertical transmissionline segment cancel. All that is left in the (n + 1)th vertical segment is the forward-going voltage V.

As indicated by Eqs. (A2)–(A5) and Fig. 14(a), all the reflected and backward voltages in the horizontal and vertical segments of a generalized multistage IMG cancel.

One can demonstrate that all the reflected and backward voltages cancel without using Eqs. (A2)–(A5), as follows. The discussion above makes clear that the total time-dependent forward-going electrical power launched by stages 1 through *n* in the first *n* vertical transmission-line segments of a generalized IMG is nV^2/Z . According to Eq. (A1), the time-dependent transmitted electrical power in the *n*th horizontal transmission-line segment is $(nV)^2/nZ = nV^2/Z$. Since these two power time histories are identical, energy conservation requires that all the reflected and backward voltages within a generalized multistage IMG must cancel.

Because all the reflected and backward voltages cancel, the voltage delivered to the load is independent of both τ and τ_s . Hence the voltage delivered to a load by a generalized multistage IMG is identical to that delivered by an IMG with $\tau_s = 0$; i.e., for the type of IMG illustrated by Fig. 5(a). Furthermore, since the load voltage is independent of τ and τ_s , it is clear that the circuit of Fig. 14(a) delivers the same time-dependent voltage to the load as does the circuit of Fig. 14(b), which assumes both τ and τ_s are equal to 0.

The results of this Appendix are valid for arbitrary values of Z and Z_{load} as long as $Z_{\text{load}} = n_s Z$. The peak electrical power delivered by a generalized IMG to its load is maximized when $Z_{\text{load}} = [1.1(L_{\text{IMG}}/C_{\text{IMG}})^{1/2} + 0.8R_{\text{IMG}}]$; however, the results of this Appendix do not require that Z_{load} be given by this expression.

We observe that the results of this Appendix are also valid for an LTD module that meets the conditions given above. In addition, the results are consistent with the discussion in Sec. III B 1 of Ref. [40].

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