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Simple proof of fault tolerance in the graph-state model

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We consider the problem of fault tolerance in the graph-state model of quantum computation. Using the notion of composable simulations, we provide a simple proof for the existence of an accuracy threshold for graph-state computation by invoking the threshold theorem derived for quantum circuit computation. Lower bounds for the threshold in the graph-state model are then obtained from known bounds in the circuit model under the same noise process.

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I. INTRODUCTION

Alternative models for quantum computation based on projective measurements [1–3] have recently attracted much attention. A common concept of these models is the simulation of individual quantum circuit operations and how simulations can be composed together [4]. More specifically, in these models a sequence of single- or two-qubit measurements is applied to a collection of fixed initial quantum states thereby in effect simulating unitary transformations on a smaller subspace of states.

Although our results are applicable to a much larger class of measurement-based models, our analysis will focus on a variation of Raussendorf and Briegel's one-way quantum computer model [1] where computation is performed by single-qubit projective measurements on some initial *graph state* [5]. Henceforth, we will refer to this model as the *graph-state model* and the computation realized in it as a *graph-state simulation*.

The graph-state model offers a decomposition of a quantum algorithm in terms of alternative elementary primitives, as well as potential advantages in certain physical implementations. For example, suppose entangling gates can only be realized nondeterministically with flagged faults as, e.g., in optical quantum computation [6]. Then, graph-state simulation offers much advantage since entangling gates are only used for the preparation of the graph state, which can be done independently from the main computation [7].

In any physical realization of quantum computation, unknown errors will always be present and they will have to be corrected using quantum error-correcting codes in a fault-tolerant manner. An important question is therefore under what conditions computation can be executed reliably in the graph-state model in the presence of physical noise. In the circuit model, fault-tolerant methods [8] are available for the reliable execution of any desired computation, if the noise is sufficiently weak. Now, if such a fault-tolerant circuit is simulated in the graph-state model with sufficiently weak noise, will the same desired computation be reliably executed? More specifically, this is answered by first analyzing noisy simulations of individual operations and then how the noisy simulations compose together.

The first results on this problem were reported in the Ph.D. thesis of Raussendorf [9]. This work proved the exis-

tence of an accuracy threshold for cluster-state computation for various independent stochastic error models. More recently, Nielsen and Dawson [10] obtained proofs for the existence of an accuracy threshold in the graph-state model that apply to more general error models (including errors due to nondeterministic gates) by reduction to a threshold theorem for local non-Markovian noise [11]. In addition, they established a conceptual framework and two technical theorems that are of independent interest.

In this paper, we use the concept of composable simulations [4] and the threshold theorem derived in the circuit model [11–15] to obtain a simple proof for the existence of an accuracy threshold in the graph-state model. Furthermore, for any specific form of noise, our proof allows known lower bounds on the threshold in the circuit model to be translated to equivalent bounds in the graph-state model. We discuss in particular how the two lower bounds are related for commonly used fault-tolerant architectures based on self-dual CSS codes.

II. REVIEW OF THE GRAPH-STATE MODEL

We begin by briefly introducing the graph-state model in the ideal noiseless case. Various interpretations of this model have been reported and reviewed recently [4,16–20]. We follow the language in Ref. [4], which explicitly uses the notion of composable simulations that forms the core of our subsequent analysis. Our discussion in this section is intended to introduce the basic notions and terminology that we will use later in our proof.

In the circuit model, an arbitrary quantum computation can be decomposed into state preparation, measurements, and a universal set of gates. To show the universality of the graph-state model of quantum computation, it suffices to show that (i) each element for universality in the standard model can be simulated and (ii) the simulation can be composed to simulate the entire computation. The approach is to first define an appropriate notion of simulation that is composable, followed by a complete recipe to composably simulate each element needed for universality.

We first describe the notion of composable simulations. Let $\mathcal F$ be an operation (a superoperator, or completely positive trace-preserving map) to be simulated and $\mathcal S_{\mathcal F}$ be the

associated operation that simulates \mathcal{F} . For simplicity, let \mathcal{F} act on n qubits. In the general case, \mathcal{F} can have quantum and classical input and output of arbitrary dimensions, but this only requires extra notations and therefore will not be written out explicitly here. For a 2n-bit string x, let \mathcal{P}_x be the superoperator corresponding to conjugation by the Pauli operator indexed by x. Our composable simulation $\mathcal{S}_{\mathcal{F}}$ takes two inputs, a classical 2n-bit string $e_{\rm in}$ and an n-qubit quantum state $\mathcal{P}_{e_{\rm in}}(\rho_{\rm in})$, so that $\forall \rho_{\rm in}, \ \forall e_{\rm in}$, it acts as

$$S_{\mathcal{F}}(e_{\mathrm{in}} \otimes \mathcal{P}_{e_{\mathrm{in}}}(\rho_{\mathrm{in}})) = \sum_{e_{\mathrm{out}}} p_{e_{\mathrm{out}}} e_{\mathrm{out}} \otimes (\mathcal{P}_{e_{\mathrm{out}}} \circ \mathcal{F})(\rho_{\mathrm{in}}), \quad (1)$$

where e_{out} is some new 2n-bit string that appears with probability $p_{e_{\text{out}}}$. (Throughout the paper, the symbol for a bit string such as e_{in} also labels the corresponding density matrix.) To rephrase the above definition, for each specific classical output e_{out} , $\mathcal{S}_{\mathcal{F}}$ evolves the arbitrary state ρ_{in} according to the intended operation \mathcal{F} up to a new known succeeding Pauli operation $\mathcal{P}_{e_{\text{out}}}$, despite the $\mathcal{P}_{e_{\text{in}}}$ occurring to ρ_{in} prior to the simulation.

Note that $e_{\rm out}$ is a function of $e_{\rm in}$ and the measurement outcomes obtained in $\mathcal{S}_{\mathcal{F}}$, and this function depends on $\mathcal{S}_{\mathcal{F}}$. However, the statistics of $e_{\rm out}$ has no consequence, because composable simulations work for *all* measurement outcomes and for all $e_{\rm in}$ —all outcome histories lead to valid simulations, where an "outcome history" denotes the set of all measurement outcomes collected in a specific run of the simulation. As we will see next, this is important as it will allow us to compose simulations of individual operations to obtain a simulation of the combined operation.

Now, consider simulating a sequence of l operations $\{\mathcal{F}_j\}$, and we will see that it can be done by composing the sequence of simulations $\{\mathcal{S}_{\mathcal{F}_j}\}$. By repeated applications of Eq. (1), $\forall \rho_{\text{in}}, \forall e_{\text{in}}$,

$$S_{\mathcal{F}_{l}} \circ \cdots \circ S_{\mathcal{F}_{1}}(e_{\text{in}} \otimes \mathcal{P}_{e_{\text{in}}}(\rho_{\text{in}}))$$

$$= \sum_{e_{\text{out}}} e_{\text{out}} e_{\text{out}} \otimes (\mathcal{P}_{e_{\text{out}}} \circ \mathcal{F}_{l} \circ \cdots \circ \mathcal{F}_{l})(\rho_{\text{in}}), \quad (2)$$

which states that, for all outcome histories, the entire sequence of operations $\{\mathcal{F}_j\}$ is simulated properly, up to a final overall $\mathcal{P}_{e_{\text{out}}}$ (which just redefines the final classical outcome of the computation).

We will now describe how composable simulations are realized in the graph-state model. Let Γ denote a graph with vertex set $V(\Gamma)$ and edge set $E(\Gamma)$. One way to specify and to create the graph state corresponding to Γ is to start with the initial state $\otimes_{i \in V(\Gamma)} |+\rangle$ and then apply a controlled-phase (CPHASE) gate to each pair of qubits in $E(\Gamma)$ [where CPHASE| $ab\rangle = (-1)^{ab}|ab\rangle$ in the computation basis]. In other words, each vertex corresponds to a qubit initially in the state $|+\rangle$ and each edge corresponds to a subsequent CPHASE.

As precursor to a graph-state simulation, our next step is to composably simulate a universal set of circuit elements (state preparation, measurements, and a universal set of gates), using single-qubit measurements and CPHASE. In the circuit model, it suffices to prepare any Pauli eigenvector and measure along any Pauli basis. Both of these can be trivially

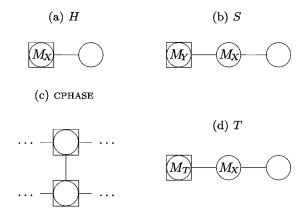


FIG. 1. Composable simulations for (a) the Hadamard gate (H), (b) the rotation around the z axis by $\pi/2$ (S), (c) the CPHASE, and (d) the rotation around the z axis by $\pi/4$ (T). Note that we use the CPHASE to simulate itself, since it can be built in as a vertical edge of the graph. We have omitted the input classical registers and their updates for simplicity. The symbols M_X and M_Y indicate measurements of σ_X or σ_Y on the corresponding qubits, and M_T indicates a measurement of the observable $(\sigma_X \pm \sigma_Y)/\sqrt{2}$ depending on whether there is a σ_X correction in the input qubit.

simulated in the graph-state model using single-qubit measurements. For the universal set of gates, we choose the Clifford group generators $\{H, S \equiv e^{-i\sigma_z \pi/4}, \text{ CPHASE}\}\$ and the additional non-Clifford $T \equiv e^{-i\sigma_z \pi/8}$. Here $\{\sigma_x, \sigma_z\}$ denote the standard Pauli operators. Figure 1 shows how to composably simulate these gates, with the classical registers omitted for simplicity. In Fig. 1, qubits are represented as circles. The boxed circles contain the quantum inputs, unboxed ones are prepared in $|+\rangle$, and open circles (unmeasured qubits) contain the quantum outputs. Edges denote CPHASE gates acting on the adjoined qubits. The measurement bases for each qubit are given in the circle. The quantum state at the input of each pattern has known Pauli corrections labeled by the classical register e_{in} (not shown), which depends on past measurement outcomes. In the simulation of T, e_{in} is used to control one of the quantum measurements. The output quantum state also has Pauli corrections labeled by an updated string e_{out} . Each simulation pattern defines an update rule, mapping e_{in} and measurement outcomes obtained in the pattern to e_{out} .

Any circuit (sequence of gates and measurements on standard initial states) can then be simulated by composing a sequence of simulations by identifying the quantum output of one simulation (the open circle) with the input to the next (the boxed circles) and similarly for the classical registers. The combined simulation thus consists of single-qubit measurements on qubits prepared in a graph state (with the CPHASE being part of the graph-state preparation), giving a complete recipe for the entire graph-state simulation. Note that evolutions of single qubits and their interactions (CPHASE) in the simulated circuit are represented in the graph as linear paths and the links between them, respectively. As an example, Fig. 2 shows how a composition of the measurement patterns for the simulation of H and CPHASE leads to a new pattern for the simulation of the operation CNOT=(I $\otimes H$)CPHASE $(I \otimes H)$.

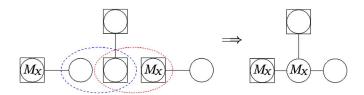


FIG. 2. (Color online) A schematic diagram of the composition of the patterns in Fig. 1(a), Fig. 1(c), and Fig. 1(a) that simulates controlled NOT $(CNOT)=(I\otimes H)CPHASE(I\otimes H)$. In the dashed ellipse on the left, the output of the measurement pattern simulating the first H is identified with the input for the lower qubit of the CPHASE simulation, whose output for the same qubit is identified with the input qubit of the simulation of the second H (right ellipse). On the right is the result of the composition.

III. NOISY GRAPH-STATE COMPUTATION

We now investigate how noise at the level of the graphstate simulation maps to noise in the simulated operations and, most importantly, whether such "simulated noise" can be tolerated by simulating a fault-tolerant circuit described in the circuit model. We begin by mentioning a modification to the graph-state model that is necessary for fault tolerance. Since the ability to prepare fresh qubits and interact them with the existing ones is essential for all fault-tolerant constructions [21], instead of creating the entire graph state before computation, a minimal modification to the graph-state model is to build the required graph state dynamically as the computation proceeds [1,7,9,10]. The simulated circuit defines a partial time ordering of the simulations and the measurements used therein, inducing a partial ordering of the qubits in the graph state. The qubits can be added slightly before their preceding neighbors are measured, as long as the CPHASE gates are applied according to the time ordering of the simulations. This change in the model still preserves the appealing feature of the graph-state model in that all unitary interactions are applied prior to and independent of the measurements that realize the computation.

Coming to the main part of this paper, we must analyze how physical noise affects the elementwise simulations and how the noisy simulations compose together. The elementary steps in the simulation are the preparation of $|+\rangle$, the CPHASE, the single-qubit measurements, and the storage of qubits. Moreover, each operation belongs to a unique simulation. Thus, noise afflicting a given operation only acts within one simulation. In particular, an erroneous CPHASE cannot affect two successive simulations.

In any noise model and without loss of generality, each noisy state preparation or noisy gate can be expressed as the ideal operation followed by a *noise operation*. Hence, noise operations intersperse pairs of successive ideal operations. A noise operation is a system-environment coupling, and it can always be described by some unitary joint evolution

$$U_{\text{fault}} = I \otimes A_0 + \sum_i P_i \otimes A_i, \tag{3}$$

where P_i ranges over all nontrivial Pauli operators indexed by i acting on the output system of the preceding ideal operation and each A_i is an arbitrary operator acting on the

environment, subject to the condition that $U_{\rm fault}$ is unitary. A noisy measurement is modeled as the ideal measurement *preceded* by a noise operation given by Eq. (3), with P_i acting on the qubits to be measured.

We first consider independent stochastic noise processes. In this case, each noise operation is by assumption acting on a separate environmental register, which is mapped to orthogonal states by the two terms in Eq. (3). Physically, this assumption corresponds to the requirement that a record be kept in the environment whenever faults occur, which can in principle be read to indicate the location of faults. In more detail, the two terms result in perfectly distinguishable environmental states, so that the corresponding states in the system do not interfere with one another, and their normalization can be interpreted as the probabilities of the first or second term in Eq. (3) occurring. These two terms thus correspond to the two events of not having or having a fault. We call the second term in Eq. (3) the fault operator or simply the fault. A fault path for the entire computation is an event occurring with some definite probability describing whether each noise operation results in a fault or not.

Our first goal is to show that faults within one simulation only affect that simulated operation, even though classical registers that carry the Pauli corrections and control the simulation are shared by many simulations. Consider a sequence of simulations $\{\mathcal{S}_{\mathcal{F}_j}\}$ applied to an input $\Sigma_{e_{\mathrm{in}}}p_{e_{\mathrm{in}}}e_{\mathrm{in}}$ $\otimes \mathcal{P}_{e_{\mathrm{in}}}(\rho_{\mathrm{in}})$. Suppose some number of faults occur within $\mathcal{S}_{\mathcal{F}_1}$. Each term in the expansion of Eq. (3) of all these fault operators consists of Pauli operators acting on the simulation qubits which can be commuted to the end of the simulation since, as shown in Fig. 1, each simulation is realized by a sequence of unitary CPHASE(s) and single-qubit measurements]. This results in a combined fault operator, each term in the Pauli expansion of which contains some Pauli operator acting on either the output classical registers of $\mathcal{S}_{\mathcal{F}_1}$ or its quantum output, or both. The most general erroneous output is thus given by $\sum_{e_{\text{out}}} p_{e_{\text{out}}}^{(1)} e_{\text{out}} \otimes \rho_{\text{out}}$ for some distribution $\{p_{e_{\text{out}}}^{(1)}\}$, where e_{out} is some possibly erroneous classical output and $\rho_{\text{out}} = \mathcal{E}_{e_{\text{out}}}(\mathcal{P}_{e_{\text{out}}}^{\text{ideal}} \circ \mathcal{F}_{1}(\rho_{\text{in}}))$, $\mathcal{E}_{e_{\text{out}}}$ is the completely positive trace nonincreasing map induced by the combined fault operator on the quantum output and is conditioned on e_{out} , and $e_{\mathrm{out}}^{\mathrm{ideal}}$ labels the ideal corrections at the output in the absence of faults inside $\mathcal{S}_{\mathcal{F}_1}$ and depends on e_{in} . Let $\widetilde{\mathcal{E}}_{e_{\text{out}}} = \mathcal{P}_{e_{\text{out}}}^{\dagger}$ $\circ \mathcal{E}_{e_{\text{out}}} \circ \mathcal{P}_{e_{\text{out}}}^{\text{ideal}}$. Then, the output of $\mathcal{S}_{\mathcal{F}_1}$ can be rewritten as $\sum_{e_{\text{out}}} p_{e_{\text{out}}}^{(1)} e_{\text{out}} \otimes \mathcal{P}_{e_{\text{out}}} (\widetilde{\mathcal{E}}_{e_{\text{out}}} \circ \mathcal{F}_1(\rho_{\text{in}}))$. Hence, besides the extra $\tilde{\mathcal{E}}_{e_{min}}$, the noisy output state is of the same form as some ideal noiseless output, with the classical register reflecting the Pauli correction on the quantum state. In particular, this means that we can include errors in both the quantum and classical registers in $\widetilde{\mathcal{E}}_{e_{\mathrm{out}}}$ and interpret it as a simulated fault operation following the simulated \mathcal{F}_1 . The above analysis can now be repeated to subsequent simulations, so that a simulated fault appears after each erroneous simulation. In each term labeled by e_{out} , the simulated evolution on the system and the environment is the intended computation (the sequence $\{\mathcal{F}_i\}$) interspersed by the action of simulated fault operators (whose particular type may depend on e_{in} or e_{out} at the corresponding erroneous simulation).

We pause to discuss the above argument again. The composable simulation has been described in many different ways in the literature, such as feed-forward of measurement outcomes and propagation of by-product Pauli operations. Since the classical knowledge (correct or not) of these byproduct Pauli operations from one simulation step is input to the next, it is worrisome that an error in them will feed forward, inducing highly *correlated* simulated faults in the simulated circuit, even if initial faults in the simulation are uncorrelated. It only takes a shift in one's perspective and inspection of the composability requirement to recognize a simpler interpretation of the error action. In particular, errors in the classical information of the by-product Pauli operations e_{out} are equivalent to unknown Pauli errors in the quantum output $ho_{
m out}$ of the erroneous simulation. The above argument takes full advantage of the equivalence and mathematically redefines e_{out} to indicate the by-product Pauli operation, attributing any "mismatch" with an ideal simulation to noise acting on the quantum output of the simulation of \mathcal{F}_1 alone. From this point of view, the errors in classical information are localized and do not propagate. Being able to localize errors to individual simulated operations achieves a simple and direct mapping from the noise in the simulation to noise in the simulated circuit.

We can now finish the proof of the existence of an accuracy threshold for independent stochastic noise in the graphstate model, using the threshold theorem for standard quantum computation [12–15]: In the circuit-model proof, certain fault paths are "good" and can be proved to give the ideal computation results. "Bad" fault paths form the complement of the "good" ones and have suppressed probability if the physical fault probability is below a certain critical value, the accuracy threshold. Consider the noisy graph-state simulation of a fault-tolerant circuit. In the final output of the faulttolerant circuit simulation, consider each $e_{\rm out}$ term. Our arguments based on composability ensures that the evolution of the quantum state is simply the intended simulated operations, interspersed by the action of faults. Since each fault path in the simulation is mapped to a unique fault path in the simulated circuit due to error localization, good fault paths in a graph-state simulation can be defined as those resulting in good fault paths in the simulated circuit [22]. All other fault paths in the simulation are bad, and their probability will be suppressed below a certain accuracy threshold just as in the circuit model, because a simulated fault appears after some simulated operation only if there is at least one fault in its simulation. Furthermore, the probability of this happening is at most the sum of the fault probabilities of all the elementary steps in the simulation. Then, with reference to Fig. 1, we note that the simulation of each gate in our universal set involves the use of one to two CPHASE gates and zero to two measurements. Therefore the probability of any simulation containing faults is bounded by $p_{sim} \leq 4p$. More specifically, if p_0 is the threshold value of the fault-tolerant architecture used in the circuit model and if $p \le p_0/4$ in the simulation, then $p_{\text{sim}} \leq p_0$ and the final measurement outcome will provide the correct computation results with the desired accuracy. This holds for each $e_{\rm out}$ term in the final state of the simulation, thereby establishing a threshold lower bound of $p_0/4$ for the graph-state model.

In the above, we have related the accuracy threshold in the graph-state model to that in the circuit model by the direct simulation of fault-tolerant architectures designed in the latter. However, we note that, in order to obtain the above threshold bound, we assumed that the fault-tolerant simulated circuit makes use of the same universal set as ours. In general, the same gate sets need not be used in both models, and elementary measurement patterns need to be composed to simulate a single operation in the simulated circuit. In particular, in most studies, CNOT rather than CPHASE is used as the elementary interaction. In this case, the measurement pattern in Fig. 2 for the simulation of CNOT implies the threshold condition $p \le p_0/5$. However, in many cases of interest this lower bound is pessimistic. For example, in faulttolerant designs based on self-dual CSS codes (e.g., [15,23,24]), CPHASE can replace CNOT as an alternative bitwise encoded operation and can also be used in error correction with a small number of additional H gates. Since there is no overhead for simulating single-qubit state preparation, measurement, or the CPHASE in the graph-state model, the thresholds for circuits based on these codes in the circuit and graph-state models will be essentially the same.

We now proceed to prove the existence of an accuracy threshold for the graph-state simulation for local non-Markovian noise. We will make use of our observation of the localization of errors and the threshold results in the circuit model [11,15].

In the local non-Markovian error model, the noise operations still have the form given by Eq. (3) and they act on the system in the same way as in the local Markovian model. However, different noise operations may now act on the same environmental register, and the term acting trivially and nontrivially on the system may not map the environmental register to orthogonal states. Altogether, faults can combine coherently. Furthermore, a fault no longer corresponds to an "event," in the sense that probabilities cannot be assigned. Instead, one imposes that the strength of the fault operator at each location is bounded below a certain value η —i.e., $\|\sum_i P_i \otimes A_i\|_{\sup} \leq \eta$.

To simplify the analysis, we consider the purification of the graph-state simulation, where measurements are replaced by coherent operations by attaching extra ancillary qubits. In our noise model, noisy measurements are modeled as being ideal with noise factored into the preceding noise operations, so that changing our description of the measurements does not affect the analysis. Likewise, the classical 2n-bit string carrying e_{in} can be replaced by a 2n-qubit register in the state $|e_{\rm in}\rangle$ and any adaptive operations inside these equivalent simulations will be controlled by this quantum register. The update of this register to obtain $|e_{out}\rangle$ can also be done coherently by controlling gates from the extra ancillary qubits and also by doing the classical processing reversibly. We emphasize that this alternative coherent description is purely mathematical and is also employed in the circuit-model proofs in Refs. [11,15]. The composable simulation $S_{\mathcal{F}}$ is now a conjugation by a unitary operator $S_{\mathcal{F}}$ taking two inputs $|e_{\rm in}\rangle$ and $P_{e_{\rm in}}|\psi_{\rm in}\rangle$ and some ancillary qubits starting in the fixed state $|+\rangle^{\otimes k}$ [25], so that $\forall |\psi_{\rm in}\rangle$, $\forall |e_{\rm in}\rangle$ it acts as

$$S_{F}(|e_{\rm in}\rangle \otimes P_{e_{\rm in}}|\psi_{\rm in}\rangle \otimes |+\rangle^{\otimes k})$$

$$= \sum_{i} c_{i}|e_{\rm out}\rangle \otimes |i\rangle \otimes |\phi_{i}\rangle \otimes P_{e_{\rm out}}F|\psi_{\rm in}\rangle, \qquad (4)$$

where $\{|\phi_i\rangle\}$ is the orthonormal basis on which measurements are to be performed, $\{|i\rangle\}$ is the computation basis with i labeling the possible measurement outcomes carried by the extra ancillas we have introduced, c_i is the *amplitude* of the ith term, $|e_{\text{out}}\rangle$ is a 2n-qubit state that depends on e_{in} and i, and F is the simulated unitary operator.

Having expressed the fault-tolerant circuit to be simulated as well as the graph-state simulation itself unitarily, a unitary noise operation of the form of Eq. (3) is inserted at every location in the simulation (where locations are specified by the original graph-state simulation before the unitary idealization). The output state is a *linear superposition* of states, each evolved according to a specific set of fault operators and expanded in the eigenbasis of all measured operators (including both measurements part of the graph-state simulation and also measurements originally in the simulated circuit). Fault paths can again be "good" or "bad," defined as in our discussion for independent stochastic noise. For each term evolved by a good fault path, a final quantum state that will provide the correct statistics will be generated, independent of the state of the register $|i\rangle$ coherently carrying the measurement information due to the localization of errors. This is because, for each term in the Pauli expansion of faults acting on $|e_{out}\rangle$, the register $|e_{out}\rangle$ can always be taken to carry the correct Pauli correction by redefining the error acting on $P_{e_{out}}F|\psi_{in}\rangle$ exactly as in our previous discussion. Therefore, for each term in this Pauli expansion, good fault paths in the simulation are mapped to good fault paths in the simulated circuit that produce the ideal computation results, using the threshold theorem in the circuit model. Hence, by linearity, the whole coherent sum of these terms will also produce the ideal computation results.

It remains to bound the *sup norm* of the bad fault paths of the graph-state simulation, which can combine coherently. Following the threshold theorem in the circuit model for local non-Markovian noise [11,15], it suffices to bound the sup norm of the "bad" part of a given simulation (i.e., the sum over terms of the form $\Sigma_i P_i \otimes A_i$ in at least one location within this simulation). But this sup norm is simply bounded by $\eta_{\text{sim}} \leq 4 \eta$, where η is a bound on the sup norm of the fault operator acting on each location in the simulation (by the triangular inequality of the sum norm). Thus $\eta \leq \eta_0/4$ is the threshold condition for the graph-state model if η_0 is the established threshold strength for the circuit model.

IV. CONCLUSION

To conclude, we have invoked the composability property of simulations in the graph-state model to show that faults in the graph-state simulation of any quantum circuit (and of a fault-tolerant circuit, in particular) can be viewed as affecting the simulated operations alone. Thus, the existence of an accuracy threshold for the graph-state model follows from the threshold theorem in the circuit model for the same noise process. As an aside, the same insight can be applied to other measurement-based models of quantum computation and the teleportation of gates. Although other proofs for the existence of an accuracy threshold in the graph-state model have already been reported for a variety of error models [9,10], we believe our analysis provides an alternative, conceptually different and in many respects simpler way of thinking about fault-tolerant circuit simulations.

We note that in optical implementations of graph-state computation [7], gate nondeterminism and photon losses give additional sources of faults not treated in this work. The works in Refs. [7,10,26] show how to control these faults by preparing microclusters. A precise threshold analysis in this setting is pursued elsewhere [27].

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^[1] R. Raussendorf and H. J. Briegel, Phys. Rev. Lett. 86, 5188 (2001).

^[2] M. A. Nielsen, Phys. Lett. A 308, 96 (2003).

^[3] D. W. Leung, Int. J. Quantum Inf. 2, 33 (2004).

^[4] A. M. Childs, D. W. Leung, and M. A. Nielsen, Phys. Rev. A 71, 032318 (2005).

^[5] R. Raussendorf, D. E. Browne, and H. J. Briegel, Phys. Rev. A 68, 022312 (2003).

^[6] E. Knill, R. Laflamme, and G. J. Milburn, Nature (London) 409, 46 (2001).

^[7] M. A. Nielsen, Phys. Rev. Lett. 93, 040503 (2004).

^[8] P. Shor, in *Proceedings of the 37th Annual Symposium on Foundations of Computer Science* (IEEE Computer Society Press, Los Alamitos, CA, 1996), p. 56.

^[9] R. Raussendorf, Ph.D. thesis, Ludwig-Maximillians Universität München, 2003.

^[10] M. A. Nielsen and C. M. Dawson, Phys. Rev. A 71, 042323 (2005).

^[11] B. M. Terhal and G. Burkard, Phys. Rev. A 71, 012336 (2005).

^[12] D. Aharonov and M. Ben-Or, in *Proceedings of the 29th Annual ACM Symposium on the Theory of Computation* (ACM Press, New York, 1998), p. 176; e-print quant-ph/9906129.

^[13] E. Knill, R. Laflamme, and W. Zurek, Proc. R. Soc. London, Ser. A **454**, 365 (1998).

^[14] A. Kitaev, in *Quantum Communication, Computing and Measurement*, edited by O. Hirota, A. S. Holevo, and C. M. Caves (Plenum, New York, 1997).

^[15] P. Aliferis, D. Gottesman, and J. Preskill, Quantum Inf. Com-

- put. **6**(2), 97 (2006).
- [16] F. Verstraete and J. I. Cirac, Phys. Rev. A 70, 060302(R) (2004).
- [17] P. Aliferis and D. W. Leung, Phys. Rev. A 70, 062314 (2004).
- [18] P. Jorrand and S. Perdrix, e-print quant-ph/0404125.
- [19] M. A. Nielsen, e-print quant-ph/0504097.
- [20] R. Jozsa, e-print quant-ph/0508124.
- [21] D. Aharonov, M. Ben-Or, R. Impagliazzo, and N. Nisan, e-print quant-ph/9611028.
- [22] Note that, with their locations fixed, the particular *types* of simulated faults can be correlated since they depend on the classical register carrying $e_{\rm in}$ and $e_{\rm out}$ at each erroneous simulation, which is shared by many simulations. However, Ref.
- [15] shows that the standard threshold theorem holds for this form of noise with memory, which is also present in the circuit-model computation at the encoded levels.
- [23] A. M. Steane, Phys. Rev. A 68, 042322 (2003).
- [24] E. Knill, Nature (London) **434**, 39 (2005).
- [25] These include the qubits of the graph state which are part of the simulation and also extra imaginary ancillas which are used to coherently replace measurements by controlling the measured operators.
- [26] D. E. Browne and T. Rudolph, Phys. Rev. Lett. 95, 010501 (2005).
- [27] C. M. Dawson, H. L. Haselgrove, and M. A. Nielsen, Phys. Rev. Lett. **96**, 020501 (2006).