Fast and High-Fidelity State Preparation and Measurement in Triple-Quantum-Dot Spin Qubits

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We demonstrate rapid high-fidelity state preparation and measurement in exchange-only Si/SiGe triplequantum-dot qubits. Fast measurement integration (980-ns) and initialization (approximately 300-ns) operations are performed with all-electrical baseband control. We emphasize a leakage-sensitive joint initialization and measurement metric, developed in the context of exchange-only qubits but applicable more broadly, and report an infidelity of $2.5 \pm 0.5 \times 10^{-3}$. This result is enabled by a high-valley-splitting heterostructure, initialization at the two- to three-electron charge boundary, and careful assessment and mitigation of T_1 during spin-to-charge conversion. The ultimate fidelity is limited by a number of comparably important factors and we identify clear paths toward further improved fidelity and speed. Along with an observed single-qubit randomized benchmarking error rate of 1.7×10^{-3} , this work demonstrates initialization, control, and measurement of Si/SiGe triple-dot qubits at fidelities and durations that are promising for scalable quantum information processing.

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I. INTRODUCTION

Spins in semiconductor quantum dots are promising foundational elements for scalable quantum information processing [1-6]. This technology leverages the hardearned knowledge and exquisite tools of the semiconductor industry, which routinely mass produces devices with billions of nanoscale structures. In this work, we employ quantum dots that are laterally confined by lithographically patterned gate electrodes and vertically confined within a high-mobility Si/SiGe heterostructure [7,8]. This construction yields suppressed sensitivity to gate-oxide disorder and noise relative to MOS-based devices [9-11]. Additionally, the use of isotopically enriched ²⁸Si permits long spin-coherence times relative to control times [12–14]. Rather than form a qubit from an individual electron spin, one can encode a qubit in the collective spin state of three electrons in three quantum dots-the exchange-only qubit [more specifically, a decoherence-free subsystem (DFS) qubit] [15–18]. These qubits are insensitive to global magnetic noise processes and allow universal control with only baseband electrical pulses, not requiring lasers, large rf circuits, or micromagnets.

High-fidelity single-qubit operations for Si/SiGe exchange-only qubits have been previously demonstrated [19] and here we show high-fidelity state preparation and measurement (SPAM) in a similar device. State preparation and measurement are integral to quantum computing-indeed, they are the first and last steps in any algorithm, be it for NISQ, fault-tolerant computing, networking, or sensing applications. A reasonable ambition for SPAM performance is that it maintain fidelities and durations comparable to those of coherent manipulations, minimizing its likelihood of being a limiting factor in a larger application that may require multiple rounds of initialization and measurement. This establishes concrete goals and forms a natural basis of comparison and using these criteria we show that exchange-only-qubit SPAM can maintain performance parity.

The measurement of spin qubits has a deep history and admits a large space of approaches [2,3,5]. With few exceptions, most schemes involve a spinto-charge conversion (S2C) mechanism followed by a measurement of the resulting charge-occupancy state. Spin-to-charge conversion is most frequently achieved with spin-dependent tunneling (SDT) [20] or with Pauli

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spin blockade (PSB) [21,22]. There are a myriad of quantum-dot charge-detection technologies, foremost the use of quantum point contacts (QPCs) [20,23], singleelectron transistors (SETs) [24-32], or dot-charge sensors (DCSs, also called sensor quantum dots) [33-35], all of which can be measured at dc or rf [36-38]. In the rf case, gate reflectometry can enable measurement at almost any gate electrode and has yielded remarkable results [39–41]. This approach bolsters applicability to dense gate architectures [31,42-45] but requires a bulky LC resonator elsewhere in the system. Here, we employ PSB, which permits higher measurement fidelity and greater flexibility in an applied magnetic field than does SDT, and a DCS, which requires only a structure of similar scale to-and simultaneously fabricated with-the qubits themselves. Furthermore, DCSs allow for greater measurement sensitivity than do QPCs [37].

Our key advancements relative to prior work in PSB-DCS spin-qubit measurements—enabling the SPAM performance reported here—include details of the device, the control and amplifier chains, and our voltage-biasing protocols. We also employ careful metrics for device-relevant characterization of SPAM fidelity and much of this analysis is applicable to measurement modalities other than the PSB-DCS approach. One goal of this work is to provide an updated basis upon which to evaluate the potential of the PSB-DCS approach; whether it is sufficient for highquality SPAM or whether more readout resources will be required for acceptable performance.

There are also several approaches to spin-qubit initialization, including the use of microwave pumping [46], relaxation [20,47], and measurement-based mechanisms [48], but in light of the desirably weak interaction of spins with electromagnetic fields, these approaches are often impractically slow. A faster method available in spin systems is to exchange quantum-dot electrons with those from a cold electron reservoir. This contrasts with initialization in other architectures (e.g., superconducting qubits or trapped-ion qubits), for which it is impossible or inefficient to deterministically replace the particles hosting the qubit.

This paper is organized into the following sections. First, in Sec. II, we describe the measurement approach used for our devices and quantify some aspects of its performance, paying attention to both fidelity and duration. Then, in Sec. III, we do the same for state preparation. We conclude in Sec. IV by discussing our preferred technique for quantifying SPAM performance using a joint metric ($F_{\rm BC}$, with BC standing for benchmarking contrast) and compare the results to other key qubit device measures.

II. MEASUREMENT

In this work, we employ a device fabricated similarly to that of Ref. [19]. This device comprises a linear array of six quantum dots and two DCSs, which share a common



FIG. 1. The signal chain. Square waves are generated at room temperature, attenuated, and sent into the dilution refrigerator, where they act as a bias between source and drain two-dimensional-electron-gas (2DEG) leads. These square waves probe the conductance of the DCS (labeled "M"), which is sensitive to the TQD charge state, and generate a current across a subsequent 20-k Ω sense resistor. This signal is amplified by a two-stage cryogenic HEMT, then demodulated and thresholded at room temperature. Signal-adder circuits (dashed contour) enable fast and accurate baseband control of the plunger (blue, "P" and "M") and barrier (pink, "T" and "X") gate biases. The adder circuit (simplified in this figure) is described in further detail in Appendix A. Note that the source-signal attenuation has a low-impedance output.

source but have independent drain leads. Only half of the device is utilized in this work and a nominally identical device is shown in the electron-microscopy inset of Fig. 1. The overlapping gates are formed from aluminum, 60 nm above a 3-nm-wide, 800-ppm²⁹Si quantum well [13,49,50].

Singlet-triplet measurement via Pauli spin blockade exploits the fermionic nature of electrons, which are constrained to antisymmetric wave functions. At low magnetic fields, the ground state of two electrons in one dot must correspond to an antisymmetric spin singlet; higher-energy triplet states that are spin-symmetric require valley and/or orbital excitations to provide overall antisymmetry. Here, we notate the charge in a set of dots as (n, m); for example, (2,0) represents two electrons in dot 1, zero electrons in dot 2, etc. A spin singlet is indicated by "S," so "S(2,0)" is the ground state of the (2,0) charge state of a double dot. In the (2,0) charge state, valley and/or orbital excitations provide a singlet-triplet energy splitting δ_{ST} that can be exploited for both spin initialization and measurement, with the magnitude of that splitting being critical to the fidelity of both operations.

In a triple-quantum-dot (TQD) DFS, the encoded- $|0\rangle$ state is given by a spin singlet on two dots, "S(1,1)," which in this work refers to the two dots closest to the drain electrode, specifically identified as P1 and P2. The thirddot (P3) charge state is omitted for brevity but is singly occupied by an electron, which is sometimes referred to as the "gauge spin" and is crucial for exchange-only universal control. This electron need not be initialized into any particular spin state [18,19] but reliable exchange coupling requires that it not be in an excited orbital or valley state. The encoded- $|1\rangle$ state of the DFS qubit is a superposition of triplet states $[T_{0,\pm}(1,1)]$ on the P1 and P2 spins, entangled with this third spin. Since PSB measures whether or not the P1 and P2 spins are in a spin singlet or triplet, the preparation and measurement of exchange-only qubits can proceed in the same fashion as in double-quantum-dot singlet-triplet qubits [51,52].

The charge states of the triple quantum dot are probed by electrostatic interaction with a nearby DCS (underneath the "M" gate in the device depicted in Fig. 1). The DCS is biased into the Coulomb-blockade regime, where its conductance is highly sensitive to the potential environment and, accordingly, to the charge occupancy of the TQD. We interrogate this conductance by applying a bias between the source and drain 2DEGs connected to this dot, modulated by a square wave at a frequency of about 2 MHz. The resulting DCS drain current flows through a 20-k Ω sense resistor R_s and the generated voltage signal is amplified by a two-stage cryogenic HEMT [53,54], shown in Fig. 1. The placement of a low-dissipation stage (Avago ATF-38143) at the still plate ($T \approx 800$ mK) before a second stage of amplification (three Avago ATF-3314 HEMTs at $T \approx 4$ K) allows for reduced added noise while respecting the thermal constraints of the dilution refrigerator. A cold finger locates that 800-mK amplifier in spatial proximity (several centimeters) to the device chip, minimizing the (bandwidth-limiting) parasitic capacitance C_p , which we estimate to be roughly 8 pF from the signal bandwidth. The signal then continues to room temperature, where it is digitized and demodulated in software. In that process, a single-shot digitized waveform is multiplied by an inphase square wave at the source-bias carrier frequency, integrated, and then scaled to the inferred average difference of the DCS current between source bias swings. This single-shot current is then optionally thresholded to report a binary measurement result.

We measure the spin state of the device by performing spin-to-charge conversion-detuning the device from an "idle" bias configuration (or "coordinate") of low exchange energy in the (1,1) charge cell toward the (2,0)regime. Because of PSB and the finite singlet-triplet energy splitting, the charge-state level crossing occurs at higher detuning bias for spin triplets than for singlets [see the energy diagram in Fig. 2(a)]. This sets a "measure window" of bias where the spin states differ in their charge character. At an intermediate bias near one half of the two-electron (2e) excited-state splitting, the singlet-triplet degree of freedom and the charge occupancy are maximally correlated, and measurement of charge also yields spin-state information. This is directly evident from spinblockade spectroscopy-histograms of DCS conductance as a function of the detuning bias [Fig. 2(b)] [55].



FIG. 2. The charge stability, energy levels, and spin-blockade spectroscopy. (a) The charge-stability diagram in the bias region relevant for measurement, showing the ground-state charge occupancy (color) as a function of the bias. The horizontal axis reflects the double-quantum-dot- (DQD) bias difference and the vertical axes represent the DQD-bias common mode. (b) The energy-level diagram near the measurement window, shown at zero magnetic field for clarity. The horizontal lines (energy independent of detuning) represent the (1,1) charge state used for exchange and idle operations, while the diagonal lines represent the (2,0) charge state. In this illustration, the relevant singlettriplet splitting δ_{ST} (and, accordingly, the region of spin-to-charge conversion) is set by the 2*e* orbital energy (E_o) rather than the excited valley-state energy (E_v) . (c) Spin-blockade spectroscopy, showing histograms of the DCS current as a function of the detuning between the (2,0) and (1,1) charge states. The fully resolved gap between the singlet and triplet branches is reflective of highly efficient spin-to-charge conversion.

Near the charge-state transition, the bias is ramped in time so as to minimize Landau-Zener processes that might excite the spins into elevated valley-orbit states. This is discussed further in Sec. III. Due to imperfect signal-integrity engineering, with the current design we require a "settling" period of order 1–10 μ s before the source-drain square waves are applied to maintain consistent measurement results. Generally, this delay, rather than integration, dominates the obtainable measurement cadence but we expect to ameliorate this with further signal-chain engineering.

A. SNR

We now probe the quality of that measurement, starting with the signal-to-noise ratio (SNR). The experimental routine begins with preparation of a dephased spin state, which is created by waiting at the idle bias for a time longer than the hyperfine-dominated T_2^* . This is followed by the previously described S2C and charge-state measurement processes and we histogram the resulting measurement current. We use this routine to explore the obtainable spin SNR as a function of the measurement duration and strength (i.e., the source-drain bias amplitude) in Fig. 3. As we increase the integration duration from 980 ns to roughly 40 μ s, the SNR first improves as we better average away white noise and then saturates as it becomes limited by the 1/f charge noise on the DCS. Similarly, the SNR increases quickly as we increase the source-drain bias amplitude but



FIG. 3. Readout histograms and the SNR as a function of time and the bias amplitude. (a) Log-scale histograms of the DCS current (data in black), with Gaussian fits indicating singlet (blue) and triplet (red) readout signals, here with a total integration time of 980 ns. The thresholding of a distribution with this SNR (6.5) provides a bound on $1 - F_{BC} \ge 6 \times 10^{-4}$. (b) Histograms taken at an integration time of 40 μ s and with an additional measurement subtracted, giving an SNR of 15.5. This distribution bounds $1 - F_{BC} \ge 5 \times 10^{-15}$ but other limiting effects (such as relaxation during measurement) are evident. (c) Histogram SNR as a function of both the source-drain bias amplitude and the integration time. The SNR saturates as a function of the increasing integration time due to 1/f noise on the DCS potential. The SNR increases with the bias amplitude and then decreases beyond a critical point.

then decreases after a critical value. This reflects nonlinearity in the DCS conductance; for instance, due to excited states. Quantitative predictions for both components of SNR are discussed in detail in Appendix B.

We can further look at two extremal cases: the shortestduration measurement that maintains a "sufficiently high" SNR and the long-duration measurement with maximized SNR. Figure 3(a) depicts a rapid measurement, integrating for only 980 ns. This time scale compares favorably to spin-relaxation T_1 (> 10 ms, which is discussed further in Sec. II B), quantum-dot dynamical-decoupling time scales [56], and anticipated two-qubit gate durations [18,19]. In that time, we obtain an SNR of 6.5, which limits $1 - F_{BC}$ to 6×10^{-4} (F_{BC} is defined carefully in Sec. IV). When noise on the singlet and triplet readout signals is Gaussian and symmetric, SNR bounds the fidelity by the relation

$$1 - F_{\rm BC} \ge \frac{1}{2} \left[1 - \operatorname{erf}\left(\frac{\mathrm{SNR}}{2\sqrt{2}}\right) \right]. \tag{1}$$

Alternatively, we can saturate the high-SNR limit by integrating for 40 μ s (and subtracting a reference measurement), as shown in Fig. 3(b), which achieves an SNR of 15.5. This SNR yields an exceptionally small theoretical bound on infidelity but other factors limit the fidelity far more strongly. An examination of the histogram of Fig. 3(b) shows evidence of T_1 (points scattered between the two Gaussian distributions) and additional spurious counts, the latter of which may indicate more-pathological noise mechanisms or a probability of populating other charge states. We now go on to discuss those T_1 effects.

B. Relaxation during measurement

As we access larger SNRs, the measurement fidelity quickly becomes limited by relaxation during the measurement. This is explored experimentally and a key result is shown in Fig. 4(a). These data are gathered by preparing a dephased qubit state, transitioning to a trial measurement bias, and then sweeping the detuning of that bias. For each detuning, we apply a typical modulated source-drain bias for a variable amount of time (called the "measurement time" in the figure) but we do not record a measurement signal. After that time, we perform a calibrated measurement at a fixed bias to examine the resulting state and we show that result as a function of both the trial measurement detuning and the duration. The detuning axis has an arbitrary offset but is referenced to the SNR at each point (extracted from spin-blockade spectroscopy), which serves to demarcate the singlet and triplet charge-state transitions.

The locations of the sharp reductions in T_1 at negative detuning are consistent with anticrossings between the S(1,1) and T_(1,1) states, given our 1.5-mT applied magnetic field. This small magnetic field has the effect of improving Clifford error by reducing the impact of



FIG. 4. T_1 and the SNR as a function of the measurement bias and the resulting limits on SPAM fidelity. (a) In color, the resulting singlet probability after trial measurements at swept bias (*x* axis) and duration (left vertical axis). In gray, the fit 1/e times at each bias point. In blue, the SNR as a function of the bias, extrapolated from a fit of spin-blockade spectroscopy (right vertical axis). Note that the *x* axis can be directly compared to that of Fig. 2. (b) The calculation of the limits on F_{BC} that result both from finite SNR and from relaxation during measurement. Combining those sources, we see clearly that the maximum fidelity measurement bias does not coincide with the maximal SNR for this choice of measurement parameters.

electron-nuclear flip-flop terms in the hyperfine interaction [57]. We can estimate the bound on the measurement fidelity imposed by a finite SNR and T_1 , as shown in Fig. 4(b). Relaxation during measurement yields a bound

$$1 - F_{\rm BC} \ge \frac{1}{2} \left[1 - \exp\left(-\frac{T_{\rm measurement}}{T_1}\right) \right].$$
(2)

Combining those two effects, we can locate the bias for optimal contrast—which, notably, is not the bias at which the SNR is maximized. Importantly, we see T_1 decay times of over 20 ms within biases that yield a high SNR, which compares favorably with our achievable measurement rates of microsecond order. We additionally perform the same experimental sequence and analysis but with the amplitude of the source-drain bias as the independent variable and discuss that result in Appendix C. That measurement shows that a large source-drain bias can induce T_1 and, accordingly, a compromise must be made in choosing that amplitude.

The rich T_1 behavior displayed in Fig. 4(a) involves a complex interplay of microscopic semiconductor physics. One extrinsic relaxation pathway is via cotunneling with the electron bath but this can typically be exponentially suppressed by increasing the relevant tunnel barrier. The intrinsic decay channels within the device require both charge-transition and spin-flip mechanisms. Charge decay proceeds via electron-phonon coupling or electromagnetic interactions with the gate, bath, or charge-noise sources, which can have different densities of states and hence differing dependence on detuning. Microscopically, singlet and triplet states can couple to each other via nuclear hyperfine interaction with ²⁹Si and ⁷³Ge atoms, as well as the spin-orbit interaction present in Si/SiGe quantum wells. T_1 "hot spots" can appear at detuning biases where excited singlet and triplet states anticross, leading to strong spin mixing and rapid charge decay, as observed experimentally in Fig. 4(a). In Appendix D, we address how these features are expected to connect with the "measurewindow" energy spectrum.

III. STATE PREPARATION

We initialize the spin state by biasing fully into the (2,0) charge configuration, maximizing the energy splitting between the triplet states and the ground-state singlet. At thermal equilibrium, we expect to preferentially populate the encoded- $|0\rangle$ (singlet) state. Waiting for relaxation to equilibrium is impractical, as singlet-triplet decay rates $(1/T_1)$ can be quite long due to the necessity for spin relaxation [58,59]. Instead, as previously mentioned, we speed up the process by exchanging electrons with a cold reservoir-here, a 2DEG that also serves as the drain for the DCS current. Typically, initialization is achieved by biasing the P1 dot to the vicinity of the (2,0)-(1,0) charge boundary where the chemical potential of the (1,0) charge state lies between that of the S(2,0) and T(2,0) states [as shown empirically in Fig. 5(a) and schematically in Fig. 5(b)]. This enables bath-dot tunneling to more rapidly equilibrate or "flush" to the ground state. We sweep the duration of this flush period and choose a duration at which the triplet population stabilizes. In this work, we exploit the (2,0)-(3,0) charge boundary [rather than (1,0)-(2,0)], seen on the right-hand side of Fig. 5(b), which to our knowledge has not been previously discussed for this purpose.

Initialization is completed by transitioning back to the idle bias via a ramped trajectory. First, we change the bias quickly from the initialization charge boundary to an "entry" point just outside the (2,0)-(1,1) boundary. Then we ramp slowly—with ramping times of order 10–100 ns—into and through the PSB coordinate, ending at a second entry point just inside the (1,1) charge cell. This is designed so as to move through the anticrossing without triggering a Landau-Zener transition. Then, we jump



FIG. 5. A sweep of initialization bias and schematic of the initialization process. (a) This signal (color) is the difference of spin measurements between preparations of an initialized state and of a dephased state. When initialization is effective, these measurement results are not identical. Sweeping the plunger-gate initialization bias of the P1 and P2 dots, we see two bands at the (1,0)-(2,0) and (2,0)-(3,0) charge-cell boundaries, which are both usable regions for initialization. The light gray arrow indicates the bias region used for S2C during measurement. (b) Near the (2,0)-(1,0) charge-state transition, the tunneling rate between the P1 dot and the 2DEG bath is greatly accelerated, leading to rapid thermalization and depopulation of the (2,0) excited states. The detuning span of this region is set by the 2e singlet-triplet splitting. The rate can be further increased by dynamically depressing the dot-bath barrier (dashed line), physically realized as the "T" gate visible in Fig. 1.

quickly from that point to idle, minimizing the time spent at low-exchange regions where magnetic dephasing is more rapid. To return from idle to measurement, we repeat the latter half of this process in reverse. The optimal choices of the ramp times and the entry coordinates balance many factors and are a subject for future study.

A. Initialization fidelity

After tunneling to and from the bath, at equilibrium we expect to populate the ground-state singlet state with a probability given by the partition function

$$P_{|0\rangle}^{-1} \approx Z_{2e} \approx 1 + 3e^{-E_o\beta} + 4e^{-E_v\beta} \approx \left(5 \times 10^{-4}\right)^{-1},$$
(3)

where $\beta = 1/(k_B T_e)$ (here, the effective electron temperature $T_e \approx 220$ mK, measured from the tunneling linewidth as a function of the mixing-chamber temperature), E_v is the excited valley-state energy in the P1 dot ($\approx 250 \ \mu eV$, measured with detuning-axis pulsed spectroscopy [60]), E_o is the 2*e* orbital excited-state energy (approximately 160 μeV , measured with spin-blockade spectroscopy), and we neglect higher excited valley-orbital states (assuming that their contributions are negligible at sufficiently low temperatures). The factors of 3 and 4 come from the respective degeneracies of the excited valley and orbital states. Zeeman splittings of the polarized triplet states are also neglected in this equation, as they are small at the low and moderate magnetic fields at which we typically operate exchange-only qubits (here, 1.5 mT). We also neglect the third electron, which we estimate to be separable from the P1 and P2 spin initialization and which may eventually reach an equilibrium set by its own (one-electron, or 1e) excited-state energy. It is possible to end in the wrong charge state during this process so that, for example, the partition function in Eq. (3) can be extended to account for (1,0) occupation as well (if initializing at that charge boundary), though empirically this is not a common outcome.

B. Initialization speed

This speed of this initialization process benefits from the fact that tunneling between the P1 dot and the bath depends exponentially on the height of the insulating tunnel barrier. Pulsing the voltage bias on the relevant barrier gate strongly and quickly modulates that potential. In practice, we observe a speed limit enforced by two main mechanisms: orbital suppression and waveform stability. First, depressing the barrier height can distort and desymmetrize the quantum-dot confining potential, which decreases the orbital excited-state energy [61] and limits the ultimate obtainable fidelity [Eq. (3)]. In principle, this effect could be countered by shaping the potential further with appropriate pulses on other nearby gate electrodes. Second, the stability of the bias waveform strongly influences this process. Ideally, when sweeping the initialization bias and duration (Fig. 6), we expect the resulting population to be strongly bias dependent and to change monotonically with time. In contrast, we believe that the observed nonmonotonic signal is a signature of bias drift. This effect limits us to a minimum duration of roughly 300 ns to reach the asymptotic population; however, this limitation should be reduced with improved engineering for signal integrity. We note also that a wider initialization bias window (resulting from larger excited-state splittings) should also reduce the sensitivity to this drift.

We find that using the alternative (2,0)-(3,0) "initialization window" leads to the same asymptotic singlet population but often more than an order of magnitude more quickly. Several factors likely contribute to this operational improvement; for one, the increased dot-plunger (P) gate voltage required at this boundary decreases the effective tunnel barrier. Second, the increased bias-space width of this initialization window (evident in Fig. 5) likely mitigates the effects of waveform drift. Finally, the spin and charge character of the multielectron states also play an important role. For instance, it can be shown from the relevant Clebsch-Gordan coefficients that the tunneling transition rate between an *N*-electron state of total spin *S_i*



FIG. 6. A sweep of initialization bias and schematic of the initialization process. (a) The charge-stability diagram in the bias region relevant for initialization, showing the ground-state charge occupancy (color) as a function of bias. In this portion of the work, we initialize at the (2,0)-(3,0) boundary. (b) The sweeping of the P1 plunger gate bias across the (2,0)-(3,0)boundary and the sweeping time. This population is the thresholded result of a spin-state measurement immediately after bath exchange following preparation of a dephased state. The populations are not monotonic in duration, which is indicative of the bias waveforms settling in time. While our high-speed adder circuit (Appendix A) attempts to provide a flat response, not all nonidealities are accounted for. This settling limits the minimum initialization time in this case to roughly 300 ns to reach an asymptotic population of 6×10^{-4} . P1 biases differ from Fig. 5(a) because additional (pulsed) tunnel-barrier bias is introduced and the "T" gate has a nontrivial cross-capacitance to the first dot potential. This result is degraded from the Boltzmann distribution prediction for initialization fidelity alone (Eq. (3), approximately 5×10^{-4}), due to measurement and mapping errors.

and an N + 1 state with spin S_i is weighted by a factor [62]

$$\gamma_{ij} = \frac{S_i + 1}{2S_i + 1} \delta_{S_i + 1/2, S_j} + \frac{S_i}{2S_i + 1} \delta_{S_i - 1/2, S_j}.$$
 (4)

As a result, while both the one- and three-electron ground states have total spin S = 1/2 at typical magnetic fields, the single-electron state has a three times larger tunneling rate to the triplets compared to singlets where both are energetically available, whereas the opposite is true for the three-electron ground state, which has a three times larger tunneling rate for singlets compared to triplets. Details of the three-electron excitation spectrum may also play a role and depend sensitively on the confining electrostatics as well as valley mixing; quantification of the effects of these states on the initialization process is an interesting area for further study.

IV. JOINT SPAM FIDELITY AND BUDGET

A. Quantifying SPAM performance

In this section, we move on from the mechanics of individual SPAM operations to focus on the quantification of their performance. SNR of the measurement and T_1 both contribute to SPAM infidelity and are useful diagnostic quantities but tell only part of the story. We can probe initialization fidelity by performing a measurement directly after initialization (as in Fig. 6); however, we do not consider this quantity to be a reliable summary metric of initialization quality, nor of integrated SPAM quality. Among other concerns, it can be gamed by biased measurement error, it does not include any errors in "mapping" when biasing to and from the idle point, and it does not guarantee what fraction of the population will exhibit proper qubit evolution. The last criterion is generally relevant due to the possibility of leakage outside of the qubit space, the effects of which vary widely between physical-qubit implementations. One plausible alternative metric is the ultimate contrast that can be observed in exchange oscillations (Appendix E) but this is also degraded by charge noise and other decoherence mechanisms, the infidelity of which is better associated with coherent manipulation.

Instead, we emphasize an integrated preparation and measurement metric (first presented in Ref. [19] and reproduced mathematically in Appendix F) that avoids the previously mentioned flaws while providing several other benefits. This measure, dubbed F_{BC} for benchmarking contrast, probes the fundamental concern of imperfect preparation and measurement: what fraction of the time do we obtain the correct result after an arbitrary qubit evolution, independent of the fidelity of that evolution? $F_{\rm BC}$ is derived from "blind" randomized benchmarking, a method originally designed to detect leakage out of the computational space during coherent manipulation of exchangeonly qubits. In this procedure, an ordinary randomized benchmarking experiment (with sequences that compile to the identity) is executed along with a second benchmarking experiment, the sequences of which are instead engineered to compile to a population-inverting gate. To assess SPAM performance, we evaluate the contrast of these fit curves at zero Clifford operations, which attempts to subtract any contrast loss due to imperfect qubit rotations.

 $F_{\rm BC}$ transposes the typical objective of randomized benchmarking—measuring the Clifford fidelity independently of the SPAM performance. This measure differs from assignment fidelity [63], which might be quite coarsely approximated by the same curves evaluated at 0.5 Clifford operations. Importantly for exchange-only qubits, we find $F_{\rm BC}$ to be more robust than assignment fidelity in the presence of leakage, though that import depends on the qubit physical implementation. $F_{\rm BC}$ also implicitly disallows the use of different processes for preparation or measurement of $|0\rangle$ and $|1\rangle$ that may boost assignment



FIG. 7. The measurement of joint initialization and measurement fidelity from benchmarking contrast. The return-to-singlet (red) and return-to-triplet (blue) data points (with empirical error bars) are from blind randomized benchmarking. The solid curves are the corresponding error-bar-weighted fits to the functions given in Eq. (F1). The contrast of these curves, extrapolated to zero Clifford operations, indicates $1 - F_{BC} = 2.5(5) \times 10^{-3}$, with a per-Clifford qubit error of $1.7(2) \times 10^{-3}$ and a per-Clifford leakage rate of $3.3(9) \times 10^{-4}$. The upper and lower insets showing enlargements of the first ten points are provided only to emphasize full contrast at the shortest sequences; our extrapolation utilizes the full data set.

fidelity at the expense of operational utility. We note that this metric is essentially universal and allows fair comparison between diverse physical implementations of qubits. One final desirable feature of $F_{\rm BC}$ as a "summary" metric is its resilience to manipulation or being increased at the expense of the fidelity of other qubit operations. Any tradeoffs between SPAM and single-qubit errors are readily apparent in this extraction.

For exchange-only qubits, single-qubit Clifford operations are compiled from exchange pulses alternating between the P1-P2 and P2-P3 electron pairs (with an average of 2.7 pulses per Clifford operation). The construction is described in Ref. [19] and its mathematical basis can be found in Ref. [64]. Blind randomized benchmarking for the device described in this work is shown in Fig. 7(a). These data indicate a per-Clifford error of 1.7×10^{-3} and F_{BC} of $2.5(5) \times 10^{-3}$. This latter estimate is highly consistent with the visible contrast of exchange oscillations (Appendix E).

B. Compiled budget and missing error

To understand this SPAM performance, we can consider three components: initialization, measurement, and mapping. The initialization quality is foremost bound by the magnitude of the 2e and 1e excited-state energies relative to the effective electron temperature T_e [Eq. (3)]. The measurement performance can be determined from a trio of "active" factors: the signal amplitude, the noise amplitude, and the relaxation during measurement—the latter of which is discussed in Sec. II B and the former two in Appendix B. These three factors are tightly interrelated and

TABLE I. The budget for F_{BC} .

Factor	Error contribution
Boltzmann distribution	5×10^{-4}
SNR (980-ns integration)	6×10^{-4}
T_1 (measurement and settling)	2×10^{-4}
(1,1) mapping errors	2×10^{-4}
Total	1.5×10^{-3}

sometimes in tension, so that overall optimization is often a balancing act between them. Mapping errors, which occur in the transitions from initialization to the idle bias and from the idle bias to the measurement bias, can reasonably be grouped with one of the above or considered as a separate category but should be included in any operationally representative integrated SPAM metric. These errors are further discussed in Appendix G. Here, we consider and assemble the effects of those components into a budget for $F_{\rm BC}$, shown in Table I.

This budget leaves roughly 1×10^{-3} infidelity unexplained relative to the observed $1 - F_{BC} = 2.5 \times 10^{-3}$. We hypothesize that the missing fidelity is attributable to probabilistically initializing with the gauge electron in an excited state. During these events, the exchange rate on one control axis will exhibit a different voltage dependence [J(V)] than occurs when the gauge electron is in its ground state. As a result, Clifford operations composed of pulses calibrated for the J(V) mapping of the ground state quickly scramble the spin state when that electron is excited, resulting in an apparent SPAM error. This effect, sometimes described as "dual exchange frequencies," has been reported in other works [9], where the Fourier transform of time-domain exchange oscillations clearly shows two peaks. For the present device under study, we are unable to observe a clear second peak in the spectra of exchange evolution on either exchange axis; however, it is unclear whether this experiment has sufficient resolution to differentiate a second frequency at the 1×10^{-3} level. The development of a better method to quantify such population is an important area for future work.

Though the energy of the first excited state of the gauge electrons is not measured, as a 1*e* property we can reasonably associate its first excited state with the valley degree of freedom. Assuming equilibrium statistics in a Boltzmann distribution, the missing infidelity implies an excited valley-state energy of roughly 130 μ eV, a value that is consistent with the measured valley-splitting distribution for this heterostructure [60]. We note that this population will not be evident in initialization metrics that do not require fairly measuring qubit evolution, pointing again to the importance of the use of blind randomized benchmarking as a fair SPAM fidelity metric. Furthermore, this emphasizes that $F_{\rm BC}$ measures a three-electron property for TQD qubits.

V. CONCLUSION

By our preferred conservative metric, we demonstrate preparation and measurement in exchange-only qubits with $1 - F_{BC}=2.5(5)\times10^{-3}$. These are obtained using only baseband control electronics, using a measurement integration time of 980 ns, an initialization duration of roughly 300 ns, and a charge-transition ramp time of 100 ns. We present a budget that explains approximately 60% of the observed error and we hypothesize that the remaining error is due to thermal excited states of the gauge electron.

Priorities for improving initialization performance are obtaining larger excited-state energies and suppressing the effective electron temperature, which should yield both improved singlet and gauge electron preparation. Initialization may be accelerated by achieving more stable voltage waveforms. Related signal-integrity engineering is also key to reducing the "settling" period, which presently dominates the possible measurement cadence. Other paths to improved PSB-DCS measurement performance include lower-noise amplification with lower parasitic capacitances, further exploration of "latching" [65-67] and "avalanche" [68] approaches, and improved T_1 decay rates. T_1 effects can likely be reduced by further decreasing the charge and magnetic noise, which are already desiderata for improved coherent manipulation. This may also require using smaller source-drain biases, further emphasizing the need for improvements in signal and noise amplitudes.

Importantly, we conclude that high-fidelity and rapid SPAM (maintaining performance parity with coherent manipulation) is achievable in spin qubits using only simple electronics and components with high flexibility in temperature and magnetic field. Routes to even further improvement without the addition of other components are clear.

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APPENDIX A: HIGH-SPEED ADDER CIRCUIT

Sending high-bandwidth baseband signals into a dilution refrigerator is a nontrivial problem. To protect sensitive quantum devices from thermal Johnson noise and instrumentation noise, microwave or all-ac signals are generally attenuated heavily at intermediate temperature stages within a refrigerator. However, it is not desirable to send large dc signals through attenuators, as they will constantly dissipate power inside of the cryostat. Fortunately, we can instead aggressively low-pass filter dc signals. In our adder circuit, we use both of these



FIG. 8. The room-temperature high-speed adder circuit. This circuit allows high-bandwidth but baseband control, combining inputs both from National Instruments AWGs and custom low-noise DACs. Together with the fridge components shown in Fig. 1 (attenuation, bias tee, and low-pass filter), this forms the full signal chain, providing the benefits discussed in the text. R_{fine} and R_{coarse} are highly stable potentiometers that allow for balancing the attenuation of the high-frequency and low-frequency signal paths.

techniques—sending the ac and dc components of a signal in separately, which are then combined at a basetemperature bias tee (see Fig. 8).

An added complication is that the baseband pulses [generated from National Instruments NI5451 arbitrary waveform generators (AWGs)] contain both ac and dc components. We get around this by splitting the AWG signal. The ac component travels directly through the (attenuated) ac lines. The dc component is attenuated at room temperature via a simple resistive divider, then combined with the true dc signal from a low-noise DAC, before traveling through the (filtered) dc lines. It is critical that the attenuation of both of these paths is matched or the mismatch will result in long-time-scale waveform distortion. This is particularly evident in the shift of measurement signals during long experimental sequences, such as randomized benchmarking. Treated improperly, this shift of signal with time can be mistaken as resulting from some other dot physics. We test this using the stabilization of a dot loading-line bias after a step-edge pulse and the attenuation of the resistive divider is fine tuned with a highly stable Vishay potentiometer.

APPENDIX B: CONTRIBUTIONS TO SNR

1. Noise amplitude

The budget for measurement noise is dominated by three effectively white-noise sources of comparable magnitude (input-referred HEMT noise, sense-resistor Johnson noise, and DCS-current shot noise) and by the noise on the DCS potential. In our measurement-chain configuration, the former three sources each generate a noise spectral density of roughly 200–300 pV/ $\sqrt{\text{Hz}}$. The quantum-dot charge-noise spectrum is typically of the form A^2/f , with an effective gate-referred voltage spectral density $A \approx 5 \,\mu\text{V}/\sqrt{\text{Hz}}$ at 1 Hz.

The effect of this noise source on readout depends on how the DCS is operated. In particular, two extreme choices are to bias the DCS gate voltage to maximize either the transconductance G_m (the peak of the derivative of conductance with respect to the measure dot gate voltage) or the conductance (the conventional Coulombblockade peak point). The former maximizes the signal when the magnitude of the charge-dependent potential shift at the DCS (Appendix B2) is small compared to the width of the Coulomb peak and we can approximate the effect of the DCS-potential noise as a linear shift of the conductance. The latter maximizes the signal when the magnitude of the charge-dependent potential shift is much larger than the width of the Coulomb peak and the statedependent conductance difference is maximal. In this case, we are insensitive to DCS potential noise to first order. From these two extrema, it is evident that the DCS bias that maximizes the signal may not be the correct bias to maximize the SNR, particularly in the 1/f -dominated long-integration regime.

To predict the ultimate expected noise amplitude, we integrate the white-noise sources with the filter function appropriate for our square-wave modulated measurement tone, yielding a current noise of σ_{SD}^2 . We account for the charge noise in a conservative analysis, taking the small-shift approximation such that, to first order, the output measured current is modulated by $G_m d(t)$, where d(t) is the (noisy) potential shift. If we add that to the previous analysis, assume that d(t) has a power-spectral density given by A^2/f , and account for our averaging scheme when incorporating an (approximately instantaneously) subsequent reference measurement, the total histogram variance can be expressed as

$$\sigma_I^2 = 2\sigma_{SD}^2 + 16\ln 2G_m^2 A^2.$$
(B1)

This sets an upper bound on the effects of DCS charge noise on measurement histograms. For relevant measurement parameters, we predict $\sigma_I \approx 5$ pA at long duration, which is in line with observed values [e.g., Fig. 3(b), though note that in that figure the current values for referenced measurements have been divided by a factor of 4 for inconsequential implementation details].

As subtracting a reference measurement increases the white-noise contribution, it is only preferable when the noise is dominated by 1/f or other slow processes. This can occur either because the 1/f contribution is generally large or because one seeks the highest possible SNR and is willing to average long enough to reduce the white-noise contribution. Even in such a regime, performing a

reference measurement for every experimental query is unlikely to be optimal.

2. Signal amplitude

We can estimate the magnitude of the HEMT-inputreferred voltage signal as a product of five factors:

- (1) The spin-to-charge conversion efficiency
- (2) The inter-charge-state DCS potential shift
- (3) The DCS transconductance G_m
- (4) The maximum applicable source-drain bias and
- (5) The value of the sense resistor

The spin-to-charge conversion efficiency describes how effectively the spin states correspond to different charge states at the measurement bias and approaches unity when the 2e excited-state splitting is larger than the singlet- or triplet-selective charge transition widths. That width can be reduced by decreasing the effective electron temperature and tunnel coupling but it is also sensitive to charge noise (see the Supplemental Material of Ref. [55]). In practice, we find that an excited-state splitting of roughly 100 μ eV is sufficient to push this term near unity at a typical operating electron temperature and tunnel coupling. The magnitude of the potential shift is a straightforward calculation from the device geometry and Coulomb's law, accounting for the effects of image charges due to the electrostatic environment. The transconductance around a Coulomb peak is a well-studied question, depending on the tunneling rates of the DCS tunnel barriers and the effective electron temperature [69,70].

The maximum applicable source bias is limited by two factors. First (as noted in Sec. II A), nonlinearity in the DCS causes the conductance to decrease after a critical value. Second, we also observe that at a sufficiently large source-drain bias, the spin relaxation lifetime begins to decrease (this is discussed in Appendix C). The voltage signal can be linearly increased with high-resistance sense resistors but at the expense of increased Johnson and shot noise. Additionally, due to parasitic capacitance, the accessible measurement frequency decreases quickly, limiting the ability to mitigate low-frequency noise.

APPENDIX C: T_1 VS SOURCE-DRAIN BIAS AMPLITUDE

We also explore the effect of measurement-bias-driven T_1 decay using the experiment discussed in Sec. II B. Figure 9 shows that T_1 decreases strongly when we bias beyond roughly 75 μ V, a value suggestively close in correspondence to the S-T splitting at the middle of the measurement window, e.g., equal to the gap at that detuning between the ground (2,0) state and the lowest (1,1) excited triplet state. These observations show similarities to other reports in the literature [71,72]. The onset



FIG. 9. T_1 as a function of the applied measurement bias. The decay rate increases after a certain bias amplitude of energy comparable to the singlet-triplet splitting. We also provide a cubic guide to the eye, which appears to track the asymptotic behavior.

value and the nonlinear scaling with bias are potentially indicative of enhanced relaxation due to phonons generated by the enhanced dissipation of the DCS. Further work is needed to understand the microscopic underpinnings of this effect.

APPENDIX D: MEASURE-WINDOW ENERGY SPECTRUM

The level structure in the vicinity of a (1,1)-(2,0) anticrossing is quite complex, especially in Si/SiGe quantum dots, where the valley degree of freedom introduces additional excitations. The valley splitting E_v is usually tens to hundreds of microelectronvolts, while the single-electron orbital energies tend to be 1 meV and higher. However, the 2e orbital energy E_o can be considerably smaller (on the order of tens to hundreds of microelectronvolts) than the valley splitting due to the interplay of the Coulomb interaction and the spatial asymmetry. A simplified model spectrum for the device in this paper is depicted in Fig. 2(a). The first excited 2e orbital state in a single dot is a spin triplet and hence is threefold degenerate in the absence of a magnetic field. By contrast, a 2e valley excitation can be either singlet or triplet and is nearly fourfold degenerate because of the negligible intervalley exchange interaction [73]. These degeneracies alter the occupation of excited states and hence the initialization fidelity, as reflected in the partition function of Eq. (3). In practice, the level structure will be further complicated by the presence of other doubly excited states, valley-spin-orbit mixing, and Zeeman splitting of polarized triplets due to external magnetic fields.

This energy spectrum also provides some hints about the physics of T_1 relaxation within the measurement window, which requires both spin and charge mixing of the singlet and triplet states. Thus, for example, the hyperfine interaction can couple the $T_x(1,1)$ states to the nearly degenerate

and parallel excited S(1,1) state, which can then decay via phonon or photon emission to the ground S(2,0) state. The spin-orbit interaction likewise introduces magnetic gradients mixing T(1,1) and S(1,1) as well as spin-flip tunneling, which couples $T_x(1,1)$ to S(2,0) directly, both of which are sensitive to the magnetic field orientation [74]. Similarly, the reverse processes are also possible. These terms will create additional excited singlet-triplet anticrossings within the measure window when the polarized triplet states are split by external magnetic fields (not pictured), which we believe are connected to the T_1 "hot spots" visible in Fig. 4. These features are qualitatively reproduced in calculations directly diagonalizing the (1,1)-(2,0)Hamiltonian including spin-mixing terms, though quantitative comparison is currently inhibited because several key parameters (particularly for the spin-orbit coupling) are not characterized. In such a model, the relaxation rate may be sensitive in both magnitude and hot-spot location to magnetic field, tunnel coupling, and valley mixing, consistent with the variable, tuneup- and device-dependent T_1 features that we observe experimentally. Accordingly, some fraction of the T_1 performance degradation may be further improved at other applied magnetic field values, a degree of freedom that is not explored in this work.

APPENDIX E: EXCHANGE OSCILLATION CONTRAST

The contrast of exchange oscillations is another plausible joint SPAM performance metric in our system. We



FIG. 10. The exchange oscillation contrast. Exchange oscillations on the *n* axis, plotting thresholded singlet population versus the magnitude of the throw on the symmetric axis, referenced to the voltage change on the "X2" gate. The *n* axis should rotate the logical zero state from 100% singlet to 25% singlet. The inset is an enlargement of the same data near the first near-25% trough. Interpreted directly, half of the difference between this contrast and 0.75 would indicate a SPAM infidelity of 2.8×10^{-3} .

prefer F_{BC} from benchmarking for the reasons noted in the text but in this case the resulting estimates are quite similar (see Fig. 10). In other physical architectures, this approach might be compared to the contrast of Rabi oscillations.

APPENDIX F: DEFINITION OF F_{BC}

This integrated preparation and measurement metric (discussed in Sec. IV) has first been presented in Ref. [19]). F_{BC} is derived from "blind" randomized benchmarking, a procedure in which an ordinary randomized benchmarking experiment (with sequences that compile to the identity) is executed along with a second benchmarking experiment, the sequences of which are instead engineered to compile to a population-inverting gate. We then fit the results of these two experiments to the form

$$y_0 = A + B(1-p)^N + C(1-q)^N$$
 (F1a)

$$y_1 = A - B(1-p)^N + C(1-q)^N$$
. (F1b)

To assess the SPAM performance, we evaluate the contrast of these fit curves at zero Clifford operations, which attempts to subtract any contrast loss due to imperfect qubit rotations. The SPAM infidelity is given by half of the missing contrast,

$$1 - F_{\rm BC} := 0.5 - B.$$
 (F2)

APPENDIX G: MAPPING ERRORS

During DFS operation, the bias configurations used for initialization and measurement are different than the idle bias used in between coherent manipulations. Transitioning between these operating points can give rise to "mapping" errors. Similar mapping steps are not uncommon in other architectures, e.g., flux-tunable superconducting qubits [75] and some trapped-ion qubit encodings [76]. Within the architecture of this work, alternate mapping operations (e.g., "latching" [65-67] and "avalanche" [68] mechanisms) can provide more favorable SNR and/or relaxation rates but potentially at added cost in mapping error. In this work, mapping errors are principally caused by dephasing due to magnetic noise and with Landau-Zener processes and to a lesser extent relaxation and the risk of transiting the boundaries of other charge cells. We are also at risk of state degradation during the settling period required before source-drain biases are applied, which we believe is constrained by signal-integrity limitations. We budget some of this error by comparing two experiments: one where we bias directly from initialization to measurement and a second where we bias from initialization to the idle bias before measurement. These two measurements yield triplet fractions of 6×10^{-4} and 8×10^{-4} , respectively, with the difference reflecting the mapping error due to that additional bias trajectory.

While the mapping errors could be reduced by engineering devices with, e.g., a longer singlet-triplet dephasing time T_2^* or better signal integrity, they can also be limited somewhat by careful pulse design. Landau-Zener transitions occur when gate voltages jump between SPAM and idle configurations too quickly, such that the ground state cannot adiabatically move between the (2,0) and (1,1)charge configurations. Such transitions are most likely when the charge states are near an anticrossing, as they are during initialization and measurement, and as such can be suppressed by ramping slowly to and from the SPAM configurations. On the other hand, magnetic dephasing is in some ways a symptom of ramping too slowly. Near the charge boundary, the same large singlet-triplet energy splitting that enables SPAM suppresses dephasing between the logical states. Close to idle, the energy splitting is much smaller and dephasing is more significant. Consequently, long-duration ramps deep within the (1,1) charge cell unnecessarily accumulate dephasing error. We employ a two-step approach to ramping (as noted in Sec. III) to minimize both types of error.

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